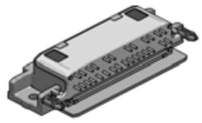
V23990-P707-F40-PM
preliminary datasheet

flow90PACK 1 2nd gen	1200V/8A

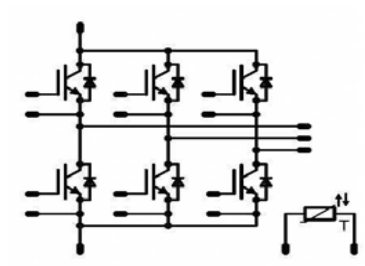
Features
- Trench Fieldstop IGBT4 Technology
- Supports designs with 90° mounting angle between heatsink and PCB
- Clip-in PCB mounting
- Clip or screw hetasink mounting

flow90PACK 1 2nd gen



Target Applications
- Motor Drives

Schematic



Types
- V23990-P707-F40-PM

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition		Value	Unit
Inverter IGBT					
Collector-emitter break down voltage	V_{CE}			1200	V
DC collector current	I_C	$T_j=T_j$max	T_h=80°C T_c=80°C	14 17	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_j$max		24	A
Turn off safe operating area		VCE ≤ 1200V, Tj ≤ Top max		24	A
Power dissipation per IGBT	P_{tot}	$T_j=T_j$max	T_h=80°C T_c=80°C	50 76	W
Gate-emitter peak voltage	V_{GE}			±20	V
Short circuit ratings	t_{SC} V_{CC}	T_j≤150°C V_{GE}=15V		10 800	µs V
Maximum Junction Temperature	$T_j$max			175	°C
Inverter FWD					
Peak Repetitive Reverse Voltage	V_{RRM}	T_j=25°C		1200	V
DC forward current	I_F	$T_j=T_j$max	T_h=80°C T_c=80°C	13 16	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j$max		20	A
Power dissipation per Diode	P_{tot}	$T_j=T_j$max	T_h=80°C T_c=80°C	40 61	W
Maximum Junction Temperature	$T_j$max			175	°C

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition		Value	Unit
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{op}			-40…+(Tjmax - 25)	°C
Insulation Properties					
Insulation voltage	V_{is}	t=2s	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	

Characteristic Values

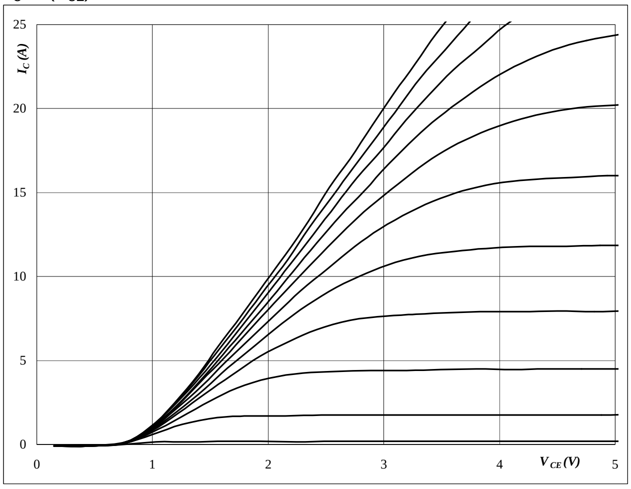
Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_C [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	
Inverter IGBT										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0003	Tj=25°C Tj=150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		8	Tj=25°C Tj=150°C	1,3	1,88 2,16	2,3	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	1200		Tj=25°C Tj=150°C			0,001	mA
Gate-emitter leakage current	I_{GES}		20	0		Tj=25°C Tj=150°C			120	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	Rgoff=64 Ω Rgon=64 Ω	±15	600	8	Tj=25°C Tj=150°C		106 104		ns
Rise time	t_r					Tj=25°C Tj=150°C		25 28		
Turn-off delay time	$t_{d(off)}$					Tj=25°C Tj=150°C		227 297		
Fall time	t_f					Tj=25°C Tj=150°C		68 140		
Turn-on energy loss per pulse	E_{on}					Tj=25°C Tj=150°C		0,66 1,01		mWs
Turn-off energy loss per pulse	E_{off}					Tj=25°C Tj=150°C		0,48 0,81		
Input capacitance	C_{ies}	f=1MHz	0	25		Tj=25°C		490		pF
Output capacitance	C_{oss}							50		
Reverse transfer capacitance	C_{rss}							30		
Gate charge	Q_{Gate}		±15	960	8	Tj=25°C		45		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,90		K/W
Inverter FWD										
Diode forward voltage	V_F				10	Tj=25°C Tj=150°C	1	1,77 1,69	2,2	V
Peak reverse recovery current	I_{RRM}	Rgon=64 Ω	±15	600	8	Tj=25°C Tj=150°C		8 9		A
Reverse recovery time	t_{rr}					Tj=25°C Tj=150°C		271 448		ns
Reverse recovered charge	Q_{rr}					Tj=25°C Tj=150°C		0,84 1,72		µC
Peak rate of fall of recovery current	di(rec)max /dt					Tj=25°C Tj=150°C		29 24		A/µs
Reverse recovered energy	Erec					Tj=25°C Tj=150°C		0,30 0,64		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,36		K/W
Thermistor										
Rated resistance	R					Tj=25°C		22000		Ω
Deviation of R100	ΔR/R	R100=1486 Ω				Tc=100°C	-5		5	%
Power dissipation	P					Tc=100°C		200		mW
Power dissipation constant						Tj=25°C		2		mW/K
B-value	$B_{(25/50)}$	Tol. ±3%				Tj=25°C		3950		K
B-value	$B_{(25/100)}$	Tol. ±3%				Tj=25°C		3996		K
Vincotech NTC Reference						Tj=25°C			B	

Output Inverter

Figure 1 Output inverter IGBT

Typical output characteristics

$I_C = f(V_{CE})$



At

t_p = 250 µs

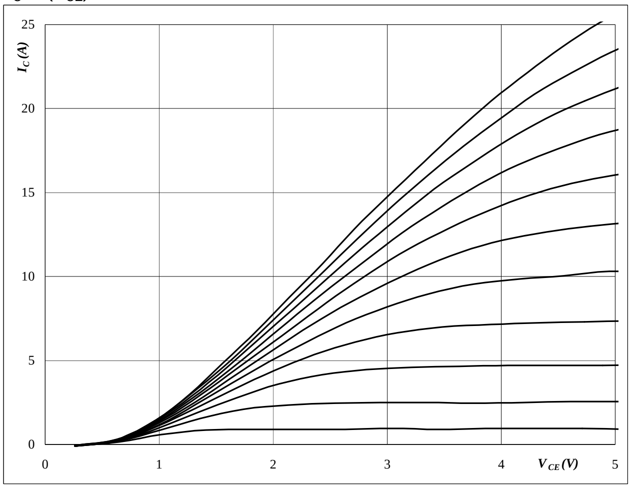
T_j = 25 °C

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2 Output inverter IGBT

Typical output characteristics

$I_C = f(V_{CE})$



At

t_p = 250 µs

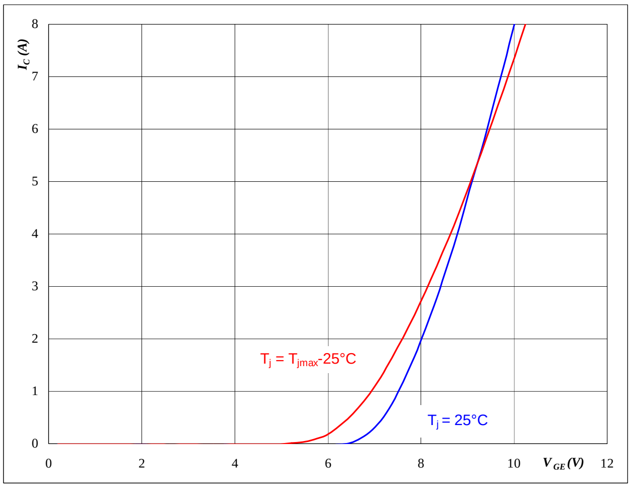
T_j = 150 °C

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3 Output inverter IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$



At

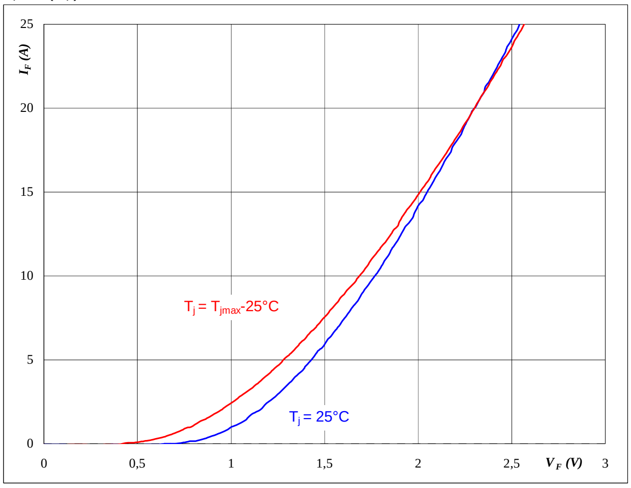
t_p = 250 µs

V_{CE} = 10 V

Figure 4 Output inverter FWD

Typical diode forward current as a function of forward voltage

$I_F = f(V_F)$



At

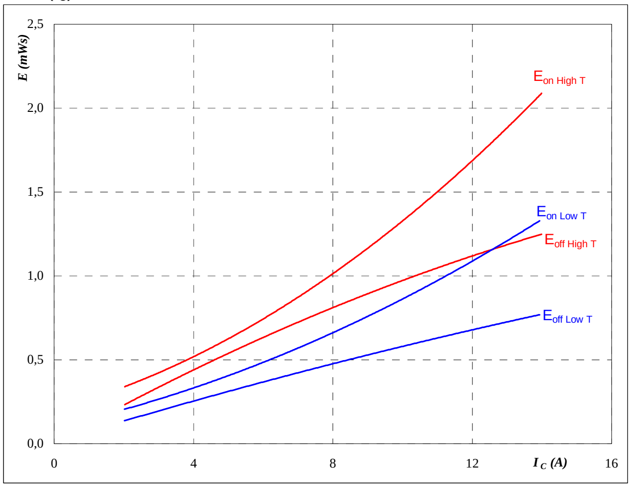
t_p = 250 µs

Output Inverter

Figure 5 Output inverter IGBT

Typical switching energy losses
as a function of collector current

$E = f(I_C)$



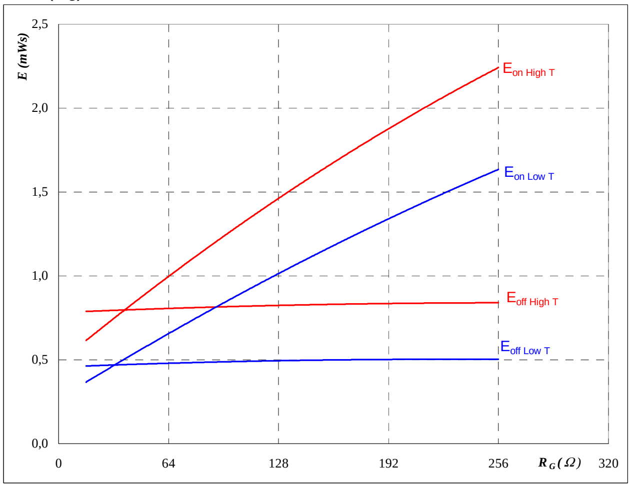
With an inductive load at

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω
R_{goff} =	64	Ω

Figure 6 Output inverter IGBT

Typical switching energy losses
as a function of gate resistor

$E = f(R_G)$



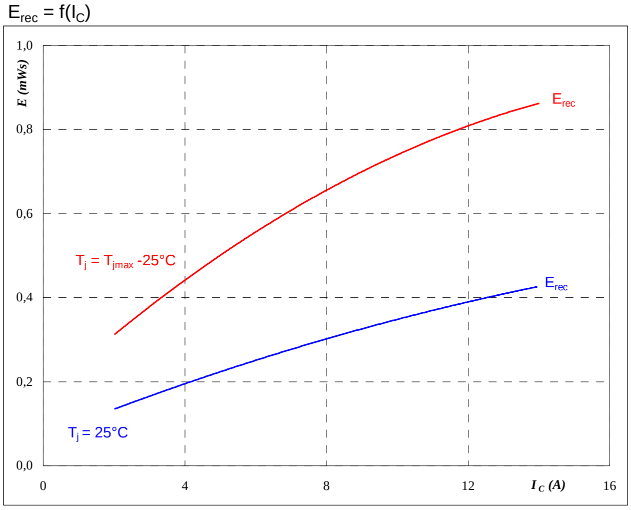
With an inductive load at

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	8	A

Figure 7 Output inverter FWD

Typical reverse recovery energy loss
as a function of collector current

$E_{rec} = f(I_C)$



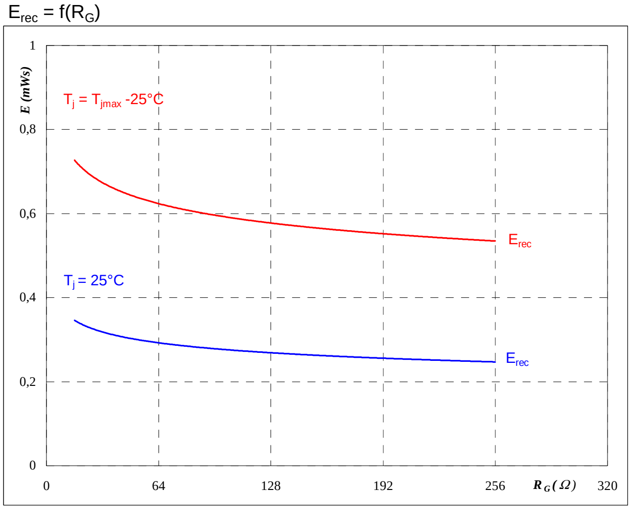
With an inductive load at

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω

Figure 8 Output inverter FWD

Typical reverse recovery energy loss
as a function of gate resistor

$E_{rec} = f(R_G)$



With an inductive load at

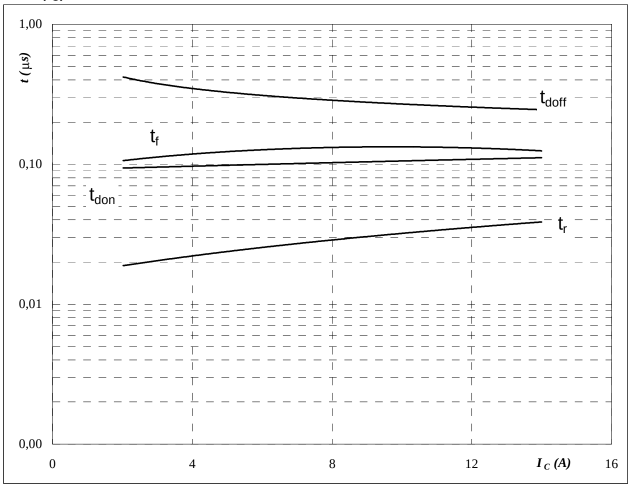
T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	8	A

Output Inverter

Figure 9 Output inverter IGBT

Typical switching times as a function of collector current

$t = f(I_C)$



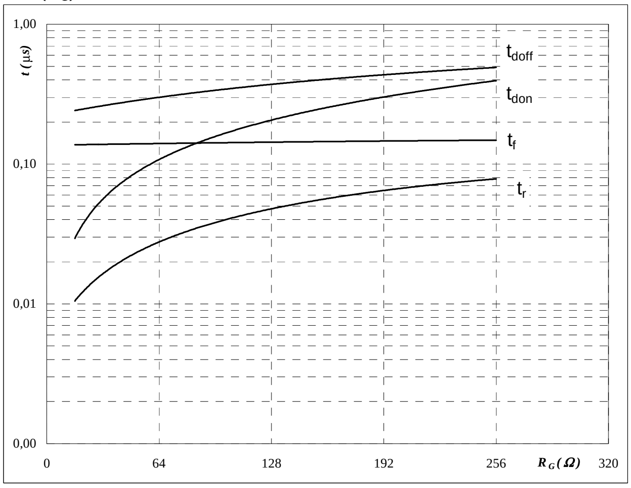
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω
R_{goff} =	64	Ω

Figure 10 Output inverter IGBT

Typical switching times as a function of gate resistor

$t = f(R_G)$



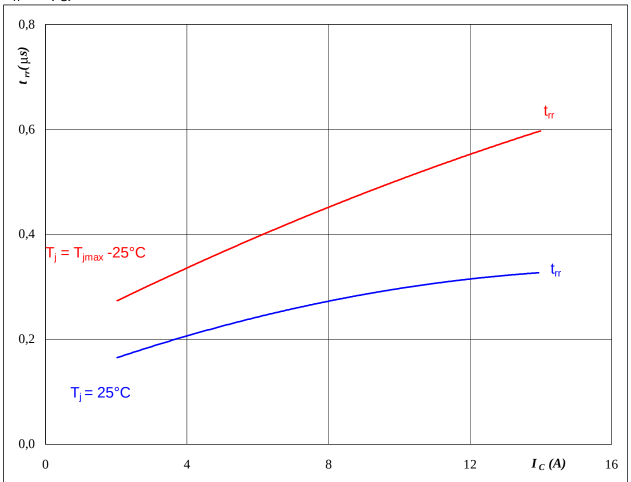
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	8	A

Figure 11 Output inverter FWD

Typical reverse recovery time as a function of collector current

$t_{rr} = f(I_C)$



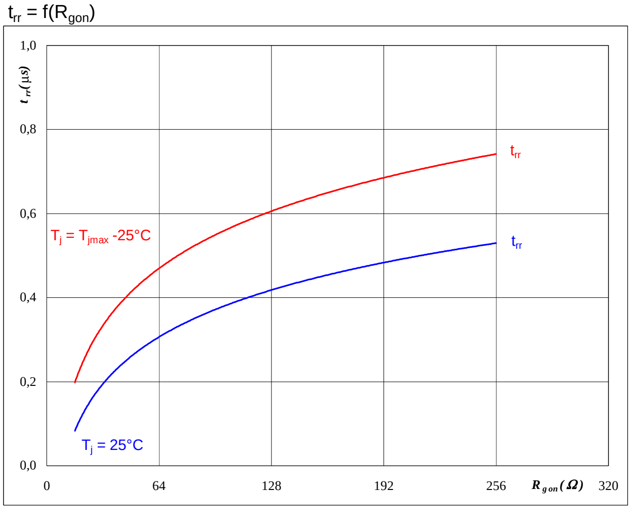
At

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω

Figure 12 Output inverter FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$t_{rr} = f(R_{gon})$



At

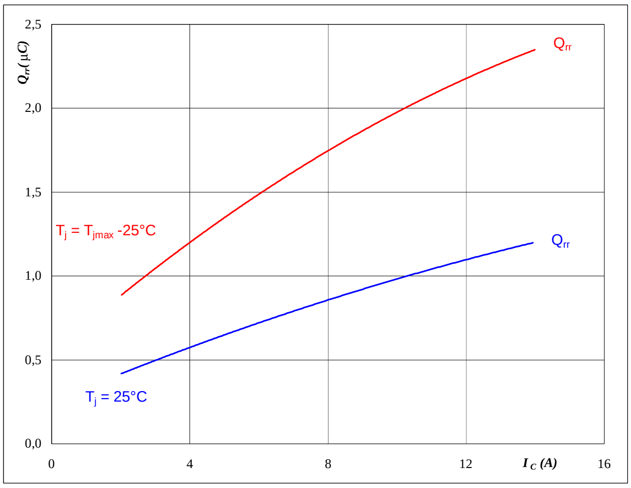
T_j =	25/150	°C
V_R =	600	V
I_F =	8	A
V_{GE} =	±15	V

Output Inverter

Figure 13 Output inverter FWD

Typical reverse recovery charge as a function of collector current

$Q_{rr} = f(I_C)$



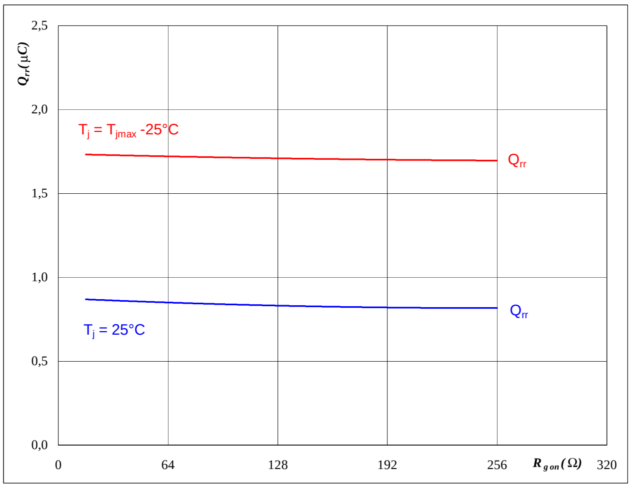
At

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω

Figure 14 Output inverter FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$Q_{rr} = f(R_{gon})$



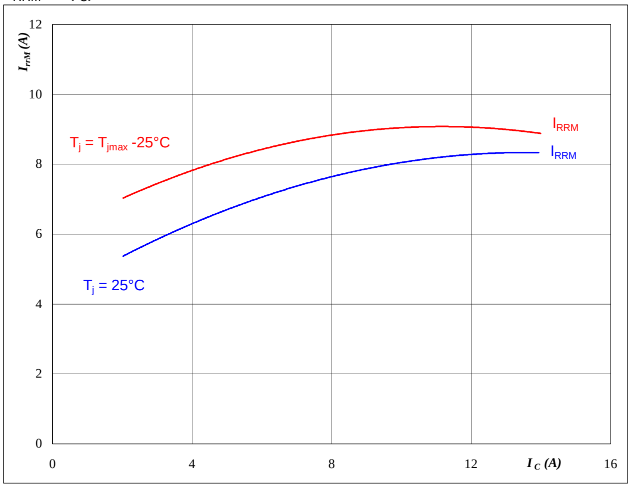
At

T_j =	25/150	°C
V_R =	600	V
I_F =	8	A
V_{GE} =	±15	V

Figure 15 Output inverter FWD

Typical reverse recovery current as a function of collector current

$I_{RRM} = f(I_C)$



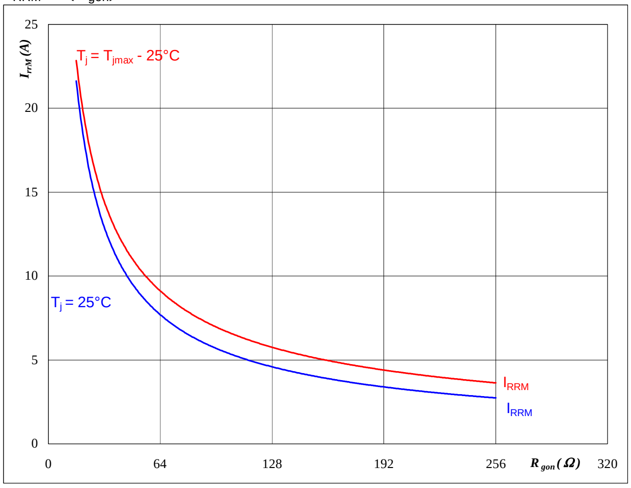
At

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω

Figure 16 Output inverter FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$I_{RRM} = f(R_{gon})$



At

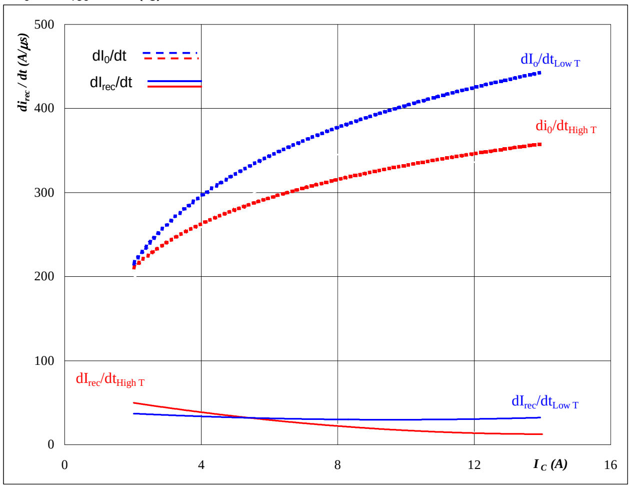
T_j =	25/150	°C
V_R =	600	V
I_F =	8	A
V_{GE} =	±15	V

Output Inverter

Figure 17	Output inverter FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current

$dI_0/dt, dI_{rec}/dt = f(I_C)$



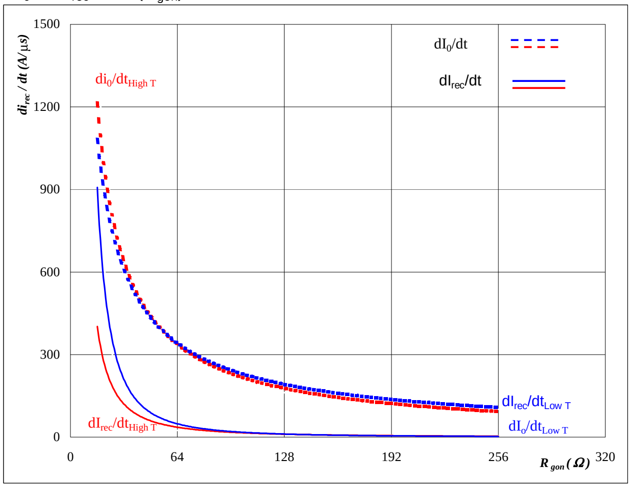
At

T_j =	25/150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	64	Ω

Figure 18	Output inverter FWD

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$dI_0/dt, dI_{rec}/dt = f(R_{gon})$



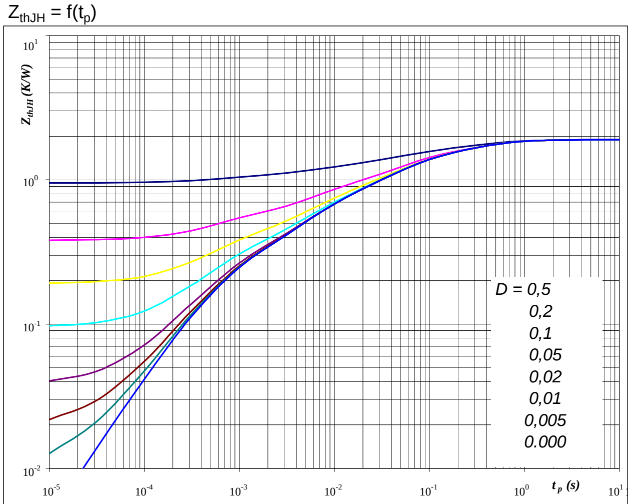
At

T_j =	25/150	°C
V_R =	600	V
I_F =	8	A
V_{GE} =	±15	V

Figure 19	Output inverter IGBT

IGBT transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



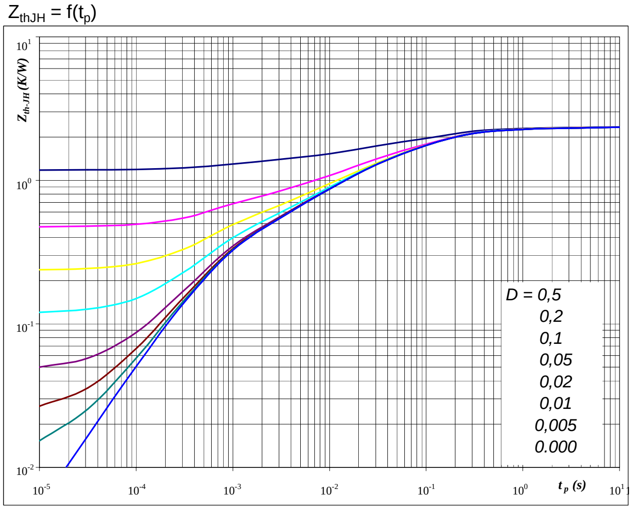
At

D =	t_p / T	
R_{thJH} =	1,90	K/W

Figure 20	Output inverter FWD

FWD transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	2,36	K/W

IGBT thermal model values

Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,08	1,6E+00	0,06	1,3E+00
0,54	2,7E-01	0,44	2,2E-01
0,67	4,7E-02	0,54	3,8E-02
0,41	6,5E-03	0,34	5,3E-03
0,21	5,7E-04	0,17	4,6E-04

FWD thermal model values

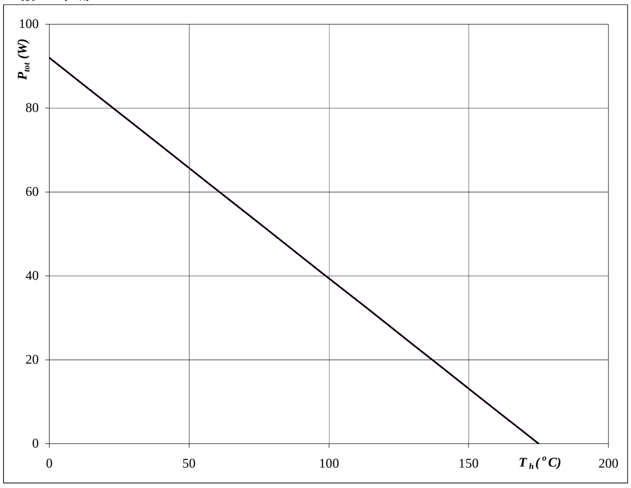
Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,07	5,6E+00	0,06	4,5E+00
0,25	4,7E-01	0,20	3,8E-01
1,04	8,9E-02	0,84	7,2E-02
0,65	1,0E-02	0,53	8,2E-03
0,34	7,5E-04	0,28	6,1E-04

Output Inverter

Figure 21	Output inverter IGBT

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



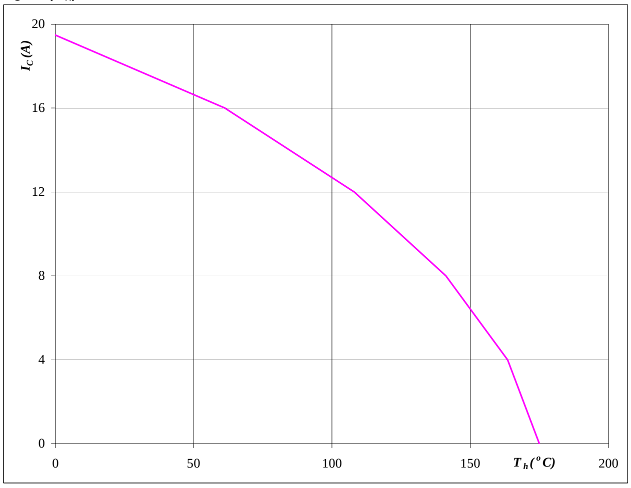
At

T_j = 175 °C

Figure 22	Output inverter IGBT

Collector current as a function of heatsink temperature

$I_C = f(T_h)$



At

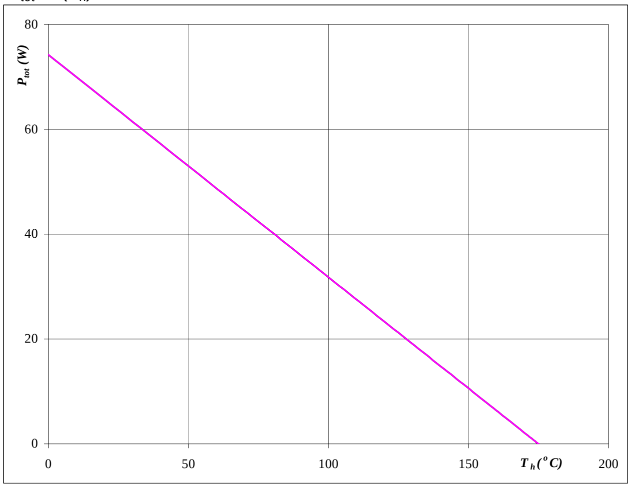
T_j = 175 °C

V_{GE} = 15 V

Figure 23	Output inverter FWD

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



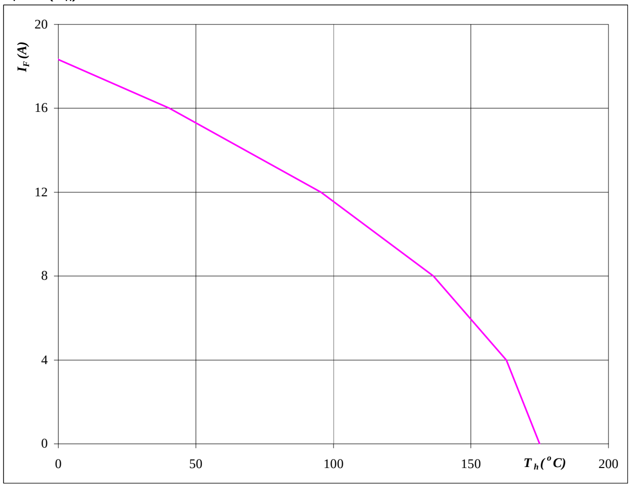
At

T_j = 175 °C

Figure 24	Output inverter FWD

Forward current as a function of heatsink temperature

$I_F = f(T_h)$



At

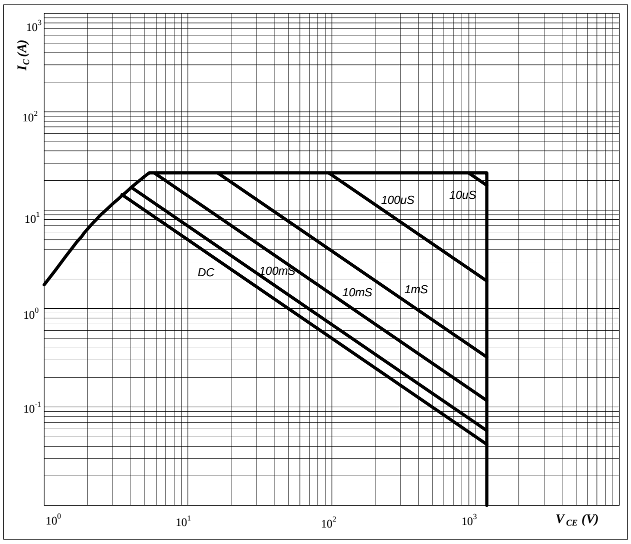
T_j = 175 °C

Output Inverter

Figure 25	Output inverter IGBT

Safe operating area as a function of collector-emitter voltage

$I_C = f(V_{CE})$



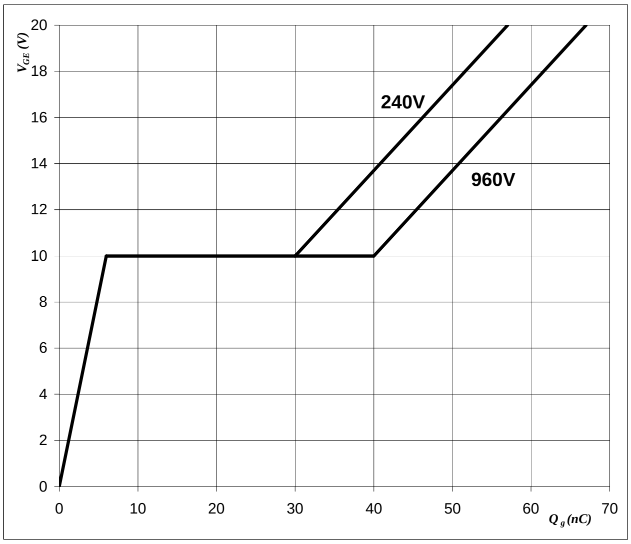
At

D =	single pulse	
T_h =	80	ºC
V_{GE} =	±15	V
T_j =	T_{jmax}	ºC

Figure 26	Output inverter IGBT

Gate voltage vs Gate charge

$V_{GE} = f(Q_{GE})$



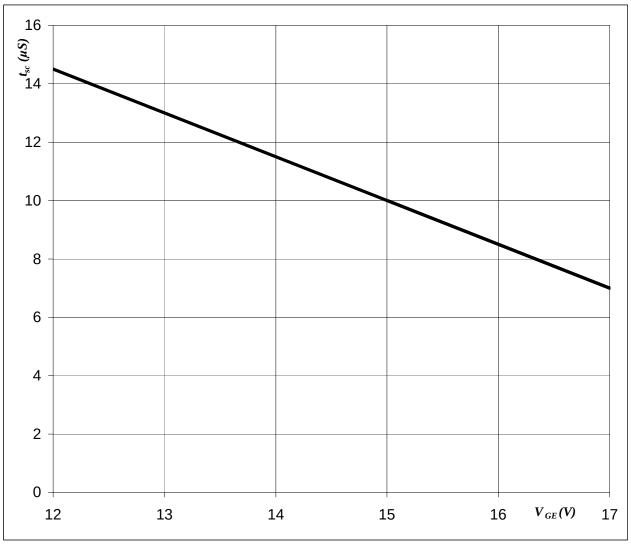
At

I_C =	8	A

Figure 27	Output inverter IGBT

Short circuit withstand time as a function of gate-emitter voltage

$t_{sc} = f(V_{GE})$



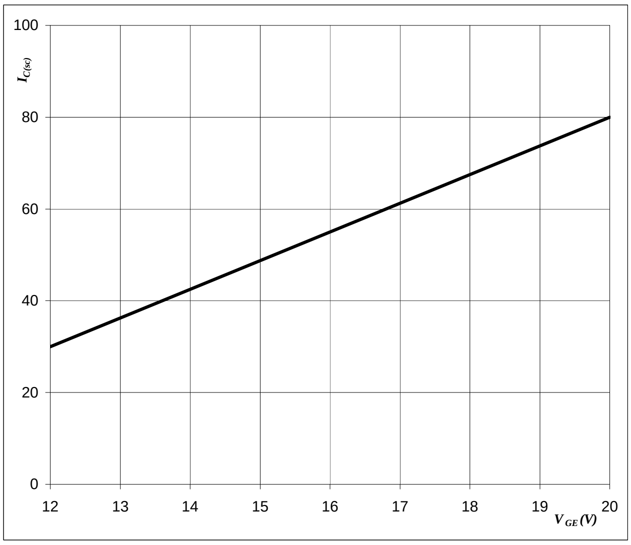
At

V_{CE} =	1200	V
T_j ≤	175	ºC

Figure 28	Output inverter IGBT

Typical short circuit collector current as a function of gate-emitter voltage

$V_{GE} = f(Q_{GE})$



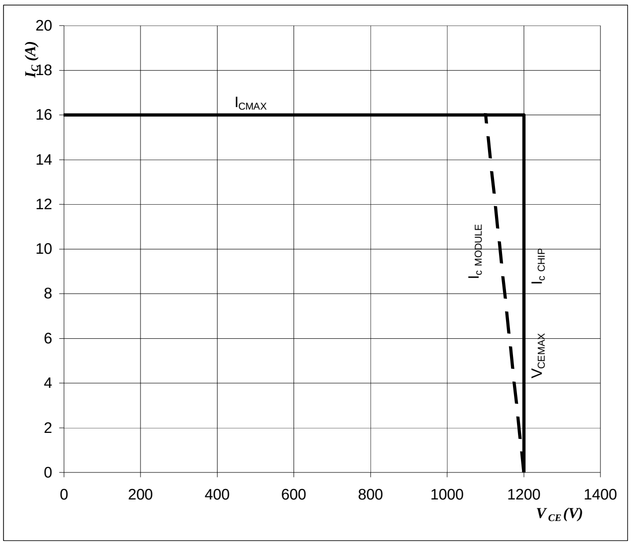
At

V_{CE} ≤	1200	V
T_j =	175	ºC

Figure 29 IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At

T_j = T_{jmax}-25 ºC

$U_{ccminus}$=U_{ccplus}

Switching mode : 3phase SPWM

Thermistor

Figure 1 Thermistor

Typical NTC characteristic as a function of temperature

$R_T = f(T)$

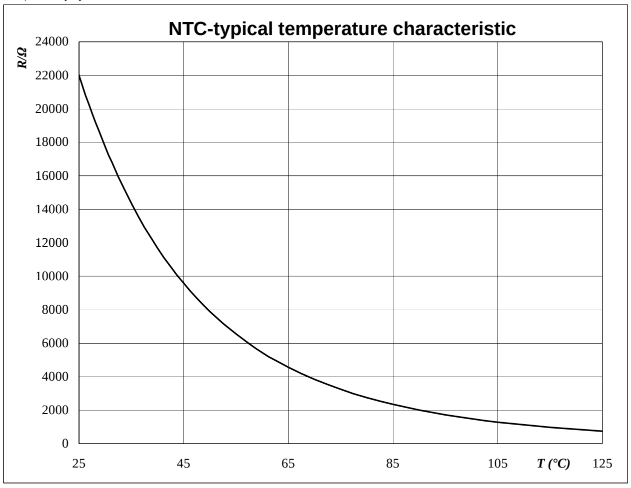


Figure 2 Thermistor

Typical NTC resistance values

$$R(T) = R_{25} \cdot e^{\left(B_{25/100} \cdot \left(\frac{1}{T} - \frac{1}{T_{25}}\right)\right)} \quad [\Omega]$$

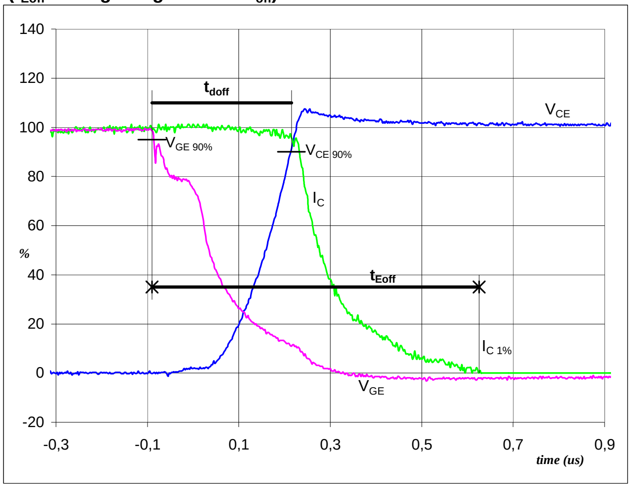
Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	64 Ω
R_{goff}	=	64 Ω

Figure 1 Output inverter IGBT

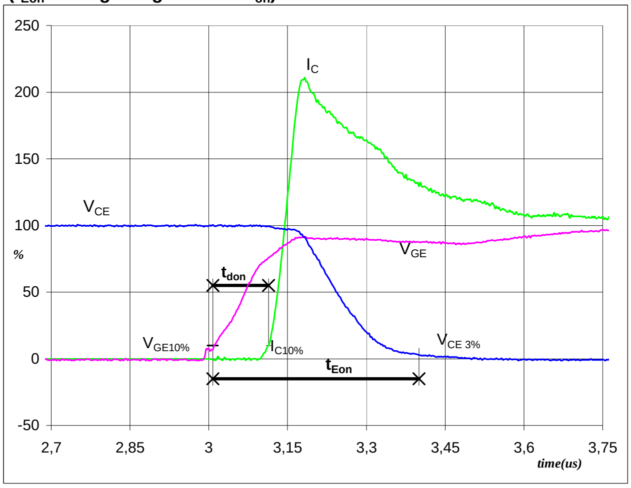
Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	600	V
I_C (100%) =	8	A
t_{doff} =	0,30	μs
t_{Eoff} =	0,72	μs

Figure 2 Output inverter IGBT

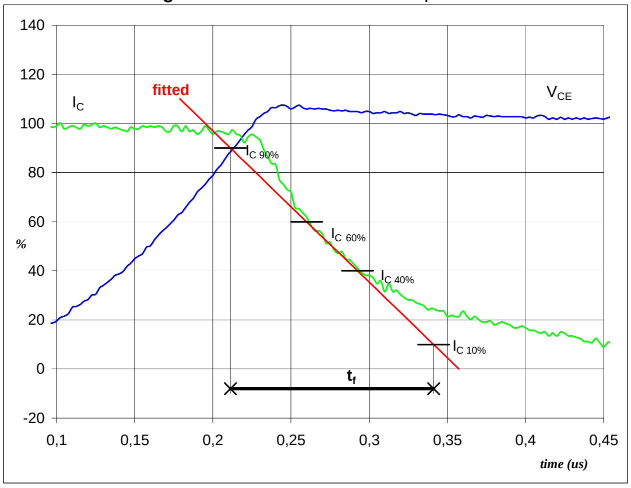
Turn-on Switching Waveforms & definition of tdon, t_{Eon}
(t_{Eon} = integrating time for E_{on})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	600	V
I_C (100%) =	8	A
t_{don} =	0,10	μs
t_{Eon} =	0,39	μs

Figure 3 Output inverter IGBT

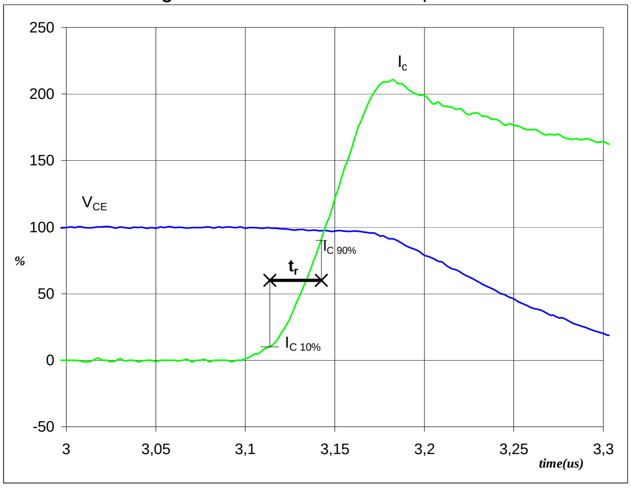
Turn-off Switching Waveforms & definition of t_f



V_C (100%) =	600	V
I_C (100%) =	8	A
t_f =	0,14	μs

Figure 4 Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r



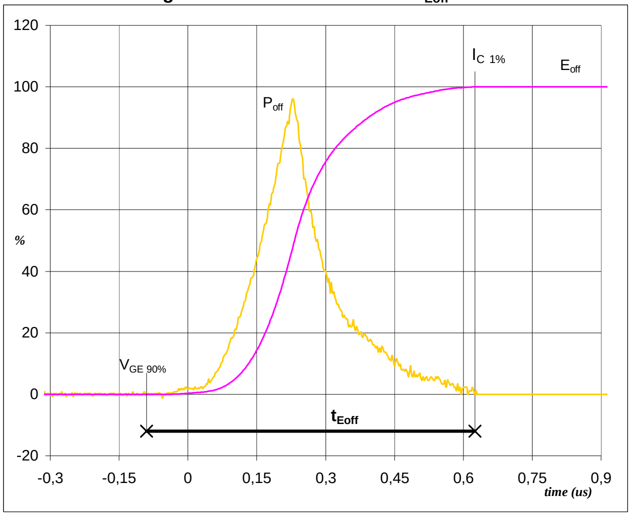
V_C (100%) =	600	V
I_C (100%) =	8	A
t_r =	0,03	μs

V23990-P707-F40-PM
preliminary datasheet

Switching Definitions Output Inverter

Figure 5 Output inverter IGBT

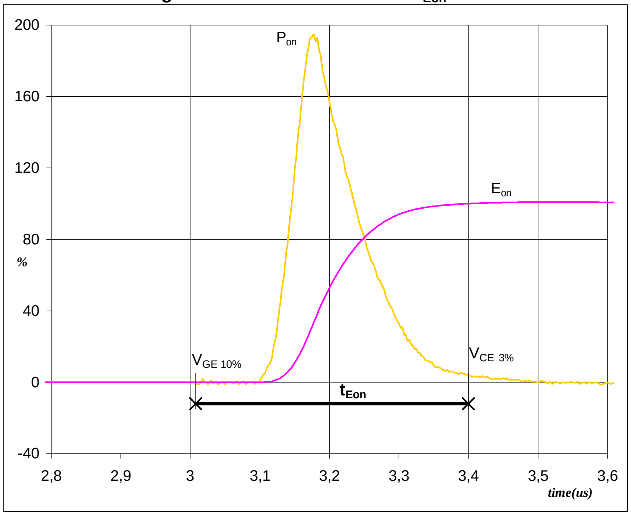
Turn-off Switching Waveforms & definition of t_{Eoff}



P_{off} (100%) =	4,78	kW
E_{off} (100%) =	0,81	mJ
t_{Eoff} =	0,72	µs

Figure 6 Output inverter IGBT

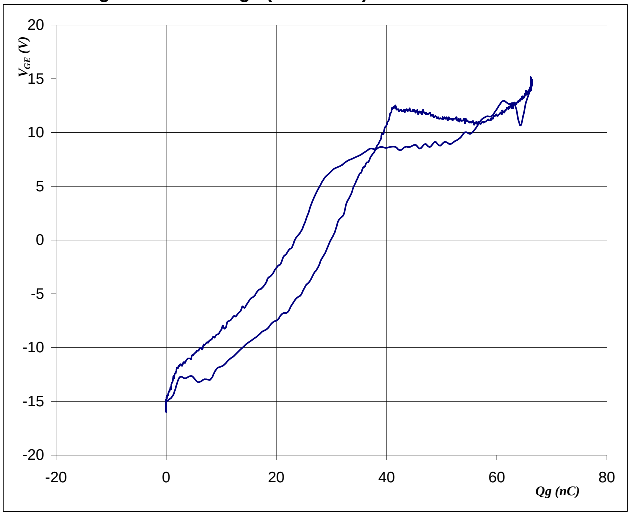
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) =	4,78	kW
E_{on} (100%) =	1,01	mJ
t_{Eon} =	0,39	µs

Figure 7 Output inverter FWD

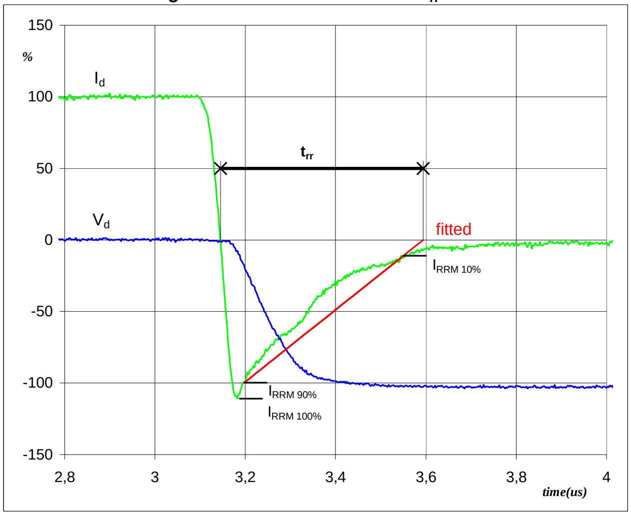
Gate voltage vs Gate charge (measured)



V_{GEoff} =	-15	V
V_{GEon} =	15	V
V_C (100%) =	600	V
I_C (100%) =	8	A
Q_g =	66,35	nC

Figure 8 Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{rr}

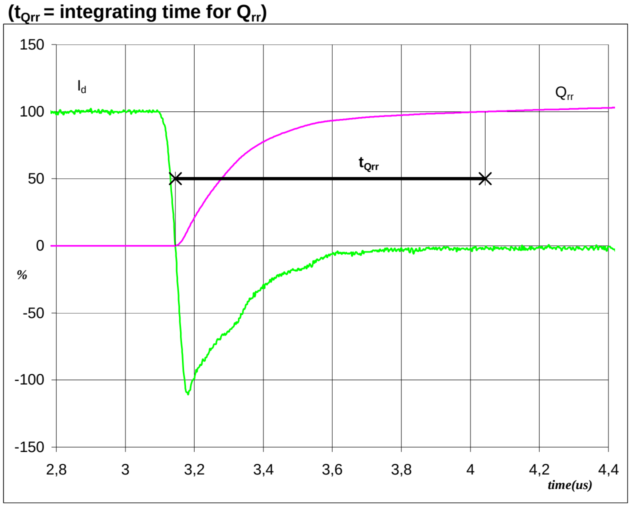


V_d (100%) =	600	V
I_d (100%) =	8	A
I_{RRM} (100%) =	-9	A
t_{rr} =	0,45	µs

Switching Definitions Output Inverter

Figure 9 Output inverter FWD

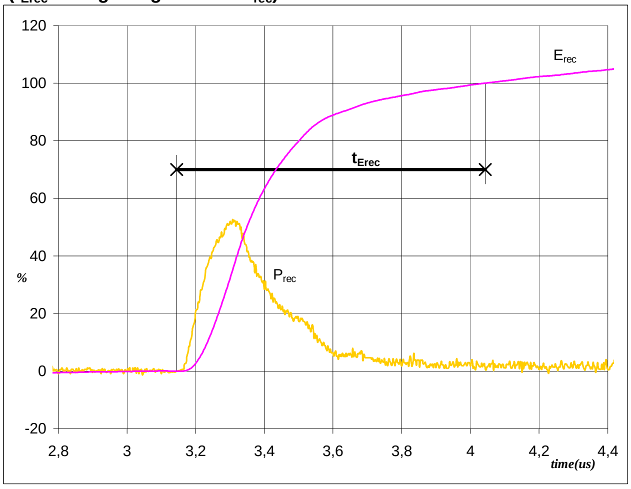
Turn-on Switching Waveforms & definition of t_{Qrr}
(t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	8	A
Q_{rr} (100%) =	1,72	μC
t_{Qrr} =	0,90	μs

Figure 10 Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
(t_{Erec}= integrating time for E_{rec})



P_{rec} (100%) =	4,78	kW
E_{rec} (100%) =	0,64	mJ
t_{Erec} =	0,90	μs

Ordering Code and Marking - Outline - Pinout

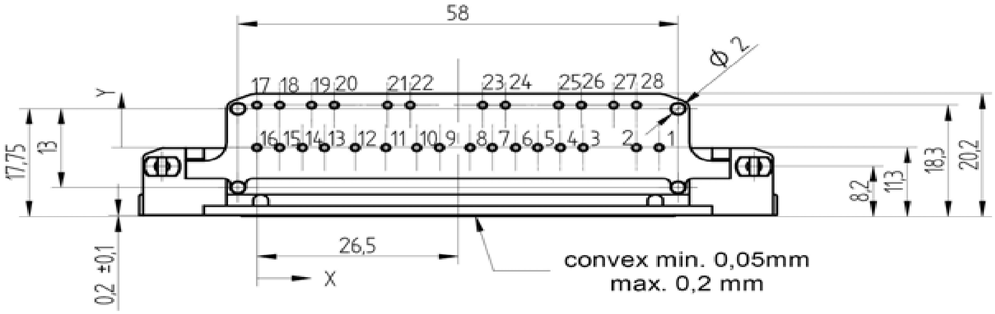
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	V23990-P707-F40	P707-F40	P707-F40

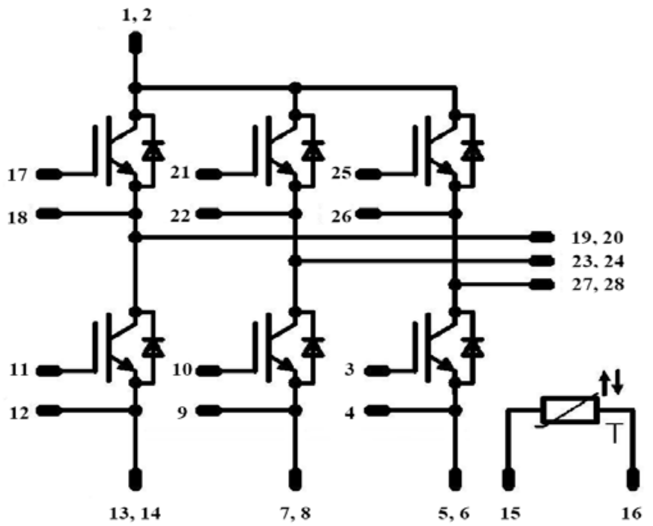
Outline

Pin Table

Pin	X	Y
1	53	0
2	50	0
3	43	0
4	40	0
5	37	0
6	34,1	0
7	31	0
8	28,1	0
9	24,05	0
10	21,05	0
11	17	0
12	12,95	0
13	8,9	0
14	6	0
15	3	0
16	0	0
17	0	7
18	3	7
19	7,2	7
20	10,2	7
21	17,2	7
22	20,2	7
23	29,75	7
24	32,75	7
25	39,75	7
26	42,75	7
27	47	7
28	50	7



Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.