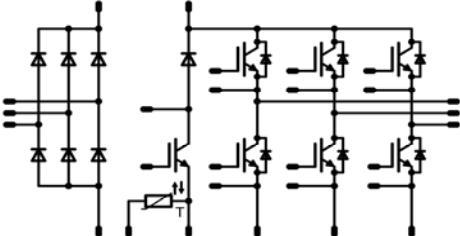


<b>flow90PIM 1</b>		<b>1200V/8A</b>
<p><b>Features</b></p> <ul style="list-style-type: none"> <li>• Trench Fieldstop Technology IGBT4 for low saturation loss</li> <li>• Supports design with 90° mounting angle between heatsink and PCB</li> <li>• Clip-in PCB mounting</li> <li>• Clip or screw on heatsink mounting</li> </ul>		<p><b>flow90PIM 1</b></p> 
<p><b>Target Applications</b></p> <ul style="list-style-type: none"> <li>• Industrial drives</li> </ul>		<p><b>Schematic</b></p> 
<p><b>Types</b></p> <ul style="list-style-type: none"> <li>• V23990-P639-A40-PM</li> </ul>		

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

### Input Rectifier Diode

Repetitive peak reverse voltage	$V_{RRM}$		1600	V
DC forward current	$I_{FAV}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	28 36	A
Surge forward current	$I_{FSM}$		200	A
$I^2t$ -value	$I^2t$	$t_p=10\text{ms}$ $T_j=25^\circ\text{C}$	200	$\text{A}^2\text{s}$
Power dissipation per Diode	$P_{tot}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 50	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$

### Inverter Transistor

Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	15 19	A
Repetitive peak collector current	$I_{Cpulse}$	$t_p$ limited by $T_{j\max}$	24	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{j\max}$	24	A
Power dissipation per IGBT	$P_{tot}$	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	58 87	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{SC}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>Inverter FWD</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	16 21	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	20	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	41 62	W
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Brake Transistor</b>				
Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	9 9	A
Repetitive peak collector current	$I_{CPuls}$	$t_p$ limited by $T_j\max$	12	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{j\max}$	12	A
Power dissipation per IGBT	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	39 59	W
Gate-emitter peak voltage	$V_{GE}$		±20	V
Short circuit ratings	$t_{SC}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_j\max$		175	°C
<b>Brake FWD</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	9 9	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	6	A
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	23 35	W
Maximum Junction Temperature	$T_j\max$		150	°C
<b>Thermal Properties</b>				
Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{op}$		-40...+( $T_{j\max} - 25$ )	°C
<b>Insulation Properties</b>				
Insulation voltage	$V_{is}$	$t=2\text{s}$ DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm
Comparative tracking index	CTI		>200	

**Characteristic Values**

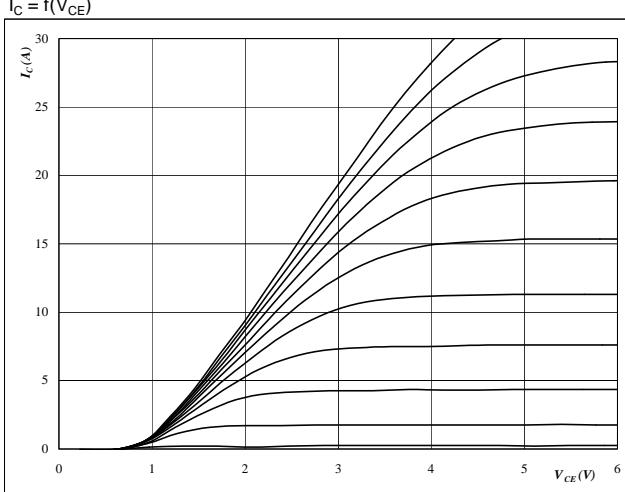
Parameter	Symbol	Conditions				Value			Unit	
			V <sub>GE</sub> [V] or V <sub>GS</sub> [V]	V <sub>I</sub> [V] or V <sub>CE</sub> [V] or V <sub>DS</sub> [V]	I <sub>C</sub> [A] or I <sub>F</sub> [A] or I <sub>D</sub> [A]	T <sub>J</sub>	Min	Typ	Max	
<b>Input Rectifier Diode</b>										
Forward voltage	V <sub>F</sub>				30	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	1	1,26 1,24	1,6	V
Threshold voltage (for power loss calc. only)	V <sub>to</sub>				30	T <sub>J</sub> =25°C T <sub>J</sub> =125°C		0,92 0,82		V
Slope resistance (for power loss calc. only)	r <sub>t</sub>				30	T <sub>J</sub> =25°C T <sub>J</sub> =125°C		11 14		mΩ
Reverse current	I <sub>r</sub>			1500		T <sub>J</sub> =25°C T <sub>J</sub> =125°C			0,2	mA
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Thermal grease thickness≤50um λ = 1 W/mK						2,10		K/W
<b>Inverter Transistor</b>										
Gate emitter threshold voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =V <sub>GE</sub>			0,0003	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>		15		8	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	1,6	1,92 2,22	2,3	V
Collector-emitter cut-off current incl. Diode	I <sub>CES</sub>		0	1200		T <sub>J</sub> =25°C T <sub>J</sub> =150°C			0,001	mA
Gate-emitter leakage current	I <sub>GES</sub>		20	0		T <sub>J</sub> =25°C T <sub>J</sub> =150°C			120	nA
Integrated Gate resistor	R <sub>gint</sub>							none		Ω
Turn-on delay time	t <sub>d(on)</sub>	R <sub>goff</sub> =32 Ω R <sub>gon</sub> =32 Ω	±15	600	8	T <sub>J</sub> =25°C T <sub>J</sub> =150°C		58 59		ns
Rise time	t <sub>r</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		23 22		
Turn-off delay time	t <sub>d(off)</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		177 244		
Fall time	t <sub>f</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		64 137		
Turn-on energy loss per pulse	E <sub>on</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		0,51 0,83		mWs
Turn-off energy loss per pulse	E <sub>off</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		0,45 0,78		
Input capacitance	C <sub>ies</sub>	f=1MHz	0	25		T <sub>J</sub> =25°C		490		pF
Output capacitance	C <sub>oss</sub>							50		
Reverse transfer capacitance	C <sub>rss</sub>							30		
Gate charge	Q <sub>Gate</sub>		±15	960	8	T <sub>J</sub> =25°C		50		nC
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Thermal grease thickness≤50um λ = 1 W/mK						1,65		K/W
<b>Inverter FWD</b>										
Diode forward voltage	V <sub>F</sub>				10	T <sub>J</sub> =25°C T <sub>J</sub> =150°C	1,35	1,86 1,77	2,05	V
Peak reverse recovery current	I <sub>RRM</sub>	R <sub>gon</sub> =32 Ω	±15	600	8	T <sub>J</sub> =25°C T <sub>J</sub> =150°C		7 9		A
Reverse recovery time	t <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		241 416		ns
Reverse recovered charge	Q <sub>rr</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		0,81 1,66		μC
Peak rate of fall of recovery current	di(rec)max /dt					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		89 51		A/μs
Reverse recovered energy	E <sub>rec</sub>					T <sub>J</sub> =25°C T <sub>J</sub> =150°C		0,31 0,66		mWs
Thermal resistance chip to heatsink per chip	R <sub>thJH</sub>	Thermal grease thickness≤50um λ = 1 W/mK						2,31		K/W

**Characteristic Values**

Parameter	Symbol	Conditions					Value			Unit
			$V_{GE}$ [V] or $V_{GS}$ [V]	$V_T$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_J$	Min	Typ	Max	
<b>Brake Transistor</b>										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,00015	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		4	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$	1,6	1,87 2,21	2,1	V
Collector-emitter cut-off incl diode	$I_{CES}$		0	1200		$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$			0,0005	mA
Gate-emitter leakage current	$I_{GES}$		20	0		$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$			120	nA
Integrated Gate resistor	$R_{gint}$							none		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=64 \Omega$ $R_{gon}=64 \Omega$	$\pm 15$	600	4	$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		83 79		ns
Rise time	$t_r$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		28 32		
Turn-off delay time	$t_{d(off)}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		178 243		
Fall time	$t_f$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		77 132		
Turn-on energy loss per pulse	$E_{on}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		0,26 0,39		mWs
Turn-off energy loss per pulse	$E_{off}$					$T_J=25^\circ\text{C}$ $T_J=150^\circ\text{C}$		0,24 0,41		
Input capacitance	$C_{ies}$							250		pF
Output capacitance	$C_{oss}$					$T_J=25^\circ\text{C}$		25		
Reverse transfer capacitance	$C_{rss}$							15		
Gate charge	$Q_{Gate}$		$\pm 15$	960	4	$T_J=25^\circ\text{C}$		23		nC
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$							2,44	K/W
<b>Brake FWD</b>										
Diode forward voltage	$V_F$				3	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$	1	1,65 1,52	2,3	V
Reverse leakage current	$I_r$	$R_{gon}=64 \Omega$		1200		$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$			250	$\text{mA}$
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=64 \Omega$ $R_{gon}=64 \Omega$	$\pm 15$	600	4	$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		2,77 3,62		A
Reverse recovery time	$t_{rr}$					$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		357 649		ns
Reverse recovered charge	$Q_{rr}$					$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,44 0,44		$\mu\text{C}$
Peak rate of fall of recovery current	$\frac{di(\text{rec})}{dt}$ max					$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		18 14		$\text{A}/\mu\text{s}$
Reverse recovery energy	$E_{rec}$					$T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$		0,20 0,44		mWs
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$							3,03	K/W
<b>Thermistor</b>										
Rated resistance	$R$					$T_J=25^\circ\text{C}$		22000		$\Omega$
Deviation of R100	$\Delta R/R$	$R_{100}=1486 \Omega$				$T_C=100^\circ\text{C}$	-5		5	%
Power dissipation	$P$					$T_C=25^\circ\text{C}$		200		mW
Power dissipation constant						$T_J=25^\circ\text{C}$		2		$\text{mW}/\text{K}$
B-value	$B_{(25/50)}$	Tol. ±3%				$T_J=25^\circ\text{C}$		3950		K
B-value	$B_{(25/100)}$	Tol. ±3%				$T_J=25^\circ\text{C}$		3996		K
Vincotech NTC Reference						$T_J=25^\circ\text{C}$			B	

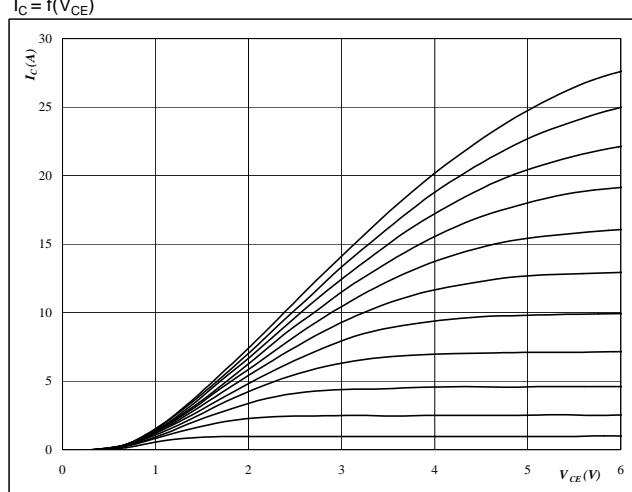
## Output Inverter

**Figure 1**  
Typical output characteristics  
 $I_C = f(V_{CE})$



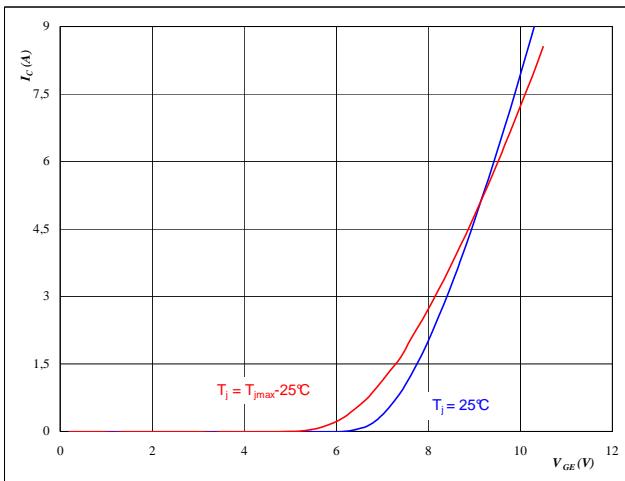
At  
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$   
 $T_j = 25 \text{ } ^\circ\text{C}$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
Typical output characteristics  
 $I_C = f(V_{CE})$



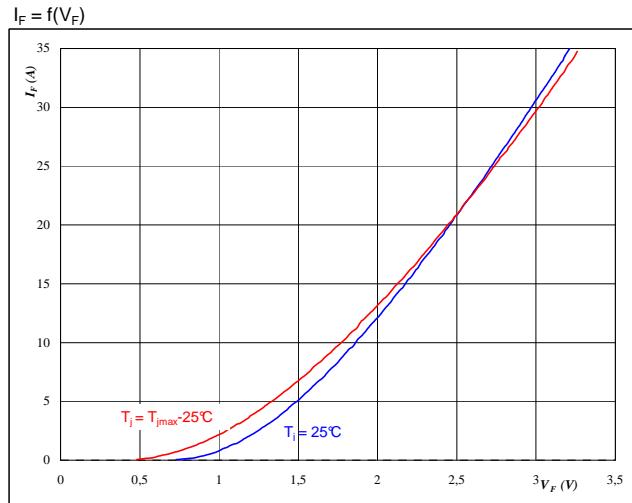
At  
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$   
 $T_j = 150 \text{ } ^\circ\text{C}$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
Typical transfer characteristics  
 $I_C = f(V_{GE})$



At  
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$   
 $V_{CE} = 10 \text{ } \text{V}$

**Figure 4**  
Typical diode forward current as a function of forward voltage  
 $I_F = f(V_F)$



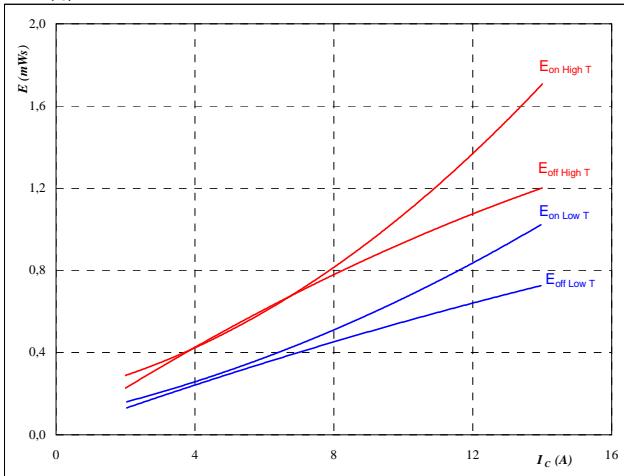
At  
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$

## Output Inverter

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



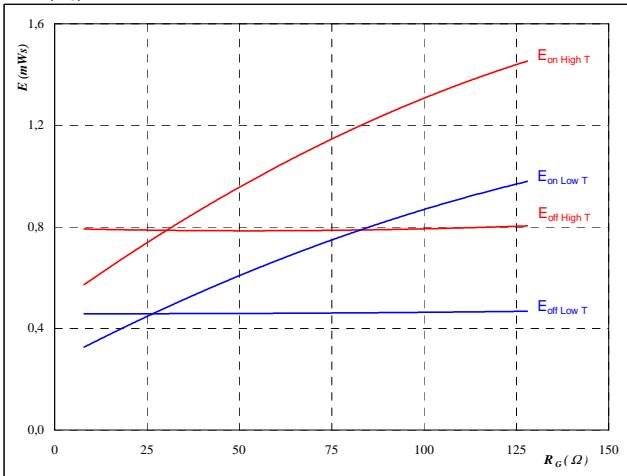
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Output inverter IGBT**
**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



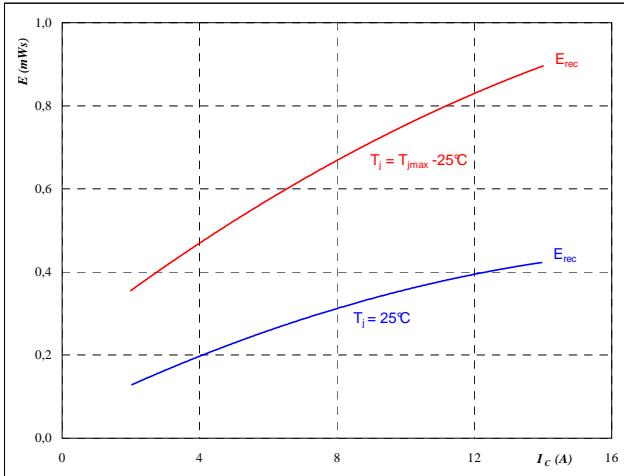
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

**Figure 7**
**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



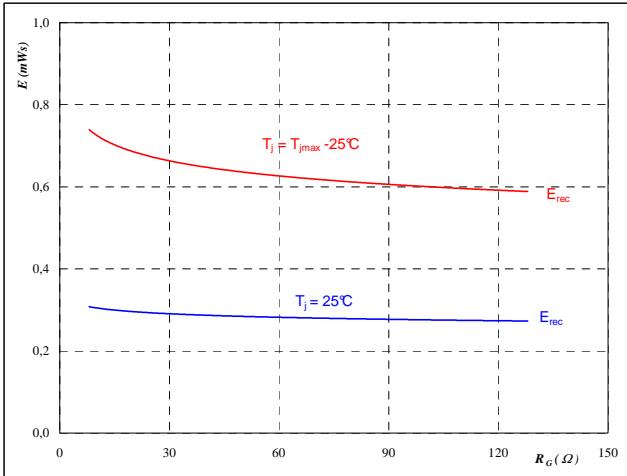
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

**Figure 8**
**Output inverter FWD**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

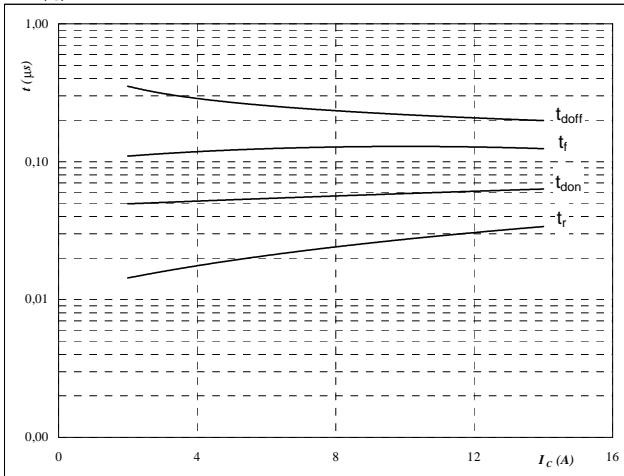
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

## Output Inverter

**Figure 9**

**Typical switching times as a function of collector current**

$$t = f(I_C)$$



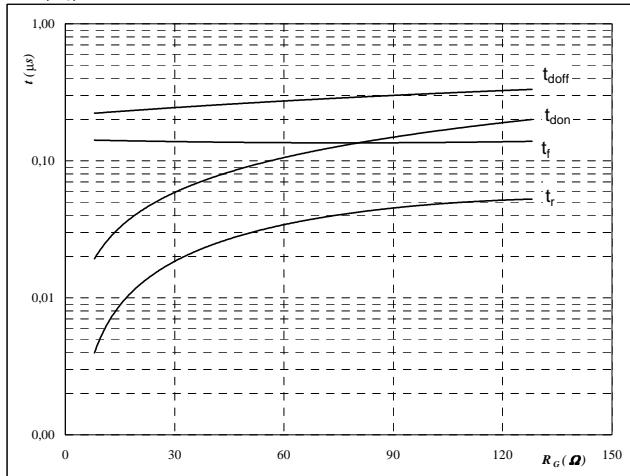
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$R_{gon} =$	32	Ω
$R_{goff} =$	32	Ω

**Figure 10**

**Typical switching times as a function of gate resistor**

$$t = f(R_G)$$



With an inductive load at

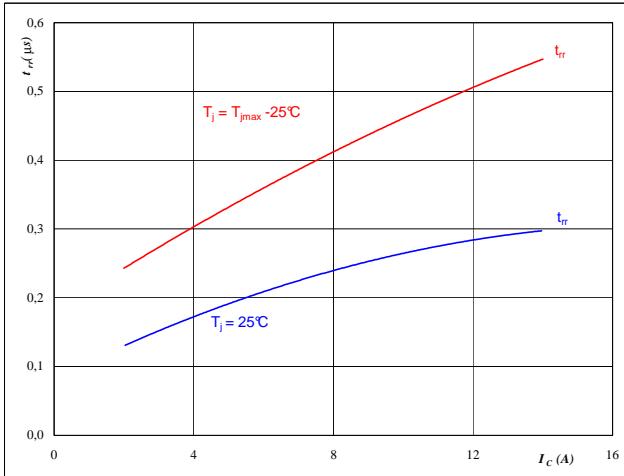
$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$I_C =$	8	A

**Figure 11**

**Output inverter FWD**

**Typical reverse recovery time as a function of collector current**

$$t_{rr} = f(I_C)$$



**At**

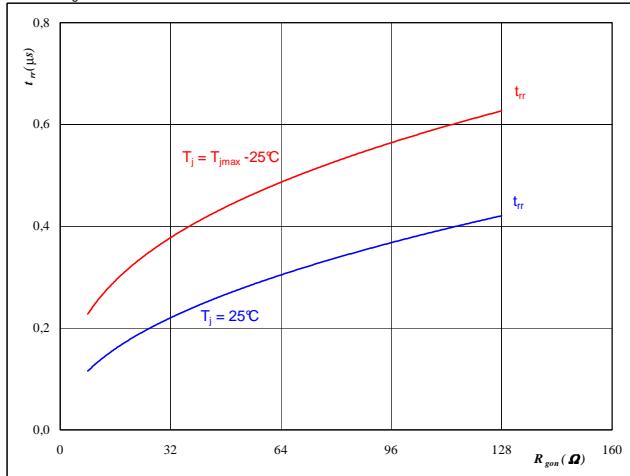
$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$R_{gon} =$	32	Ω

**Figure 12**

**Output inverter FWD**

**Typical reverse recovery time as a function of IGBT turn on gate resistor**

$$t_{rr} = f(R_{gon})$$



**At**

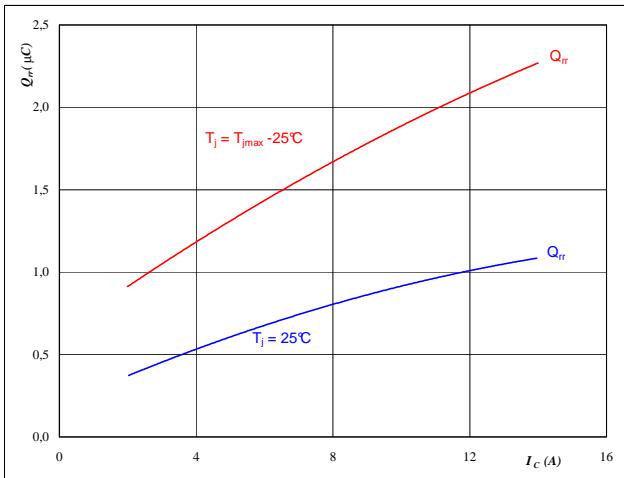
$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	8	A
$V_{GE} =$	$\pm 15$	V

## Output Inverter

**Figure 13**

Typical reverse recovery charge as a function of collector current

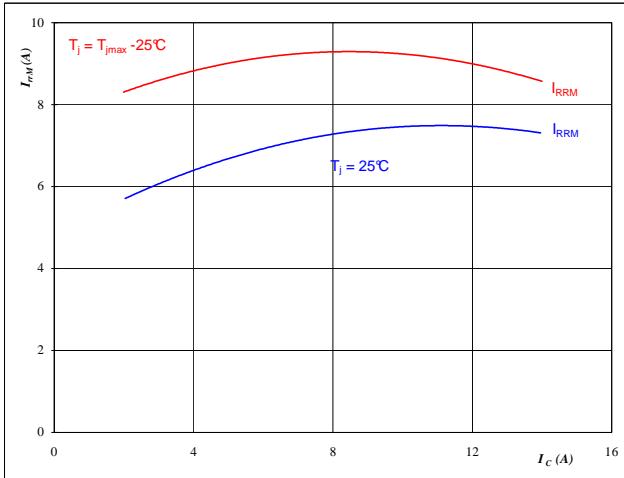
$$Q_{rr} = f(I_C)$$


**Figure 15**

Output inverter FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


**At**

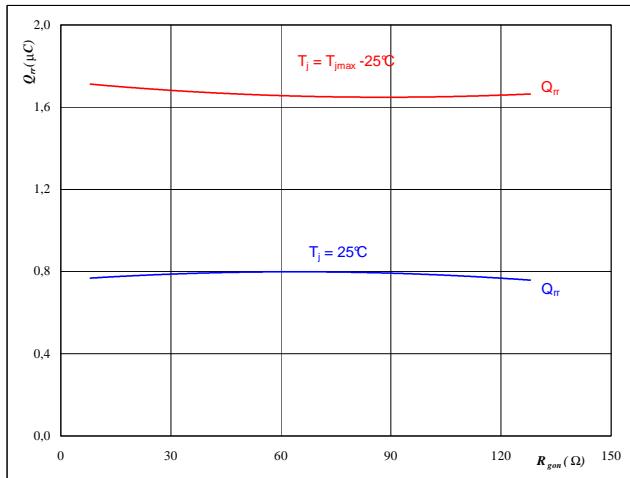
$T_j =$	25/150	$^\circ C$
$V_{CE} =$	600	V
$V_{GE} =$	$\pm 15$	V
$R_{gon} =$	32	$\Omega$

**Figure 14**

Output inverter FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


**At**

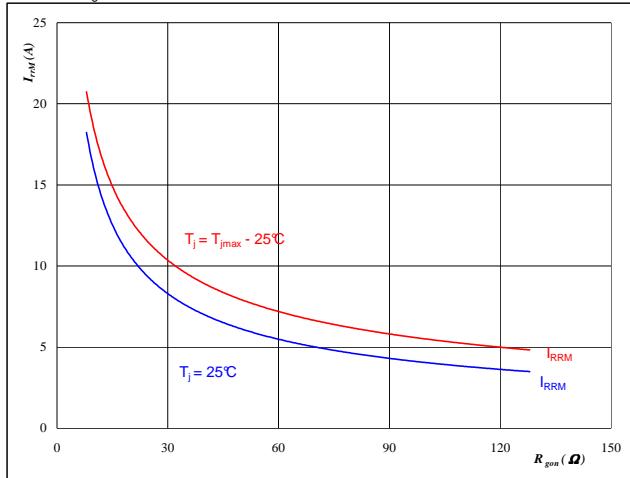
$T_j =$	25/150	$^\circ C$
$V_R =$	600	V
$I_F =$	8	A
$V_{GE} =$	$\pm 15$	V

**Figure 16**

Output inverter FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

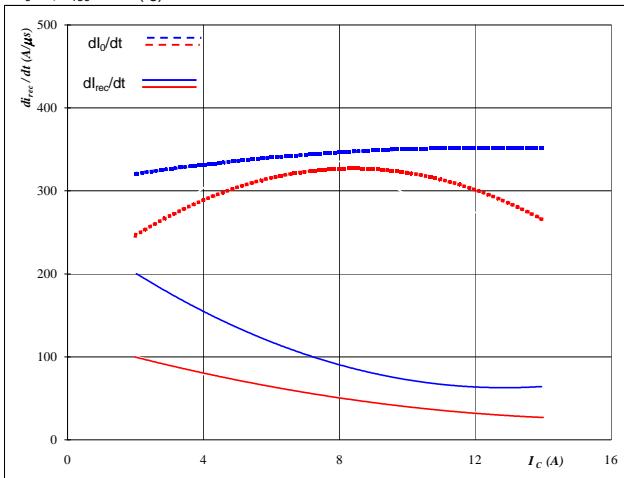

**At**

$T_j =$	25/150	$^\circ C$
$V_R =$	600	V
$I_F =$	8	A
$V_{GE} =$	$\pm 15$	V

## Output Inverter

**Figure 17**

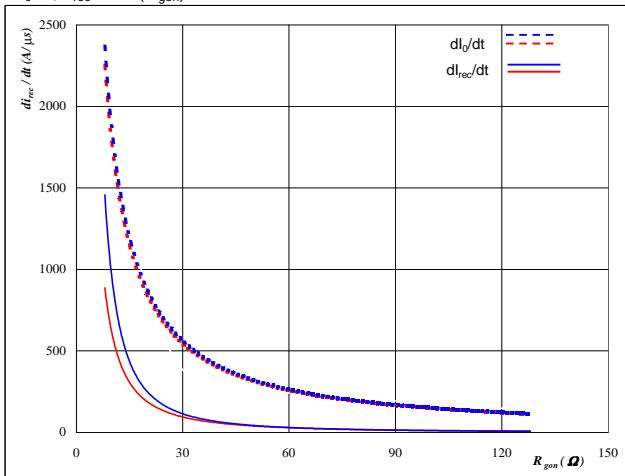
Typical rate of fall of forward  
and reverse recovery current as a  
function of collector current  
 $dl_0/dt, dl_{rec}/dt = f(I_C)$


**At**

T<sub>j</sub> = 25/150 °C  
V<sub>CE</sub> = 600 V  
V<sub>GE</sub> = ±15 V  
R<sub>gon</sub> = 32 Ω

**Output inverter FWD**
**Figure 18**

Typical rate of fall of forward  
and reverse recovery current as a  
function of IGBT turn on gate resistor  
 $dl_0/dt, dl_{rec}/dt = f(R_{gon})$

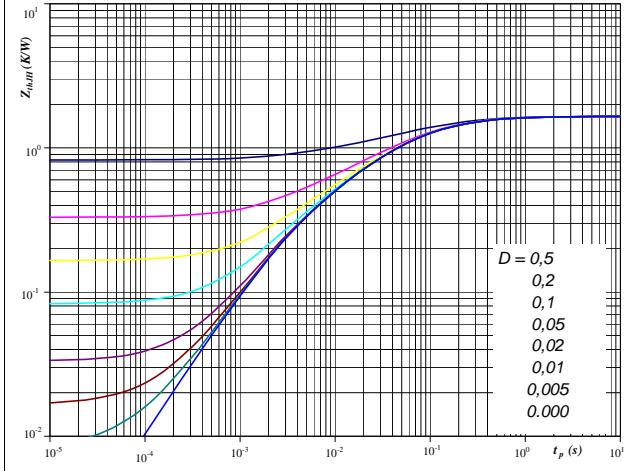

**At**

T<sub>j</sub> = 25/150 °C  
V<sub>R</sub> = 600 V  
I<sub>F</sub> = 8 A  
V<sub>GE</sub> = ±15 V

**Figure 19**

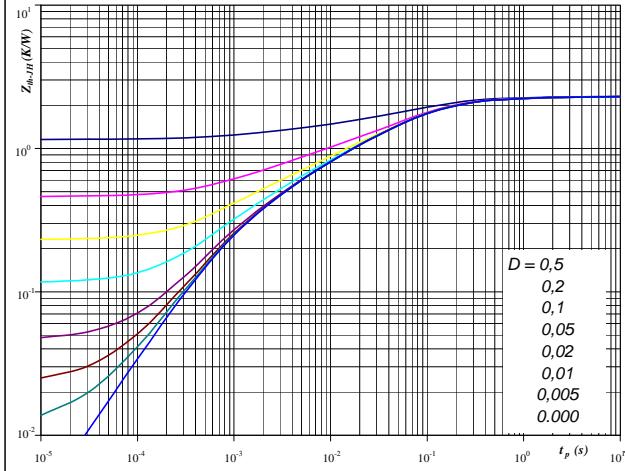
IGBT transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


**Output inverter IGBT**
**Figure 20**

FWD transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$

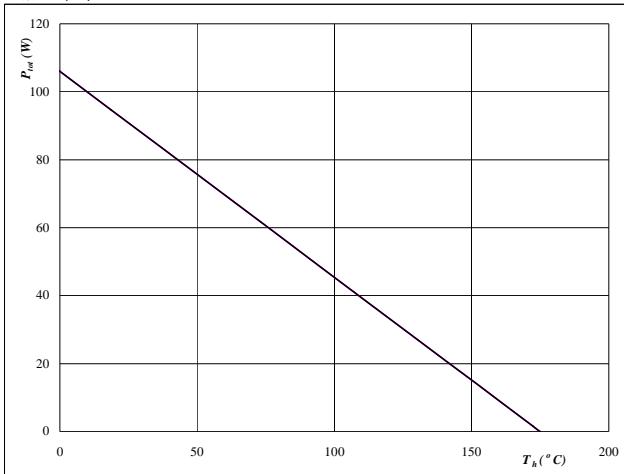

**Output inverter FWD**

## Output Inverter

**Figure 21**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

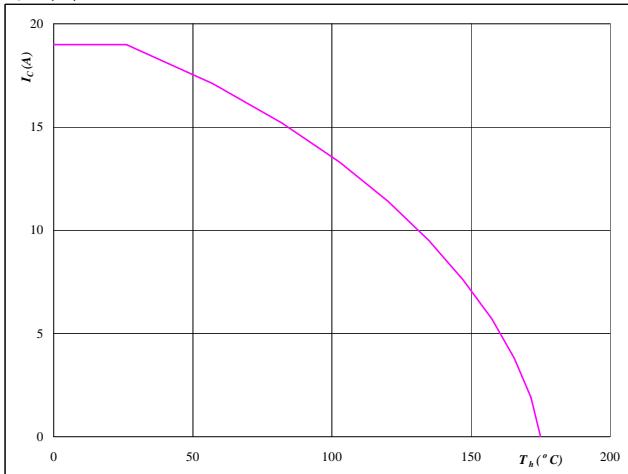

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 22**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

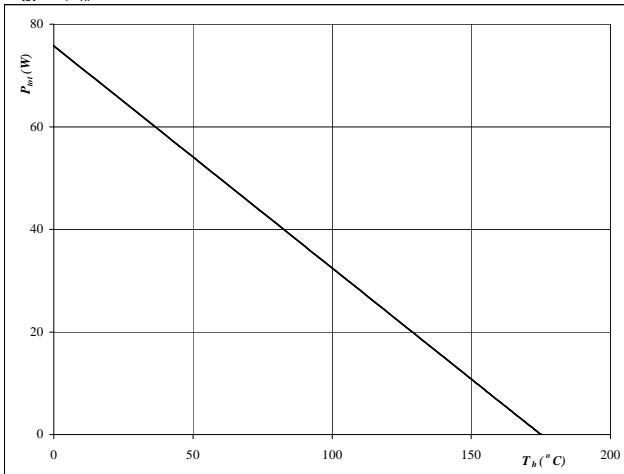
$$T_j = 175 \quad ^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 23**
**Output inverter FWD**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

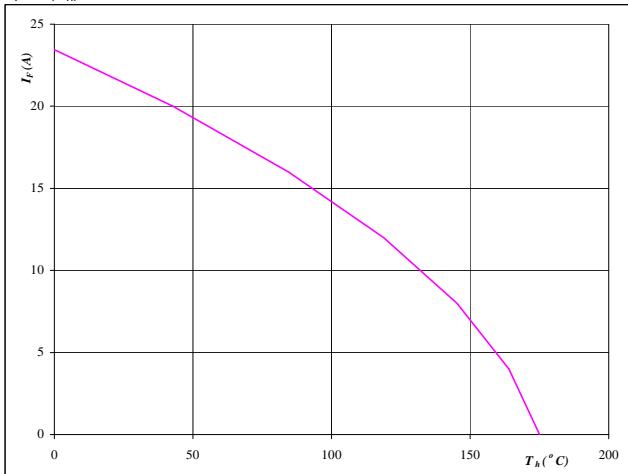

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Figure 24**
**Output inverter FWD**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

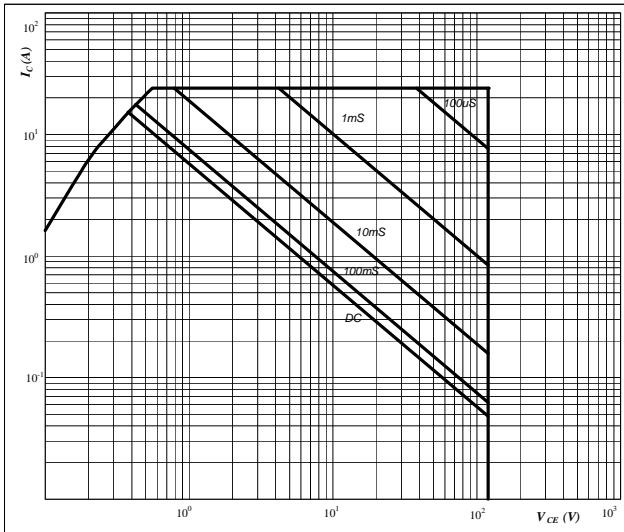
$$T_j = 175 \quad ^\circ\text{C}$$

## Output Inverter

**Figure 25**

**Safe operating area as a function of collector-emitter voltage**

$$I_C = f(V_{CE})$$


**At**

D = single pulse

T<sub>h</sub> = 80 °C

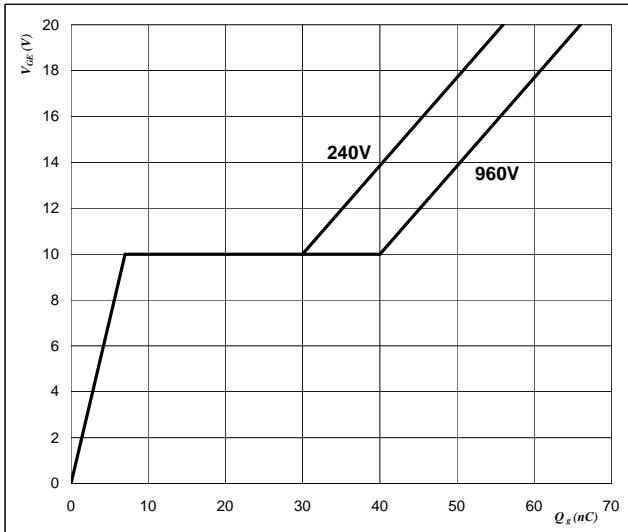
V<sub>GE</sub> = ±15 V

T<sub>j</sub> = T<sub>jmax</sub> °C

**Output inverter IGBT**
**Figure 26**

**Gate voltage vs Gate charge**

$$V_{GE} = f(Q_{GE})$$

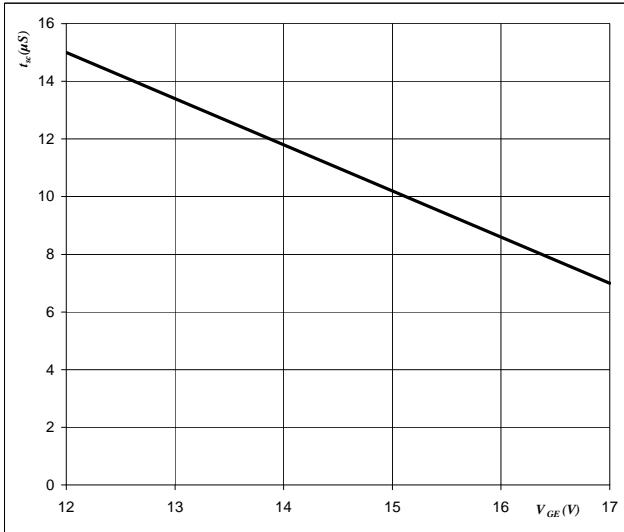

**At**

I<sub>C</sub> = 8 A

**Figure 27**
**Output inverter IGBT**

**Short circuit withstand time as a function of gate-emitter voltage**

$$t_{sc} = f(V_{GE})$$


**At**

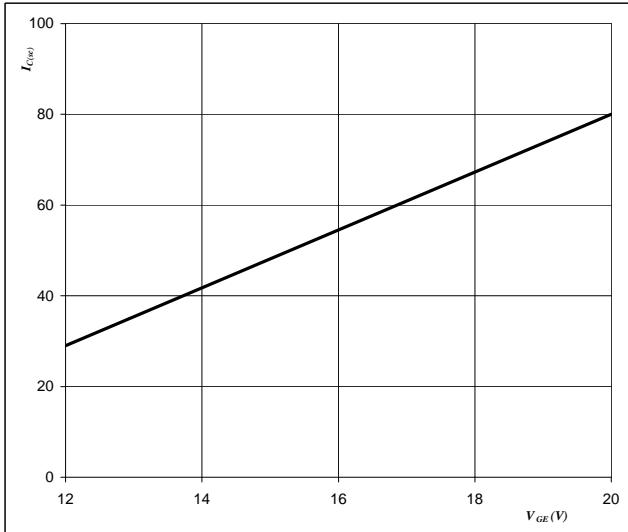
V<sub>CE</sub> = 1200 V

T<sub>j</sub> ≤ 175 °C

**Figure 28**
**Output inverter IGBT**

**Typical short circuit collector current as a function of gate-emitter voltage**

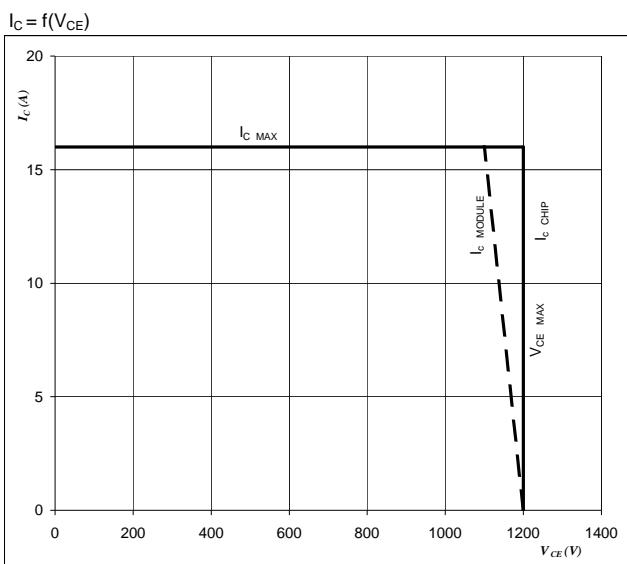
$$V_{GE} = f(I_{CSC})$$


**At**

V<sub>CE</sub> ≤ 1200 V

T<sub>j</sub> = 175 °C

**Figure 29**  
**Reverse bias safe operating area**



**At**

$T_j = T_{jmax} - 25 \quad ^\circ\text{C}$

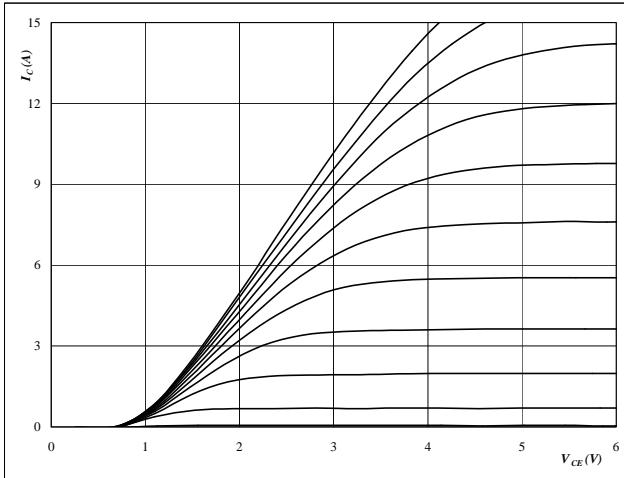
$U_{ccminus} = U_{ccplus}$

Switching mode : 3phase SPWM

## Brake

**Figure 1**
**Typical output characteristics**

$$I_C = f(V_{CE})$$


**At**

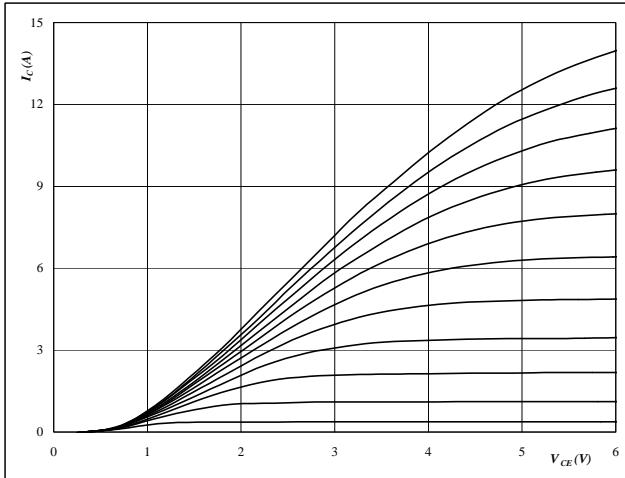
$$t_p = 250 \text{ } \check{\text{s}}$$

$$T_j = 25 \text{ } ^\circ\text{C}$$

 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Brake IGBT**
**Figure 2**
**Typical output characteristics**

$$I_C = f(V_{CE})$$


**At**

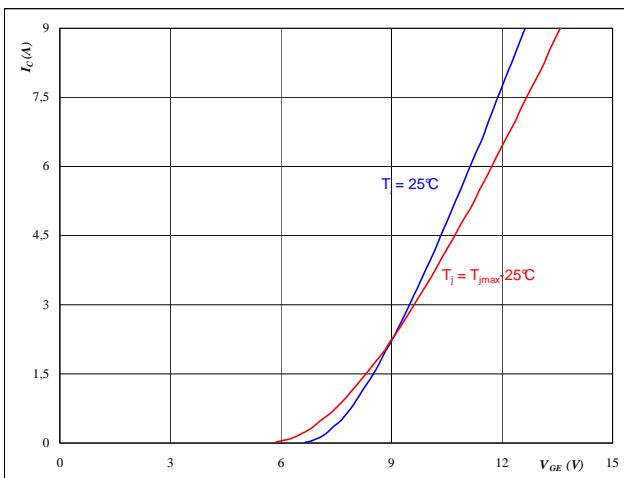
$$t_p = 250 \text{ } \check{\text{s}}$$

$$T_j = 150 \text{ } ^\circ\text{C}$$

 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**
**Brake IGBT**
**Typical transfer characteristics**

$$I_C = f(V_{GE})$$

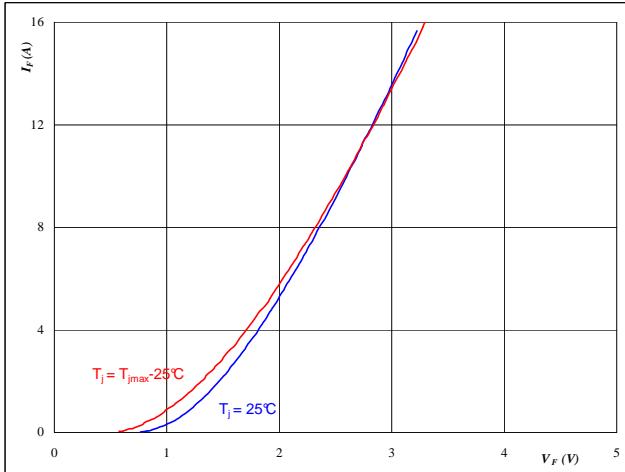

**At**

$$t_p = 250 \text{ } \check{\text{s}}$$

$$V_{CE} = 10 \text{ } \text{V}$$

**Figure 4**
**Brake FWD**
**Typical diode forward current as**
**a function of forward voltage**

$$I_F = f(V_F)$$


**At**

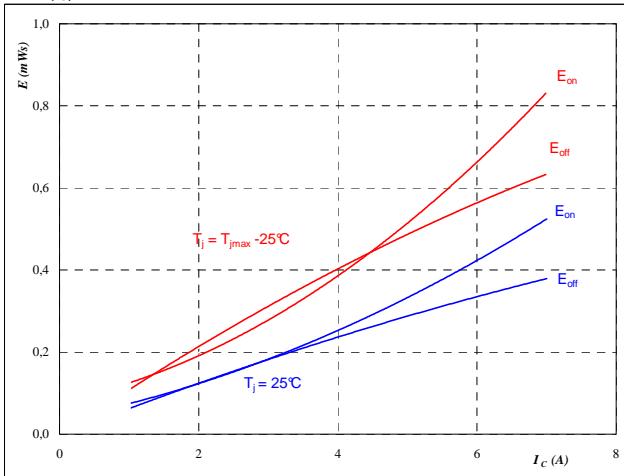
$$t_p = 250 \text{ } \check{\text{s}}$$

## Brake

**Figure 5**

Typical switching energy losses  
as a function of collector current

$$E = f(I_C)$$



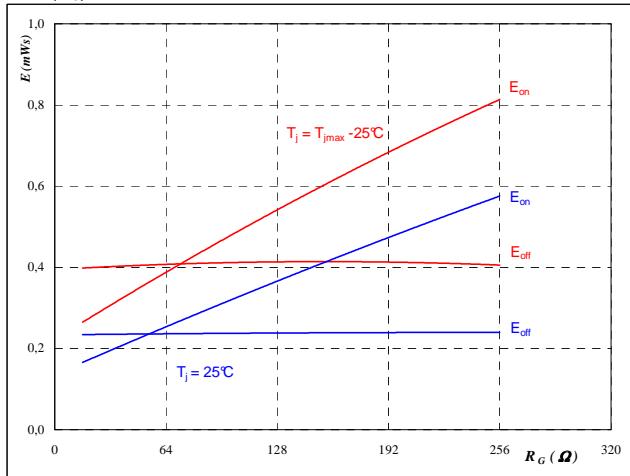
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \\ R_{goff} &= 64 \quad \Omega \end{aligned}$$

**Brake IGBT**
**Figure 6**

Typical switching energy losses  
as a function of gate resistor

$$E = f(R_G)$$



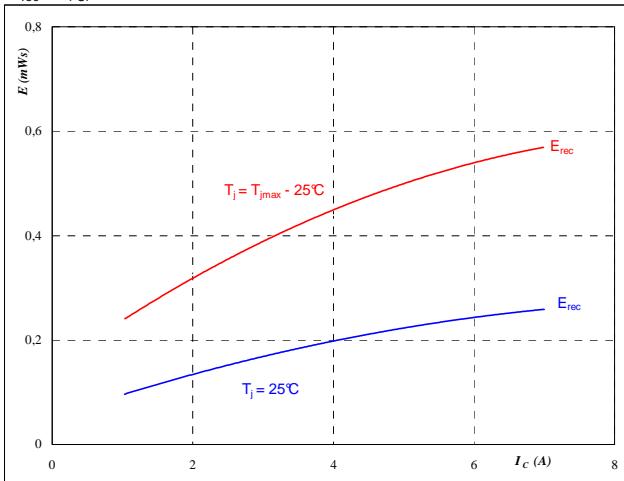
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 4 \quad \text{A} \end{aligned}$$

**Figure 7**

Typical reverse recovery energy loss  
as a function of collector current

$$E_{rec} = f(I_C)$$



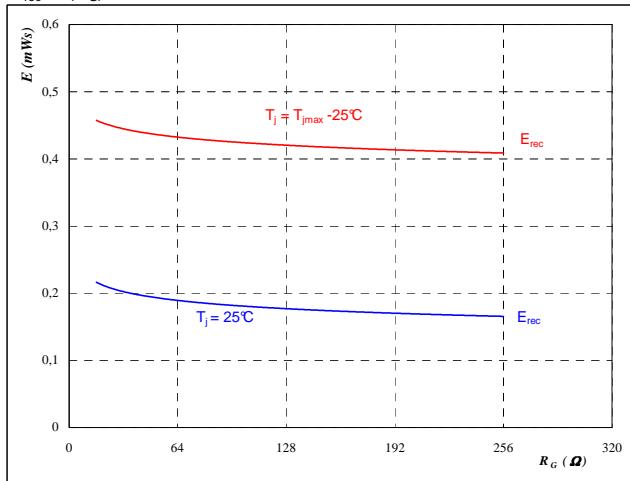
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \end{aligned}$$

**Brake FWD**
**Figure 8**

Typical reverse recovery energy loss  
as a function of gate resistor

$$E_{rec} = f(R_G)$$



With an inductive load at

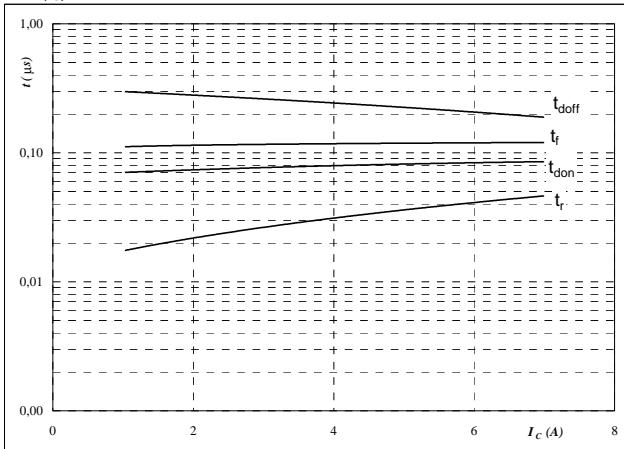
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 4 \quad \text{A} \end{aligned}$$

## Brake

**Figure 9**

**Typical switching times as a function of collector current**

$$t = f(I_C)$$



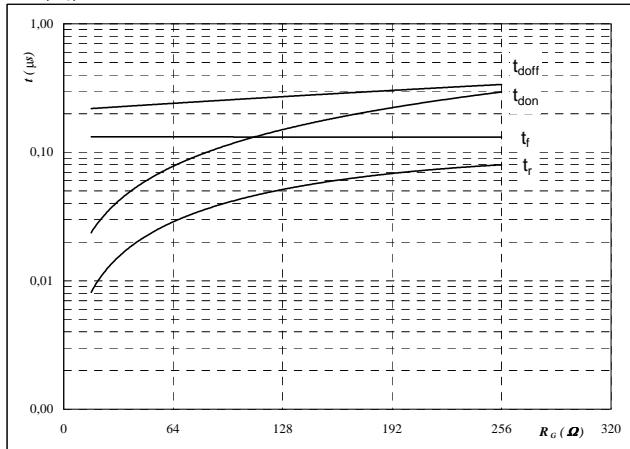
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
R <sub>gon</sub> =	64	Ω
R <sub>goff</sub> =	64	Ω

**Brake IGBT**
**Figure 10**

**Typical switching times as a function of gate resistor**

$$t = f(R_G)$$



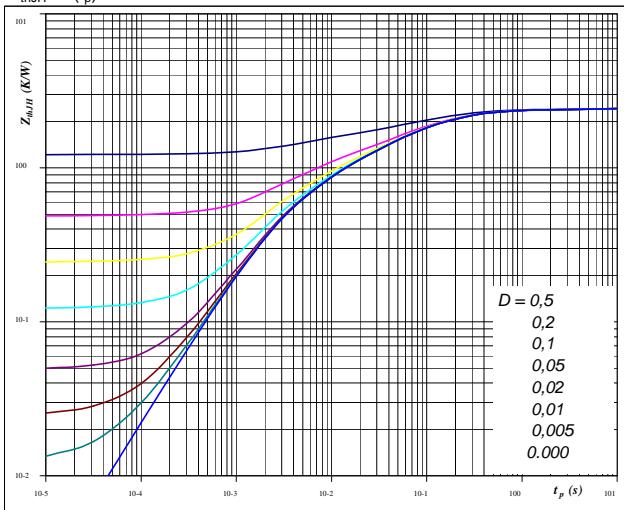
With an inductive load at

T <sub>j</sub> =	150	°C
V <sub>CE</sub> =	600	V
V <sub>GE</sub> =	±15	V
I <sub>C</sub> =	4	A

**Figure 11**
**Brake IGBT**

**IGBT transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$

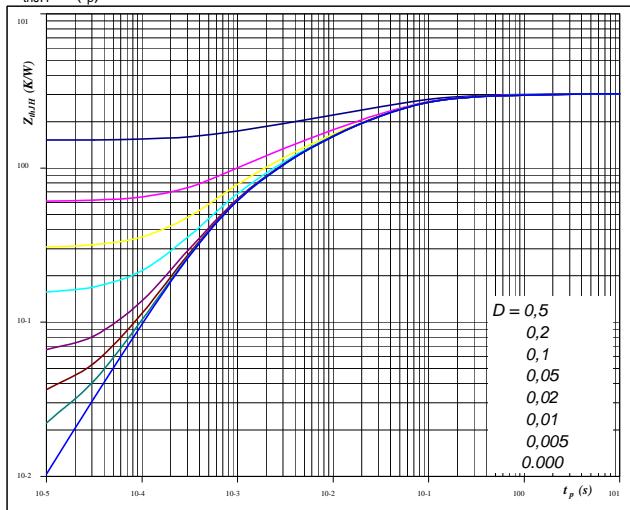


<b>At</b>	<b>D =</b>	<b>tp / T</b>
Thermal grease		Phase change material
R <sub>thJH</sub> =	2,436	K/W

**Figure 12**
**Brake FWD**

**FWD transient thermal impedance as a function of pulse width**

$$Z_{thJH} = f(t_p)$$



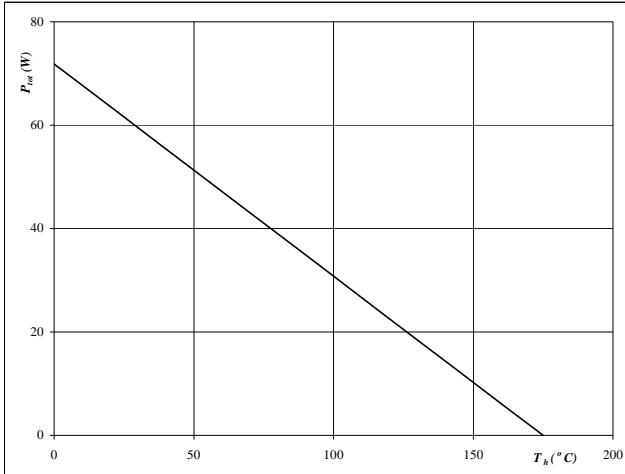
<b>At</b>	<b>D =</b>	<b>tp / T</b>
Thermal grease		Phase change material
R <sub>thJH</sub> =	3,03	K/W

## Brake

**Figure 13**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

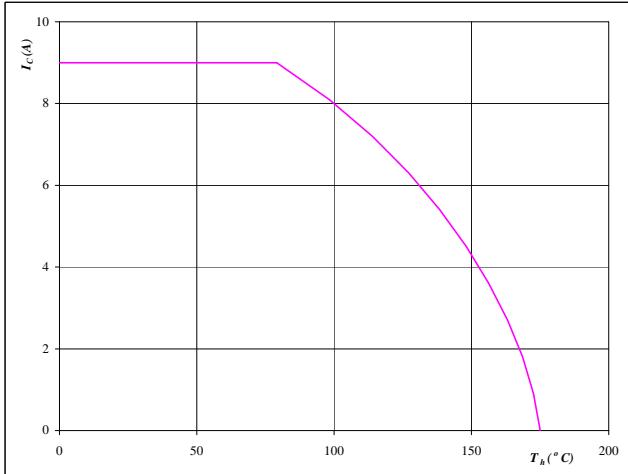

**At**

$$T_j = 175 \quad {}^\circ\text{C}$$

**Brake IGBT**
**Figure 14**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

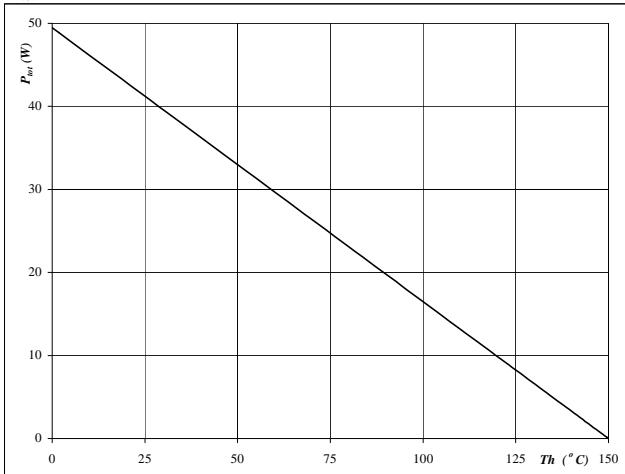
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 15**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

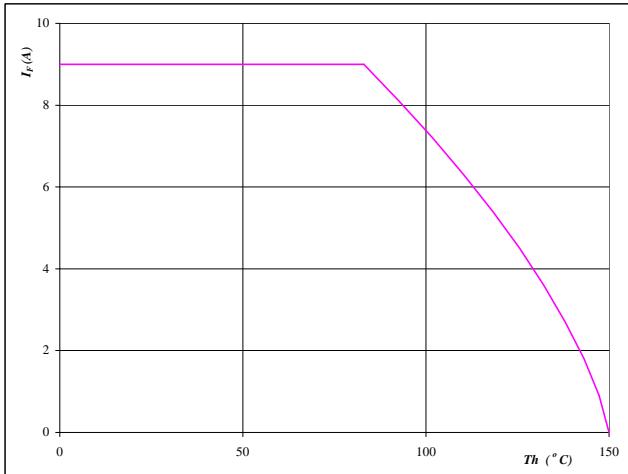

**At**

$$T_j = 150 \quad {}^\circ\text{C}$$

**Brake FWD**
**Figure 16**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

$$T_j = 150 \quad {}^\circ\text{C}$$

**Brake FWD**

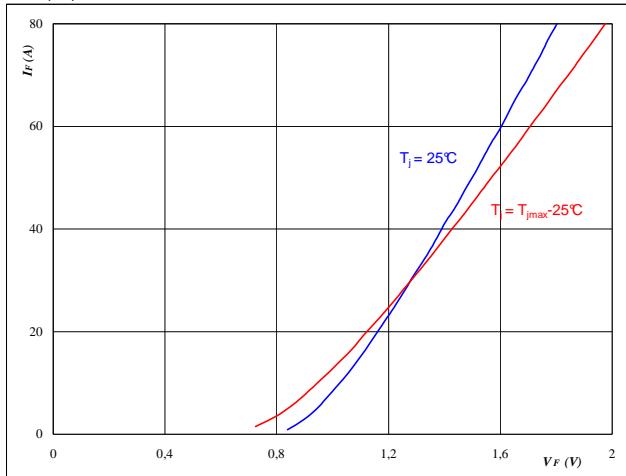
## Input Rectifier Bridge

**Figure 1**

Rectifier diode

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$


**At**

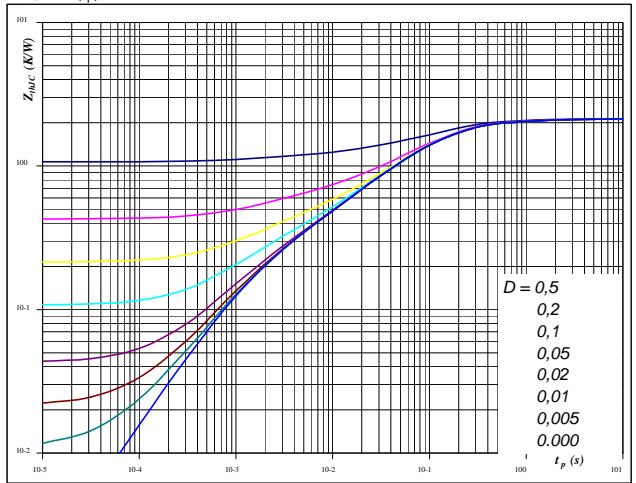
$$t_p = 250 \text{ } \mu\text{s}$$

Rectifier diode

**Figure 2**

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$


**At**

$$D = t_p / T$$

$$R_{thJH} = 2,10 \text{ K/W}$$

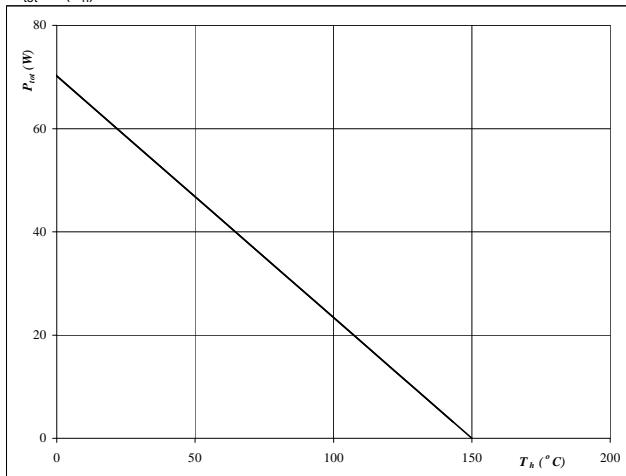
Rectifier diode

**Figure 3**

Rectifier diode

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$


**At**

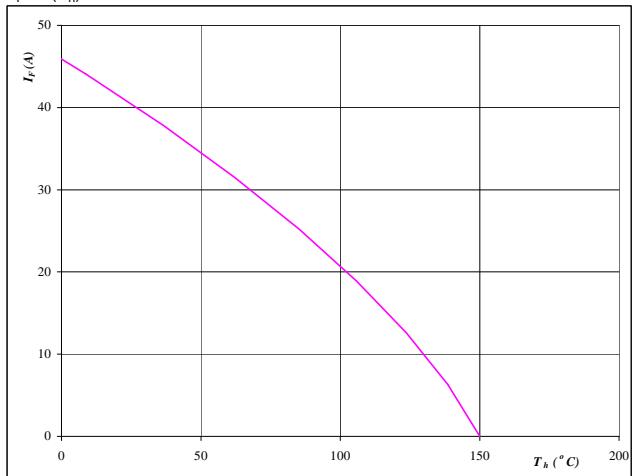
$$T_j = 150 \text{ } ^\circ C$$

Rectifier diode

**Figure 4**

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


**At**

$$T_j = 150 \text{ } ^\circ C$$

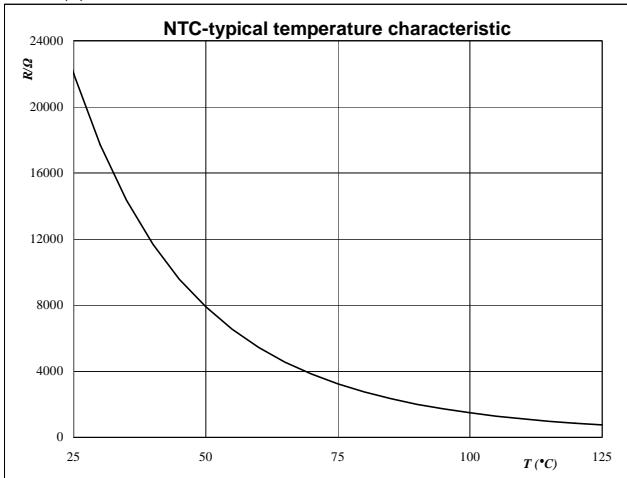
## Thermistor

**Figure 1**

Thermistor

Typical NTC characteristic  
as a function of temperature

$$R_T = f(T)$$



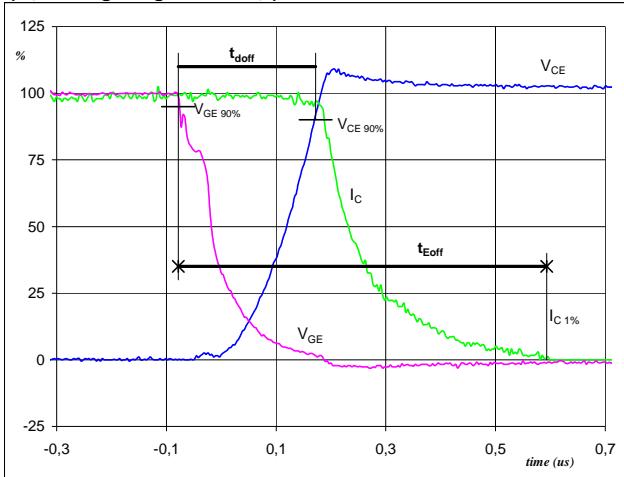
## Switching Definitions Output Inverter

**General conditions**

$T_j$	=	150 °C
$R_{gon}$	=	32 Ω
$R_{goff}$	=	32 Ω

**Figure 1**

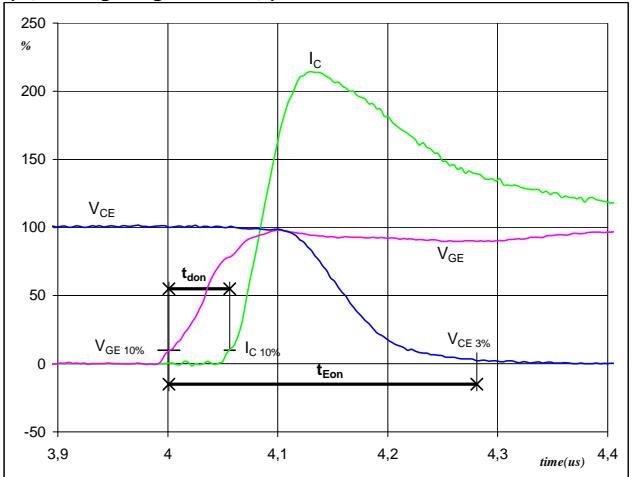
Output inverter IGBT  
Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$   
( $t_{Eoff}$  = integrating time for  $E_{off}$ )



$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 8$  A  
 $t_{doff} = 0,24$  Šs  
 $t_{Eoff} = 0,67$  Šs

**Figure 2**

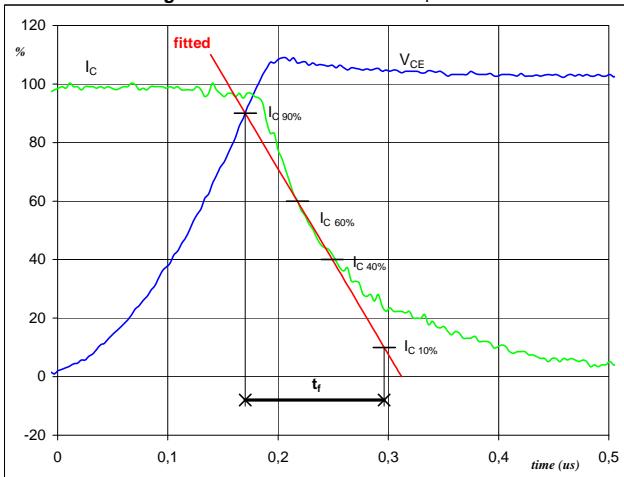
Output inverter IGBT  
Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$   
( $t_{Eon}$  = integrating time for  $E_{on}$ )



$V_{GE}(0\%) = -15$  V  
 $V_{GE}(100\%) = 15$  V  
 $V_C(100\%) = 600$  V  
 $I_C(100\%) = 8$  A  
 $t_{don} = 0,06$  Šs  
 $t_{Eon} = 0,28$  Šs

**Figure 3**

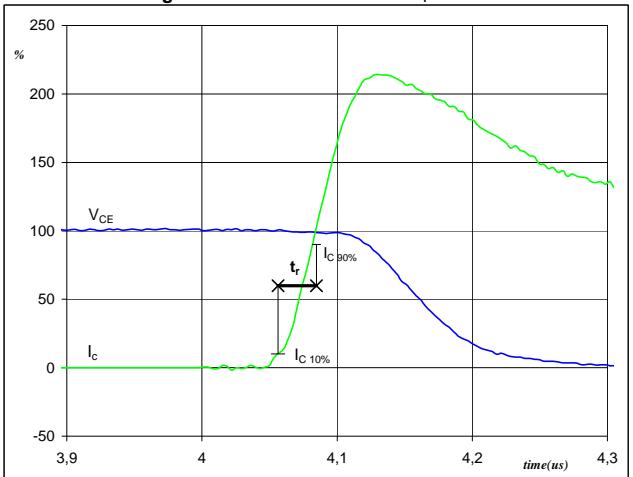
Output inverter IGBT  
Turn-off Switching Waveforms & definition of  $t_f$



$V_C(100\%) = 600$  V  
 $I_C(100\%) = 8$  A  
 $t_f = 0,14$  Šs

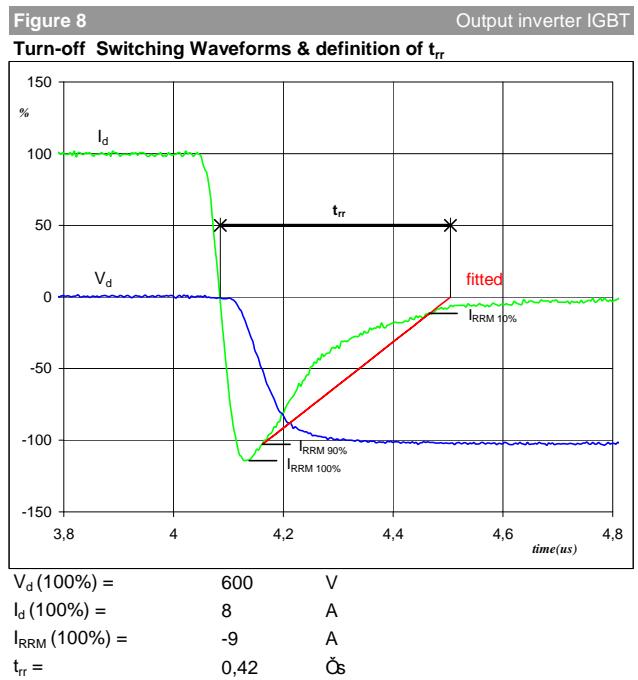
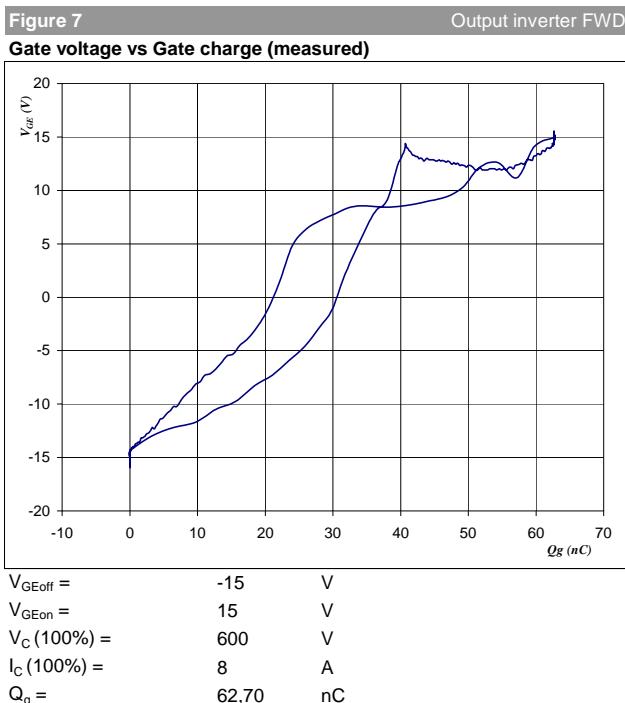
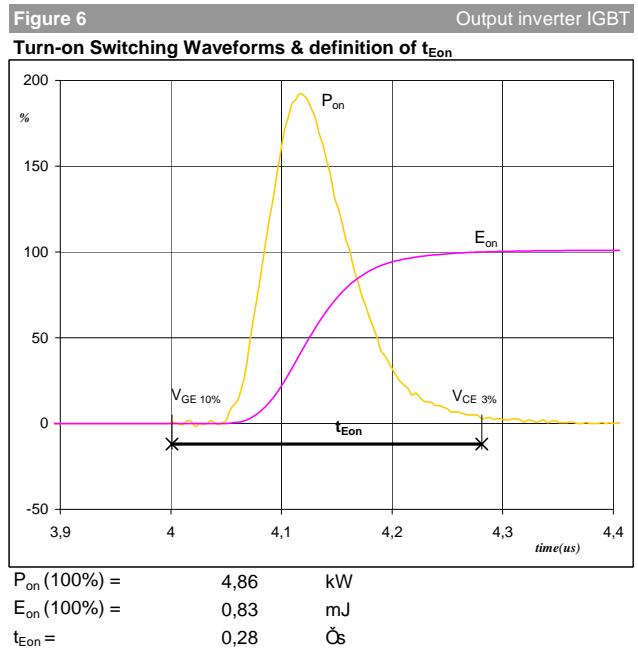
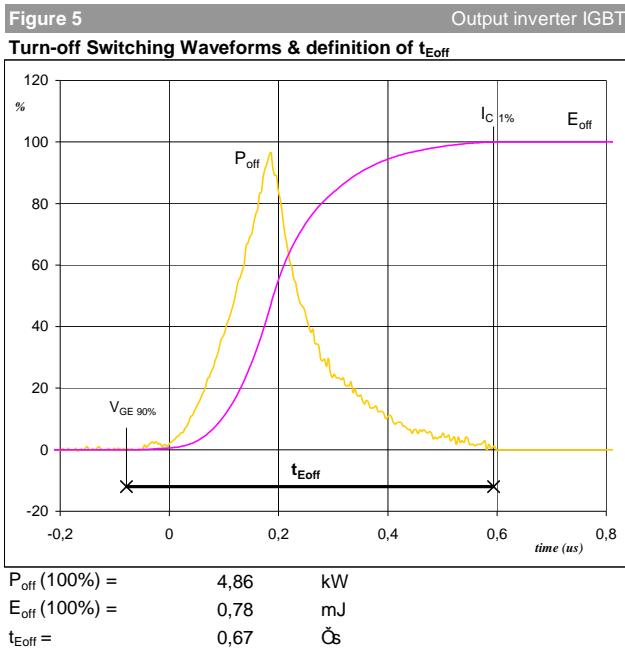
**Figure 4**

Output inverter IGBT  
Turn-on Switching Waveforms & definition of  $t_r$



$V_C(100\%) = 600$  V  
 $I_C(100\%) = 8$  A  
 $t_r = 0,02$  Šs

## Switching Definitions Output Inverter

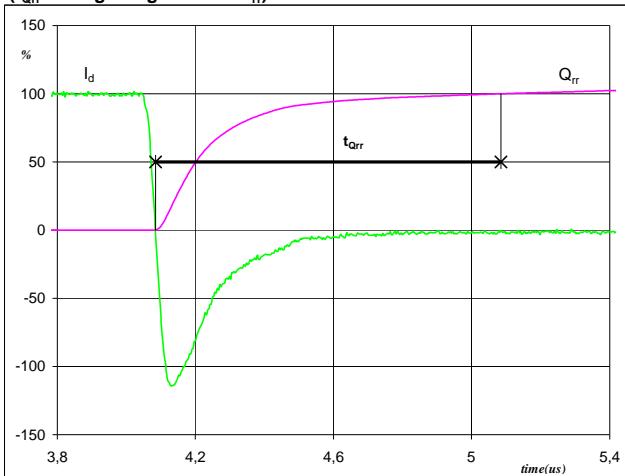


## Switching Definitions Output Inverter

**Figure 9**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Qrr}$**   
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

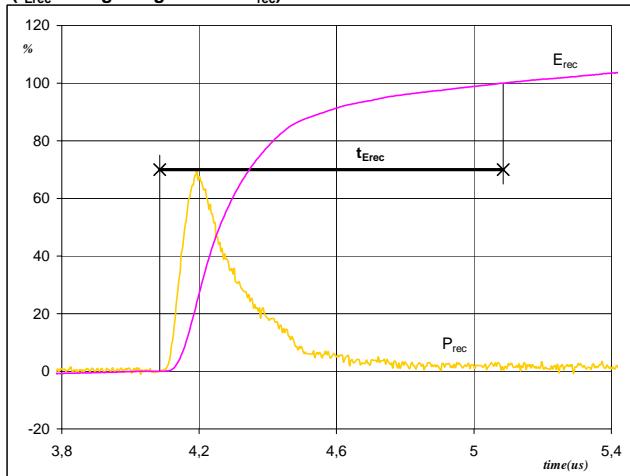


$I_d(100\%) = 8 \text{ A}$   
 $Q_{rr}(100\%) = 1,66 \text{ C}$   
 $t_{Qrr} = 1,00 \text{ s}$

**Figure 10**

Output inverter FWD

**Turn-on Switching Waveforms & definition of  $t_{Erec}$**   
 $(t_{Erec} = \text{integrating time for } E_{rec})$



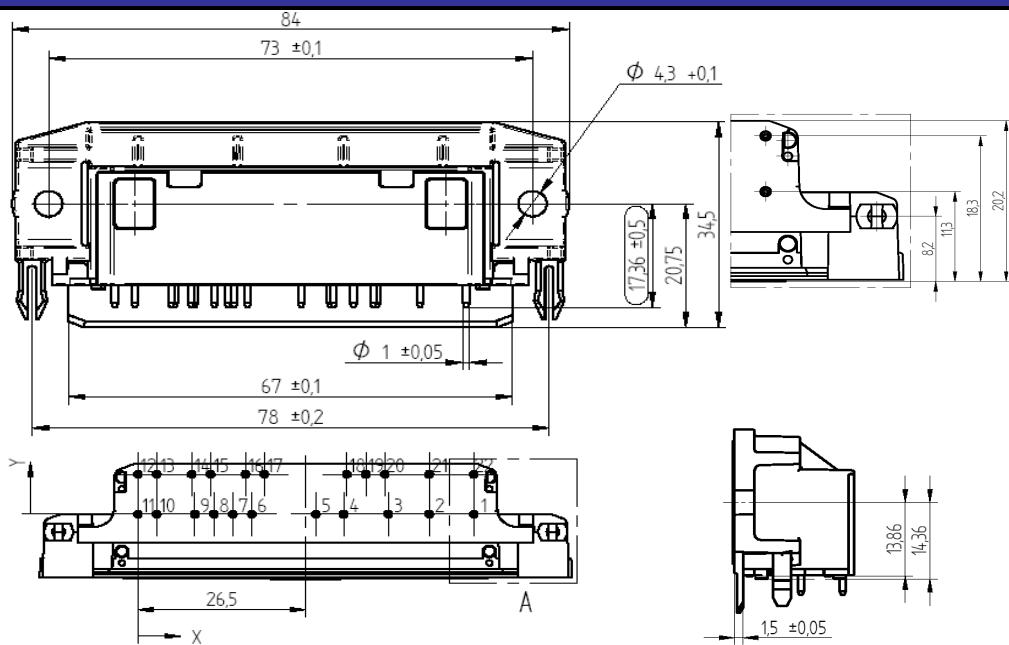
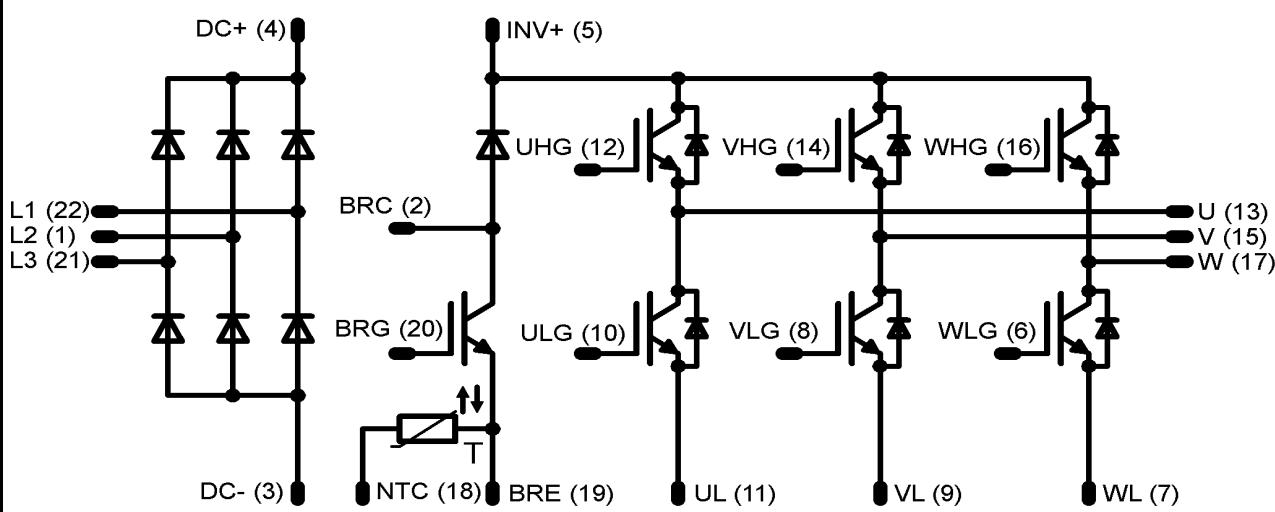
$P_{rec}(100\%) = 4,86 \text{ kW}$   
 $E_{rec}(100\%) = 0,66 \text{ mJ}$   
 $t_{Erec} = 1,00 \text{ s}$

**Ordering Code and Marking - Outline - Pinout**
**Ordering Code & Marking**

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste	V23990-P639-A40-PM	P639-A40	P639-A40

**Outline**

Pin table		
Pin	X	Y
1	53	0
2	46	0
3	39,5	0
4	32,5	0
5	28,1	0
6	18	0
7	15	0
8	12	0
9	9	0
10	3	0
11	0	0
12	0	7
13	3	7
14	8,5	7
15	11,5	7
16	17	7
17	20	7
18	33	7
19	36	7
20	39	7
21	46	7
22	53	7


**Pinout**


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Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.