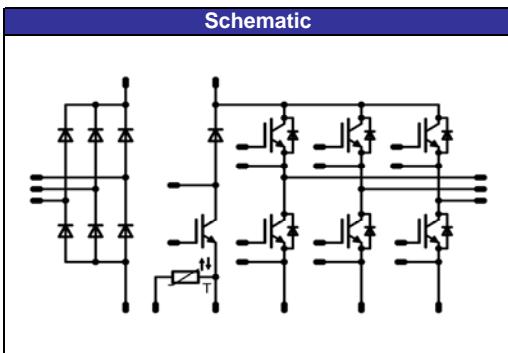


flow90PIM 1		1200V/4A
Features		
<ul style="list-style-type: none"> Trench Fieldstop Technology IGBT4 for low saturation loss Supports design with 90° mounting angle between heatsink and PCB Clip-in PCB mounting Clip or screw on heatsink mounting 		
Target Applications	<ul style="list-style-type: none"> Industrial drives 	
Types	<ul style="list-style-type: none"> V23990-P638-A40-PM 	
Schematic		

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Input Rectifier Diode				
Repetitive peak reverse voltage	V_{RRM}		1600	V
DC forward current	I_{FAV}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	28 36	A
Surge forward current	I_{FSM}		200	A
I^2t -value	I^2t	$t_p=10\text{ms}$ $T_j=25^\circ\text{C}$	200	A^2s
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 50	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$
Inverter Transistor				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	9 9	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$	12	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{j\max}$	12	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	39 60	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Inverter FWD

Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
DC forward current	I_F	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	18 23	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\text{max}$	20	A
Power dissipation per Diode	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	42 63	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Brake Transistor

Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	9 9	A
Repetitive peak collector current	I_{CPuls}	t_p limited by $T_j\text{max}$	12	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{Op\text{ max}}$	12	A
Power dissipation per IGBT	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	39 59	W
Gate-emitter peak voltage	V_{GE}		±20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Brake FWD

Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
DC forward current	I_F	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	7 7	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\text{max}$	6	A
Power dissipation per Diode	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	31 38	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{op}		-40...+($T_{j\text{max}} - 25$)	°C

Insulation Properties

Insulation voltage	V_{is}	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	

Characteristic Values

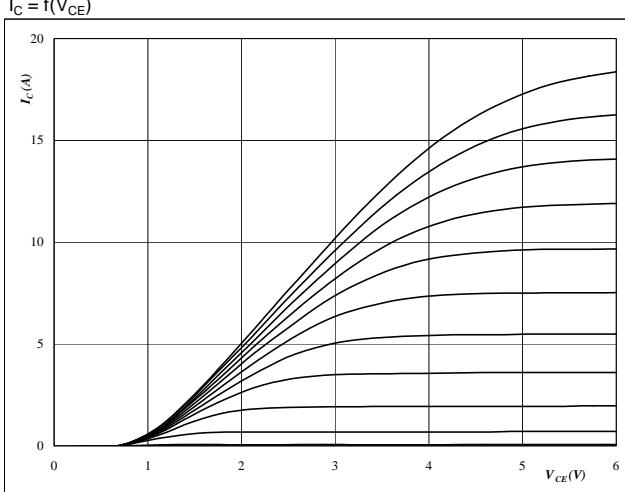
Parameter	Symbol	Conditions					Value			Unit
			V _{GE} [V] or V _{GS} [V]	V _I [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _J	Min	Typ	Max	
Input Rectifier Diode										
Forward voltage	V _F				30	T _J =25°C T _J =125°C	1	1,26 1,24	1,6	V
Threshold voltage (for power loss calc. only)	V _{to}				30	T _J =25°C T _J =125°C		0,92 0,82		V
Slope resistance (for power loss calc. only)	r _t				30	T _J =25°C T _J =125°C		11 14		mΩ
Reverse current	I _r			1500		T _J =25°C T _J =125°C			0,2	mA
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,10		K/W
Inverter Transistor										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,00015	T _J =25°C T _J =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		4	T _J =25°C T _J =150°C	1,6	1,87 2,20	2,3	V
Collector-emitter cut-off current incl. Diode	I _{CES}		0	1200		T _J =25°C T _J =150°C			0,0005	mA
Gate-emitter leakage current	I _{GES}		20	0		T _J =25°C T _J =150°C			120	nA
Integrated Gate resistor	R _{gint}							none		Ω
Turn-on delay time	t _{d(on)}	R _{goff} =64 Ω R _{gon} =64 Ω	±15	600	4	T _J =25°C T _J =150°C		83 77		ns
Rise time	t _r					T _J =25°C T _J =150°C		26 28		
Turn-off delay time	t _{d(off)}					T _J =25°C T _J =150°C		191 254		
Fall time	t _f					T _J =25°C T _J =150°C		77 122		
Turn-on energy loss per pulse	E _{on}					T _J =25°C T _J =150°C		0,36 0,63		mWs
Turn-off energy loss per pulse	E _{off}					T _J =25°C T _J =150°C		0,23 0,39		
Input capacitance	C _{ies}	f=1MHz	0	25		T _J =25°C		250		pF
Output capacitance	C _{oss}							25		
Reverse transfer capacitance	C _{rss}							15		
Gate charge	Q _{Gate}		±15	960	4	T _J =25°C		28		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,42		K/W
Inverter FWD										
Diode forward voltage	V _F				4	T _J =25°C T _J =150°C	1,2	1,43 1,23	2,3	V
Peak reverse recovery current	I _{RRM}	R _{gon} =64 Ω	±15	600	4	T _J =25°C T _J =150°C		4 6		A
Reverse recovery time	t _{rr}					T _J =25°C T _J =150°C		246 426		ns
Reverse recovered charge	Q _{rr}					T _J =25°C T _J =150°C		0,54 1,20		μC
Peak rate of fall of recovery current	di(rec)max /dt					T _J =25°C T _J =150°C		65 42		A/μs
Reverse recovered energy	E _{rec}					T _J =25°C T _J =150°C		0,19 0,43		mWs
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,28		K/W

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
		V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max		
Brake Transistor										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,00015	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		4	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,6	1,87 2,21	2,3	V
Collector-emitter cut-off incl diode	I_{CES}		0	1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0,0005	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			120	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=64 \Omega$ $R_{gon}=64 \Omega$	± 15	600	4	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		83 79		ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		28 32		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		178 243		
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		77 132		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,26 0,39		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,24 0,41		
Input capacitance	C_{ies}							250		
Output capacitance	C_{oss}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		25		pF
Reverse transfer capacitance	C_{rss}							15		
Gate charge	Q_{Gate}							28		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						2,44		K/W
Brake FWD										
Diode forward voltage	V_F				3	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1	1,67 1,74	2,3	V
Reverse leakage current	I_r			1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			250	mA
Peak reverse recovery current	I_{RRM}	$R_{gon}=64 \Omega$ $R_{goff}=64 \Omega$	± 15	600	4	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		2,77 3,62		A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		357 649		ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,44 0,44		μC
Peak rate of fall of recovery current	$d(i/\text{rec})/\text{max dt}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		18 14		$\text{A}/\mu\text{s}$
Reverse recovery energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		0,20 0,44		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						3,03		K/W
Thermistor										
Rated resistance	R					$T=25^\circ\text{C}$		22000		Ω
Deviation of R100	$\Delta R/R$	$R100=1486 \Omega$				$T=100^\circ\text{C}$	-5		5	%
Power dissipation	P					$T=25^\circ\text{C}$		200		mW
Power dissipation constant						$T=25^\circ\text{C}$		2		mW/K
B-value	$B_{(25/50)}$	Tol. ±3%				$T=25^\circ\text{C}$		3950		K
B-value	$B_{(25/100)}$	Tol. ±3%				$T=25^\circ\text{C}$		3996		K
Vincotech NTC Reference									B	

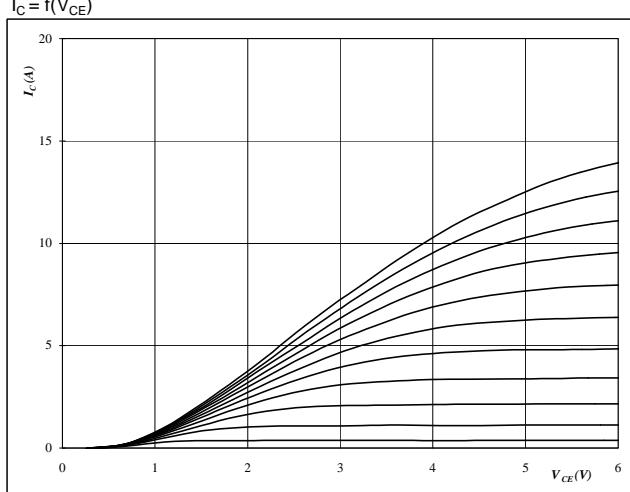
Output Inverter

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



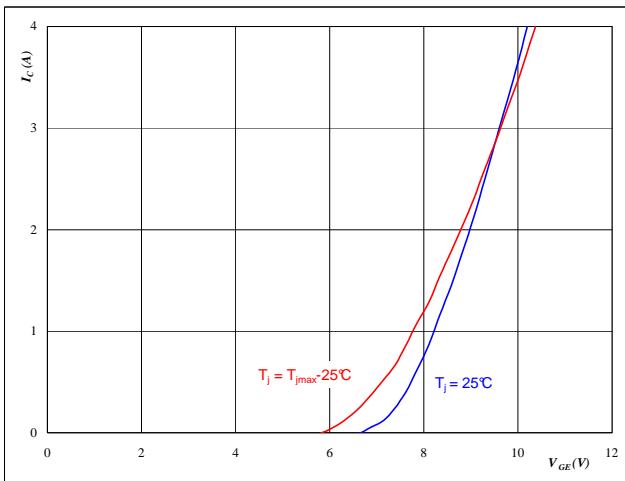
At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $T_j = 25 \text{ } ^\circ\text{C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



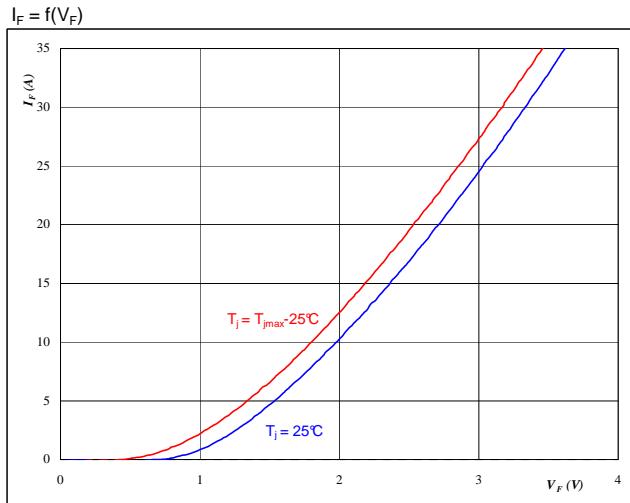
At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $T_j = 150 \text{ } ^\circ\text{C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$



At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $V_{CE} = 10 \text{ } \text{V}$

Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



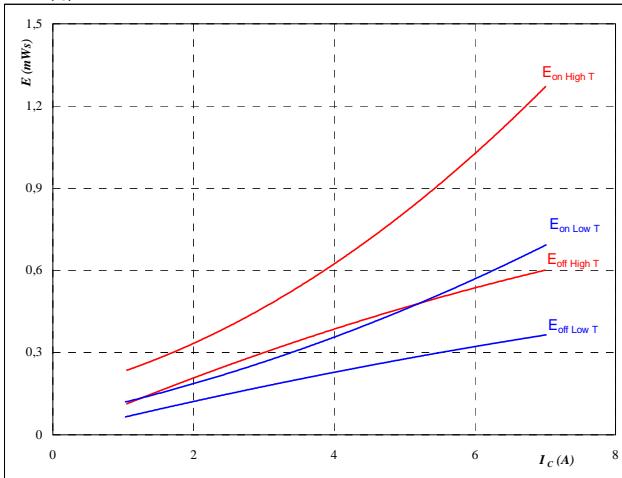
At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$

Output Inverter

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



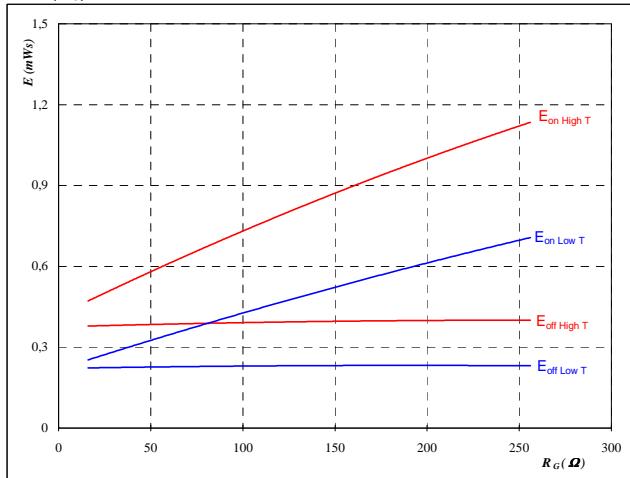
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \\ R_{goff} &= 64 \quad \Omega \end{aligned}$$

Output inverter IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



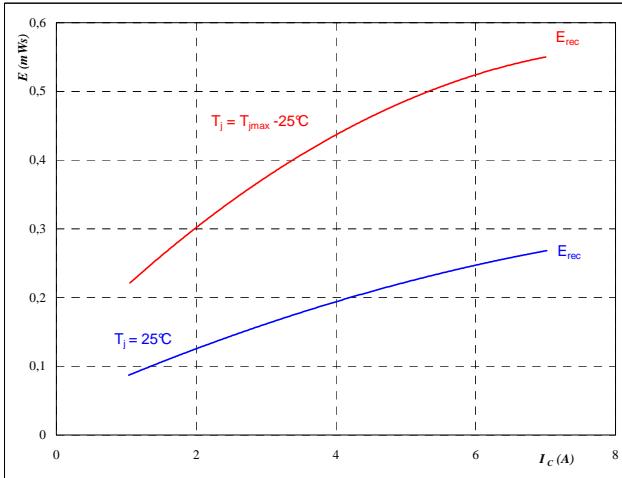
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 4 \quad \text{A} \\ \end{aligned}$$

Figure 7
Output inverter FWD

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



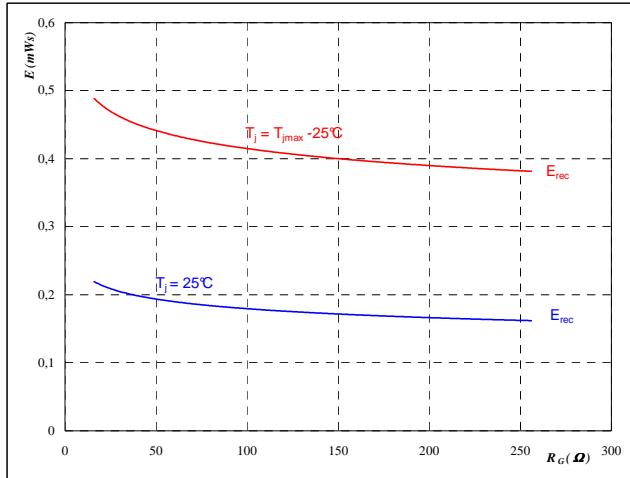
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \\ \end{aligned}$$

Figure 8
Output inverter FWD

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

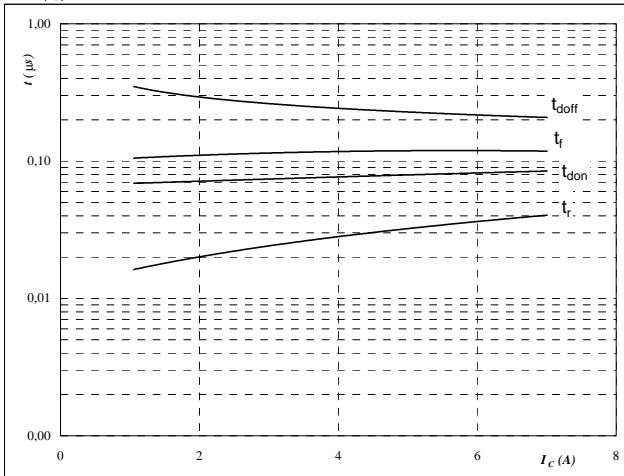
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 4 \quad \text{A} \\ \end{aligned}$$

Output Inverter

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



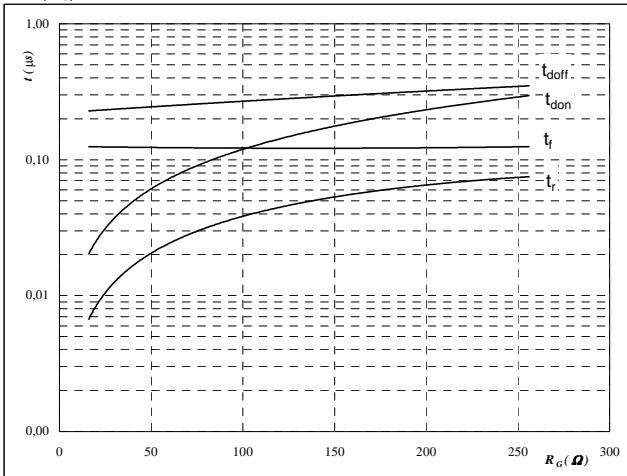
With an inductive load at

T _j =	150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
R _{gon} =	64	Ω
R _{goff} =	64	Ω

Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



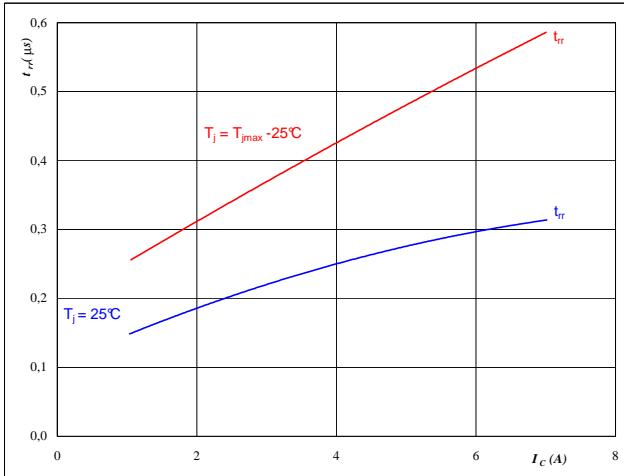
With an inductive load at

T _j =	150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
I _C =	4	A
R _{goff} =	64	Ω

Figure 11
Output inverter FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



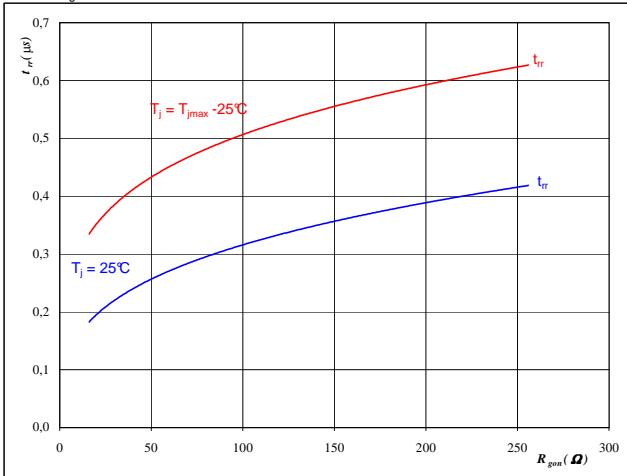
At

T _j =	25/150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
R _{gon} =	64	Ω

Figure 12
Output inverter FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

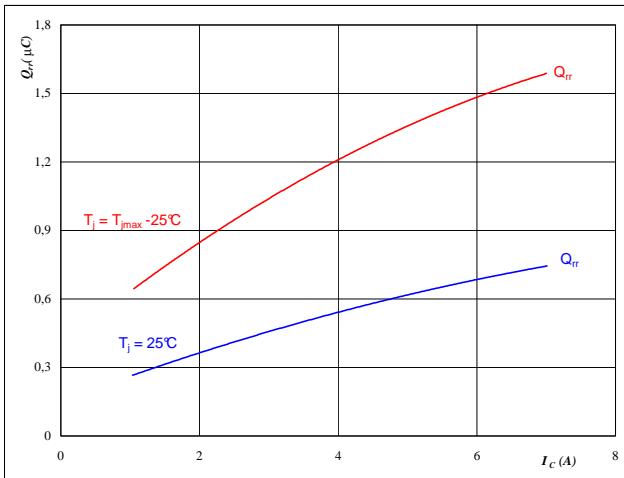
T _j =	25/150	°C
V _R =	600	V
I _F =	4	A
V _{GE} =	±15	V

Output Inverter

Figure 13

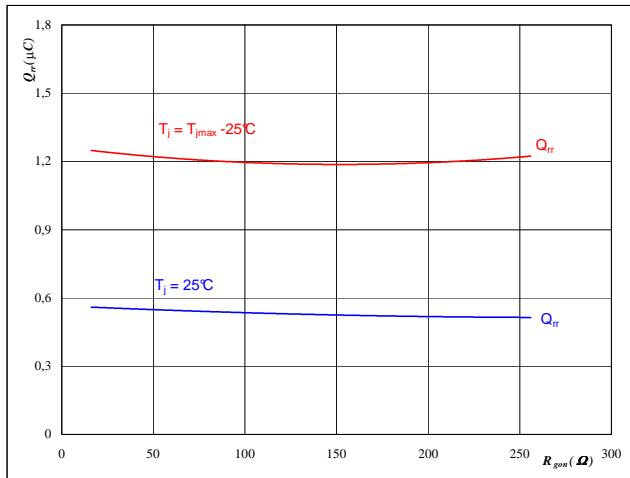
Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


Figure 14

Typical reverse recovery charge as a function of IGBT turn on gate resistor

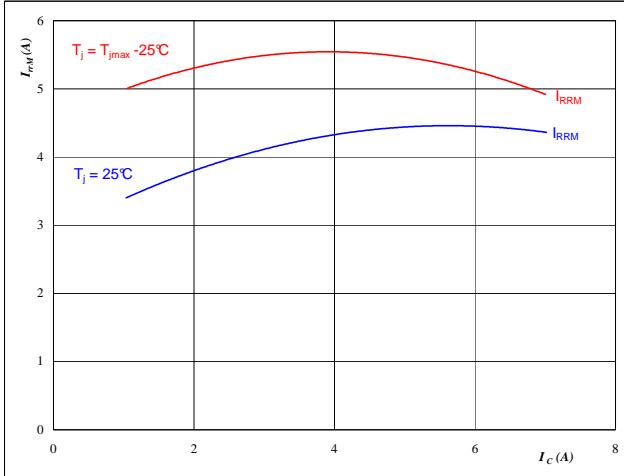
$$Q_{rr} = f(R_{gon})$$


Figure 15

Output inverter FWD

Typical reverse recovery current as a function of collector current

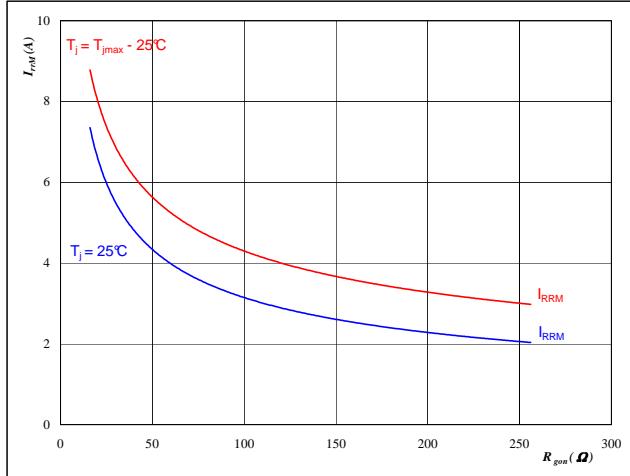
$$I_{RRM} = f(I_C)$$


Figure 16

Output inverter FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

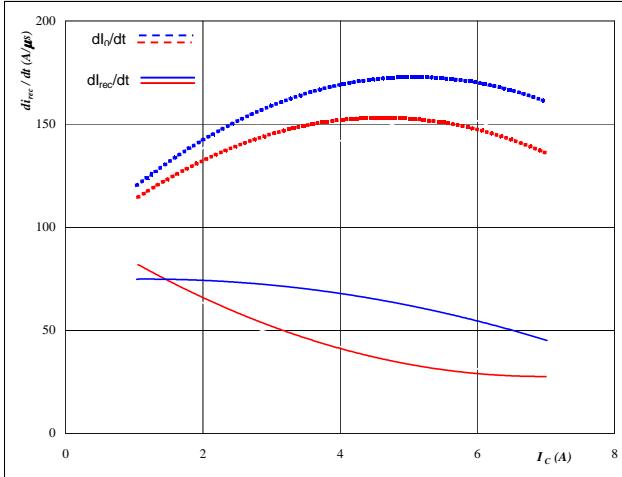
$$I_{RRM} = f(R_{gon})$$



Output Inverter

Figure 17

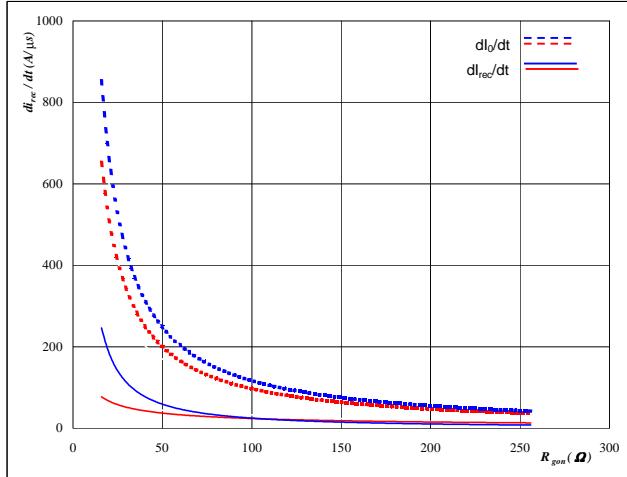
Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dl_0/dt, dl_{rec}/dt = f(I_C)$


At

$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 64 \Omega$

Output inverter FWD
Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dl_0/dt, dl_{rec}/dt = f(R_{gon})$

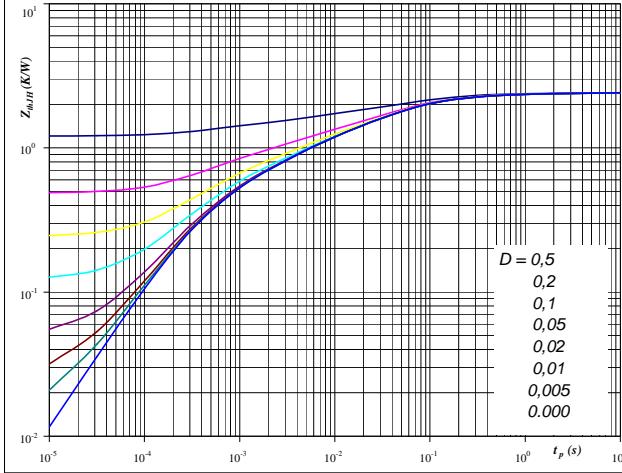

At

$T_j = 25/150 \text{ } ^\circ\text{C}$
 $V_R = 600 \text{ V}$
 $I_F = 4 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Figure 19

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$

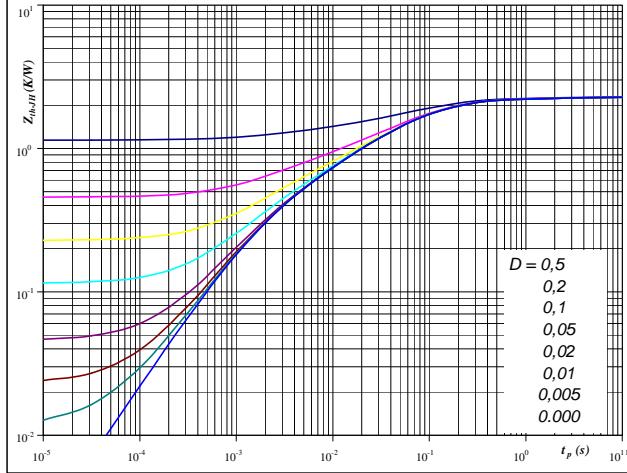

At

$D = t_p / T$
 $R_{thJH} = 2,42 \text{ K/W}$

Output inverter IGBT
Figure 20

FWD transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$D = t_p / T$
 $R_{thJH} = 2,28 \text{ K/W}$

IGBT thermal model values

Thermal grease		Phase change material	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,09	2,4E+00	0,07	2,0E+00
0,34	2,1E-01	0,27	1,7E-01
0,75	4,8E-02	0,61	3,9E-02
0,41	1,3E-02	0,33	1,1E-02
0,46	2,8E-03	0,37	2,2E-03
0,37	3,9E-04	0,30	3,1E-04

FWD thermal model values

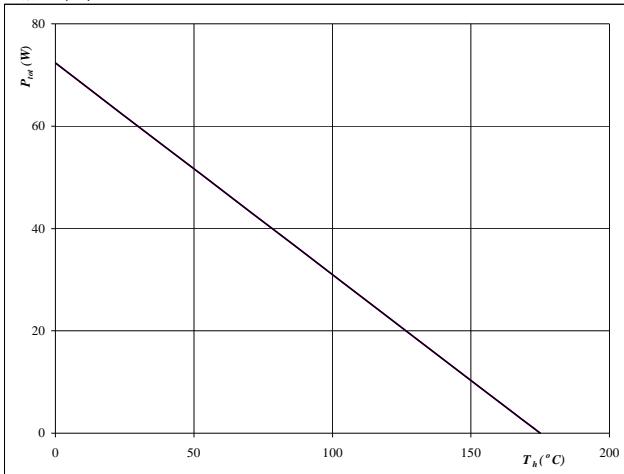
Thermal grease		Phase change material	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,05	5,6E+00	0,04	4,5E+00
0,14	7,2E-01	0,12	5,8E-01
0,79	1,2E-01	0,64	9,6E-02
0,71	3,6E-02	0,57	2,9E-02
0,37	7,3E-03	0,30	5,9E-03
0,22	1,5E-03	0,18	1,2E-03

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

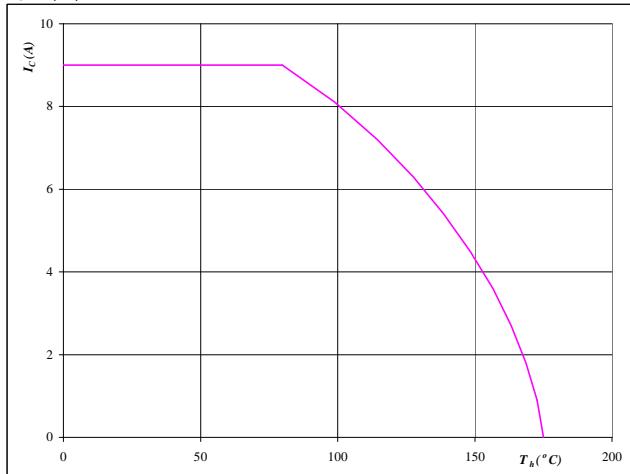

At

$$T_j = 175 \quad ^\circ\text{C}$$

Output inverter IGBT
Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$

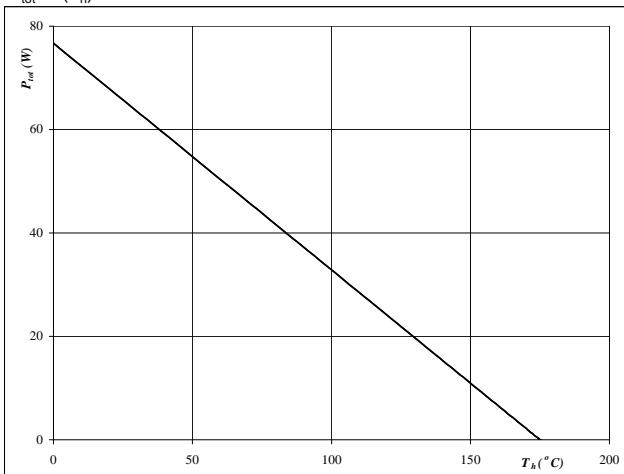

At

$$T_j = 175 \quad ^\circ\text{C}$$

Output inverter IGBT
Figure 23
Output inverter FWD

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

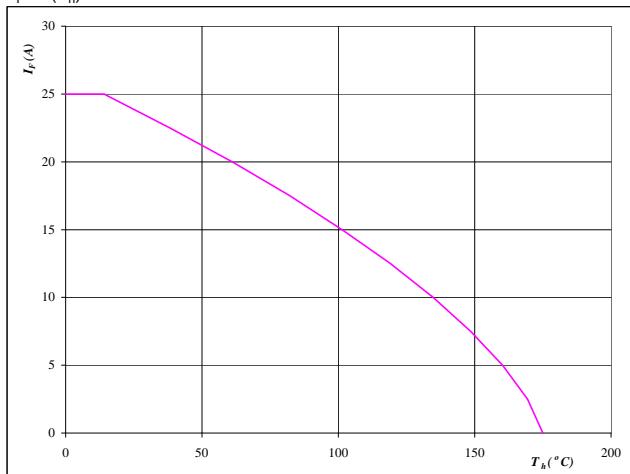

At

$$T_j = 175 \quad ^\circ\text{C}$$

Figure 24
Output inverter FWD

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

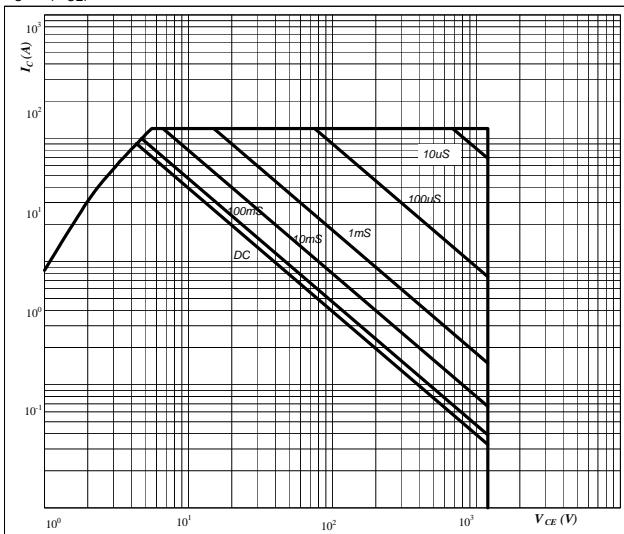
$$T_j = 175 \quad ^\circ\text{C}$$

Output Inverter

Figure 25

Safe operating area as a function of collector-emitter voltage

$$I_C = f(V_{CE})$$


At

D = single pulse

T_h = 80 °C

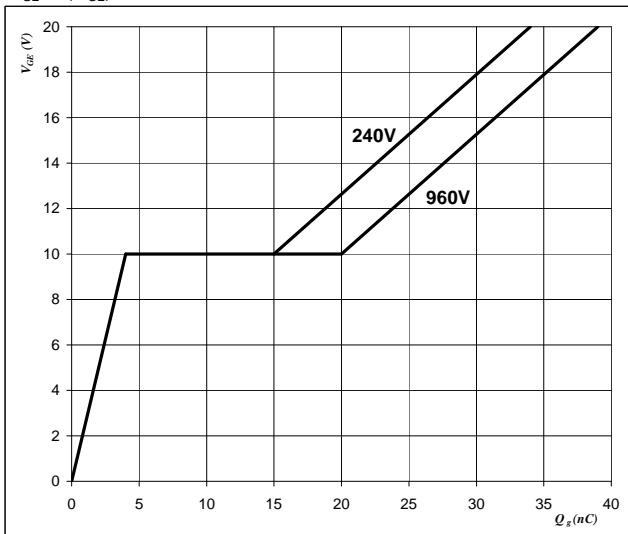
V_{GE} = ±15 V

T_j = T_{jmax} °C

Output inverter IGBT
Figure 26

Gate voltage vs Gate charge

$$V_{GE} = f(Q_{GE})$$

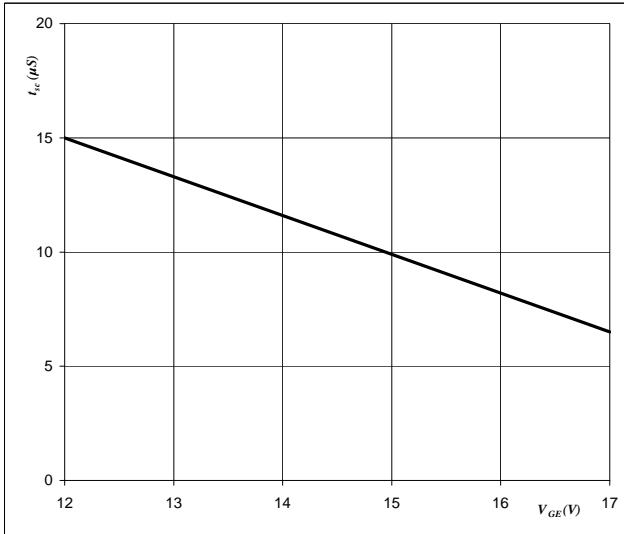

At

I_C = 4 A

Figure 27
Output inverter IGBT

Short circuit withstand time as a function of gate-emitter voltage

$$t_{sc} = f(V_{GE})$$


At

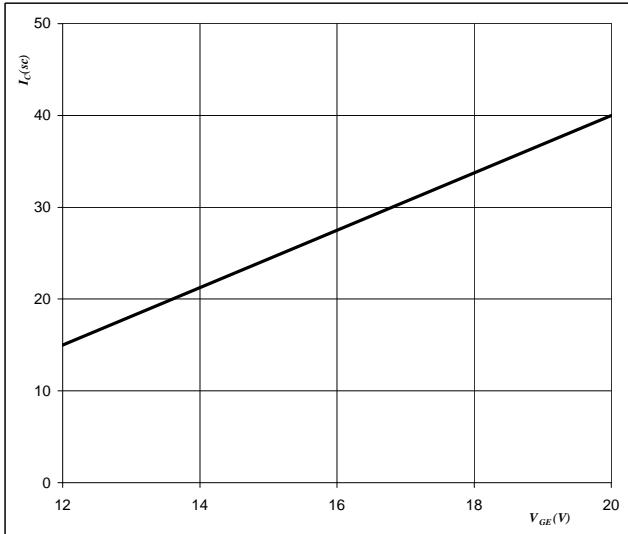
V_{CE} = 600 V

T_j ≤ 175 °C

Figure 28
Output inverter IGBT

Typical short circuit collector current as a function of gate-emitter voltage

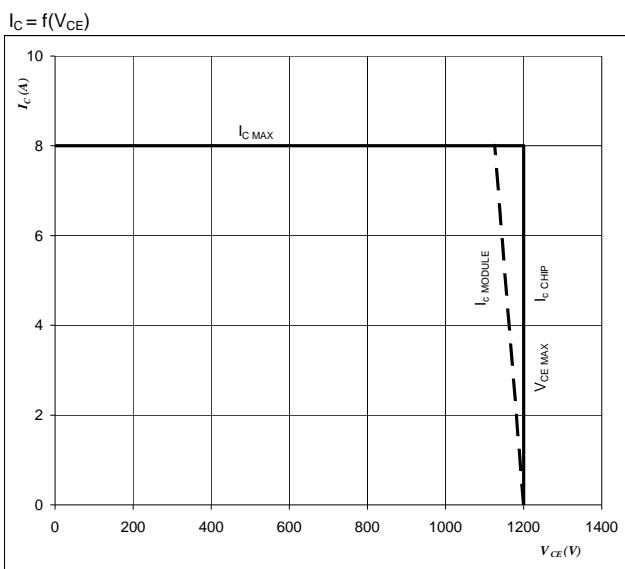
$$V_{GE} = f(Q_{GE})$$


At

V_{CE} ≤ 600 V

T_j = 175 °C

Figure 29
Reverse bias safe operating area



At

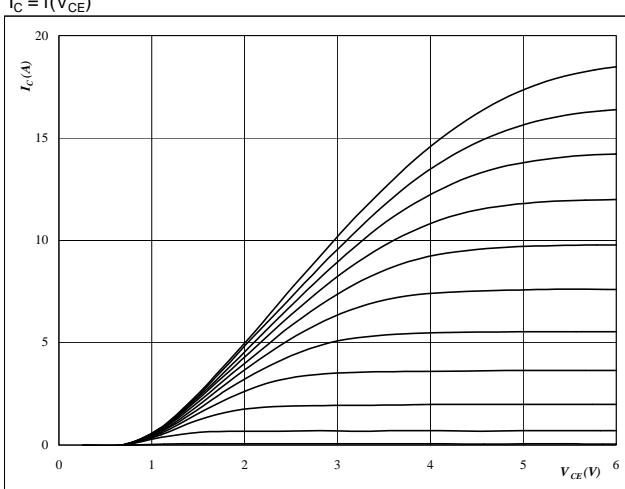
$T_j = T_{j\max} - 25 \quad ^\circ\text{C}$

$U_{ccminus} = U_{ccplus}$

Switching mode : 3 level switching

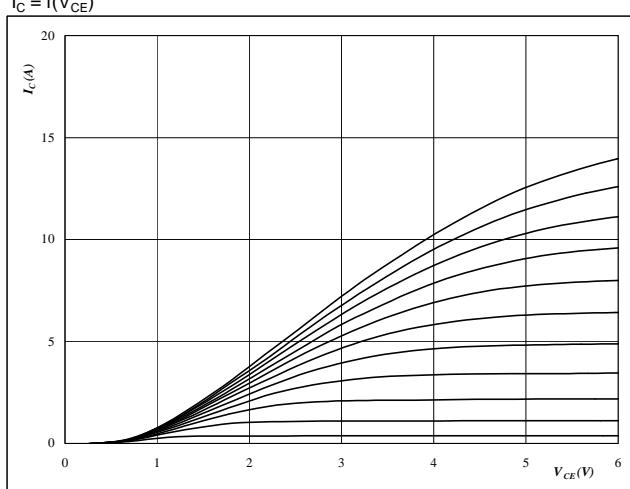
Brake

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



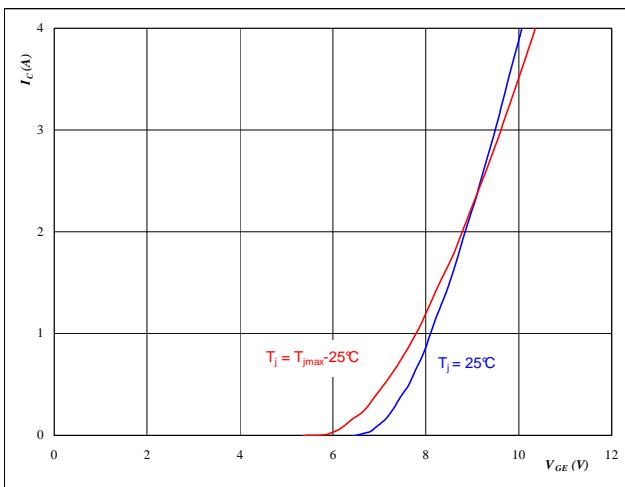
At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $T_j = 25 \text{ } ^\circ\text{C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



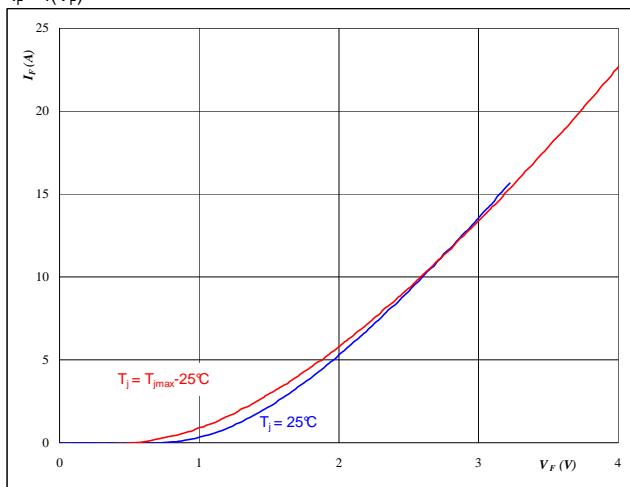
At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $T_j = 150 \text{ } ^\circ\text{C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$



At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$
 $V_{CE} = 10 \text{ } \text{V}$

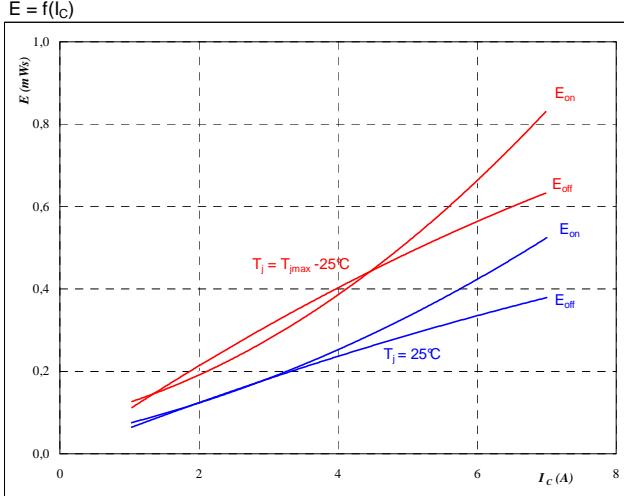
Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



At
 $t_p = 250 \text{ } \check{\text{o}}\text{s}$

Brake

Figure 5
Typical switching energy losses
as a function of collector current
 $E = f(I_C)$

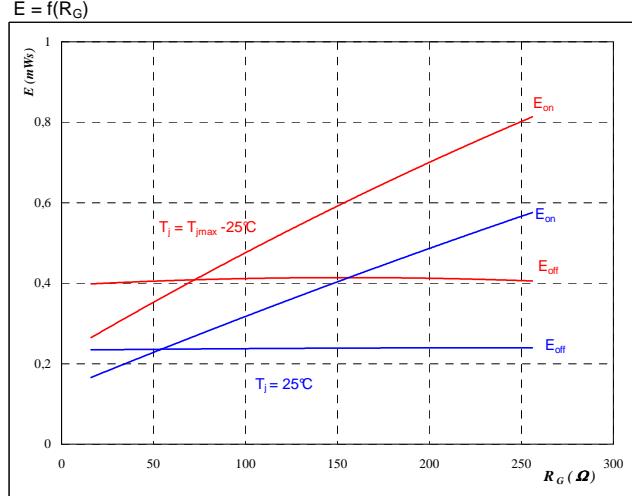


With an inductive load at

$T_j = 25/150 \quad ^\circ C$
 $V_{CE} = 600 \quad V$
 $V_{GE} = \pm 15 \quad V$
 $R_{gon} = 64 \quad \Omega$
 $R_{goff} = 64 \quad \Omega$

Brake IGBT

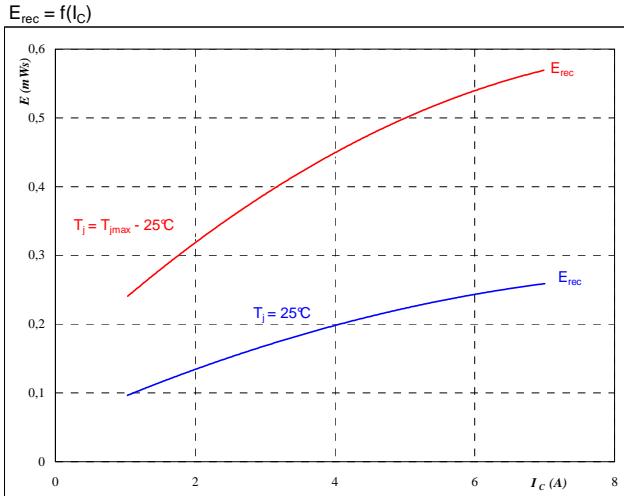
Figure 6
Typical switching energy losses
as a function of gate resistor
 $E = f(R_G)$



With an inductive load at

$T_j = 25/150 \quad ^\circ C$
 $V_{CE} = 600 \quad V$
 $V_{GE} = \pm 15 \quad V$
 $I_C = 4 \quad A$

Figure 7
Typical reverse recovery energy loss
as a function of collector current
 $E_{rec} = f(I_C)$

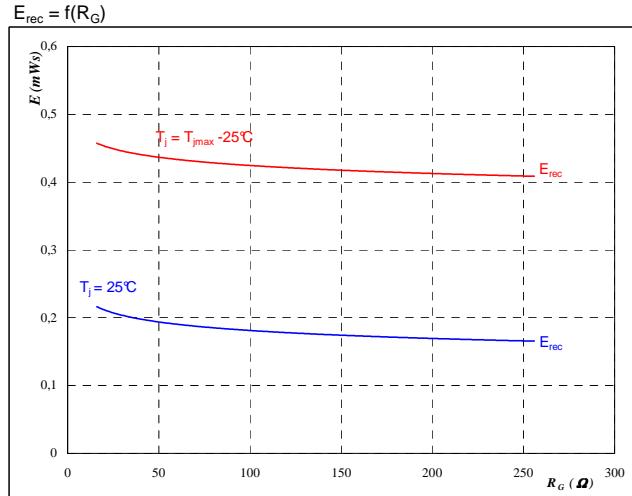


With an inductive load at

$T_j = 25/150 \quad ^\circ C$
 $V_{CE} = 600 \quad V$
 $V_{GE} = \pm 15 \quad V$
 $R_{gon} = 64 \quad \Omega$

Brake FWD

Figure 8
Typical reverse recovery energy loss
as a function of gate resistor
 $E_{rec} = f(R_G)$



With an inductive load at

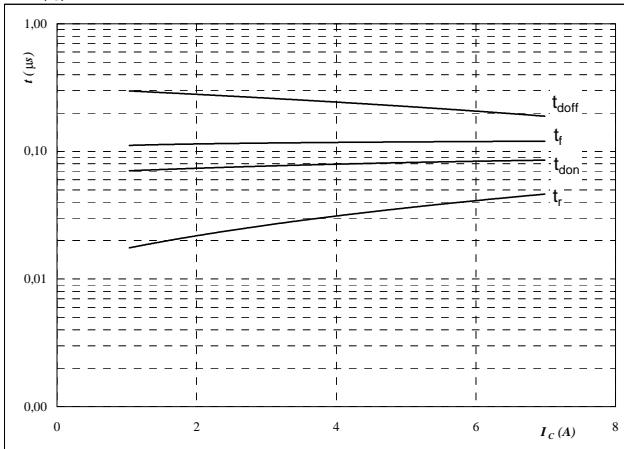
$T_j = 25/150 \quad ^\circ C$
 $V_{CE} = 600 \quad V$
 $V_{GE} = \pm 15 \quad V$
 $I_C = 4 \quad A$

Brake

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



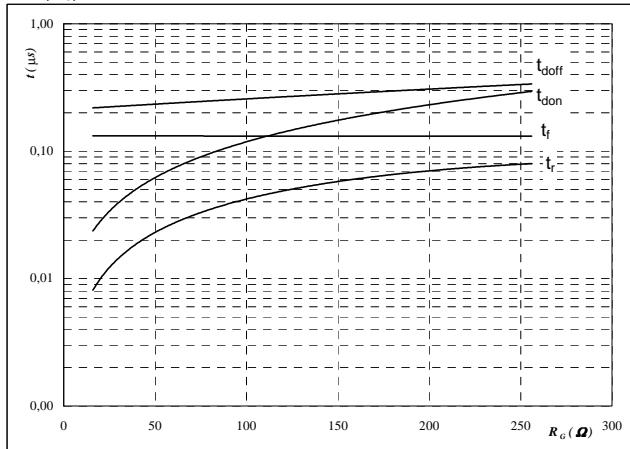
With an inductive load at

T _j =	150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
R _{gon} =	64	Ω
R _{goff} =	64	Ω

Brake IGBT
Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



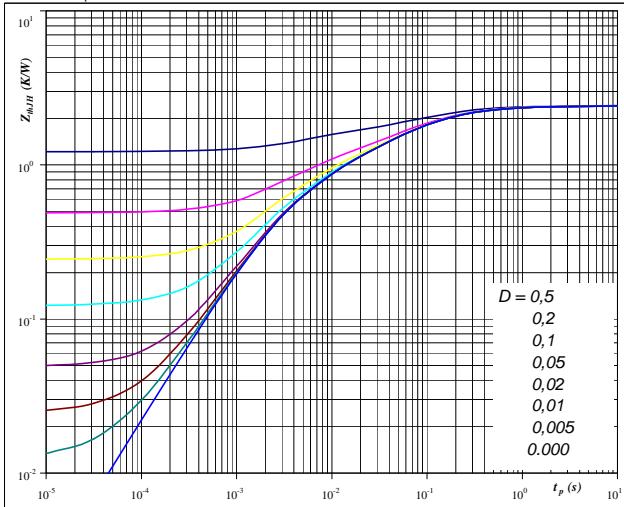
With an inductive load at

T _j =	150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
I _C =	4	A

Figure 11
Brake IGBT

IGBT transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At Thermal grease

$$R_{thJH} = 2.44 \text{ K/W}$$

D =

$$tp / T$$

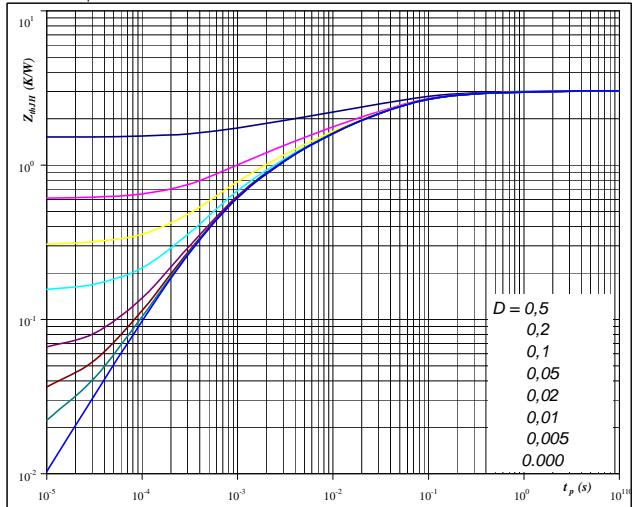
Phase change material

$$R_{thJH} = 1.98 \text{ K/W}$$

Figure 12
Brake FWD

FWD transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At Thermal grease

$$R_{thJH} = 3.03 \text{ K/W}$$

D =

$$tp / T$$

Phase change material

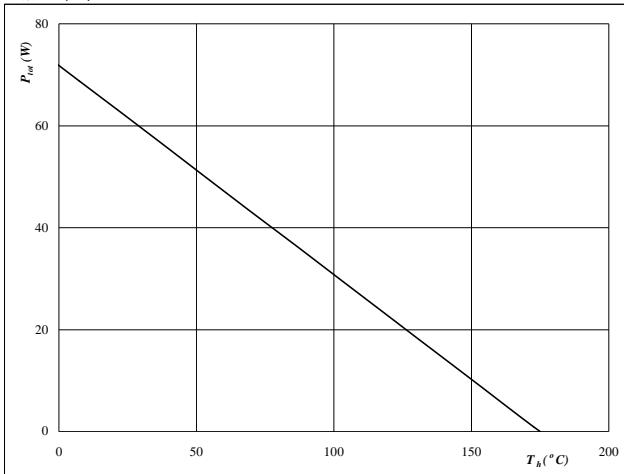
$$R_{thJH} = 2.46 \text{ K/W}$$

Brake

Figure 13

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

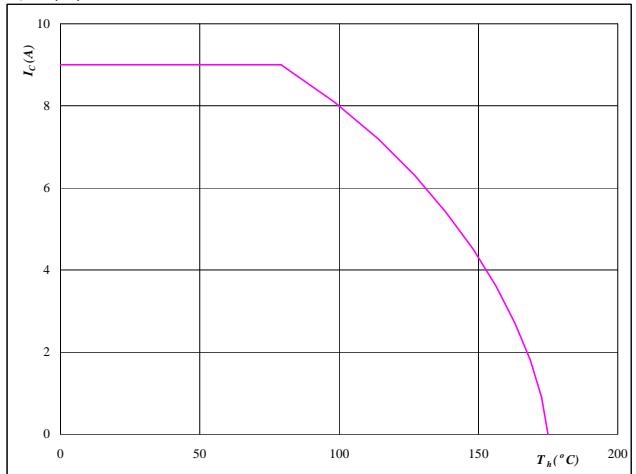

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake IGBT
Figure 14

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

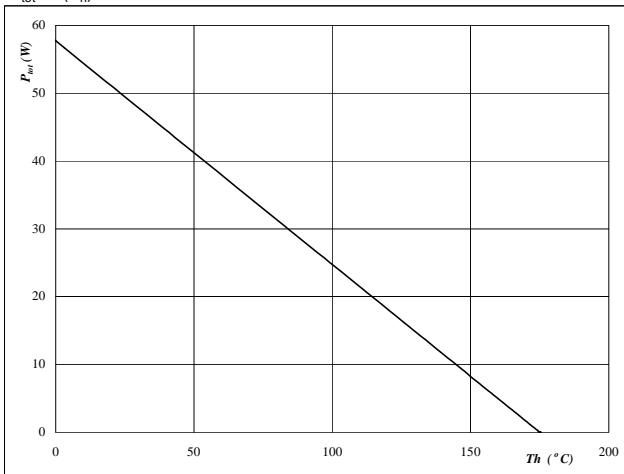
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 15

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

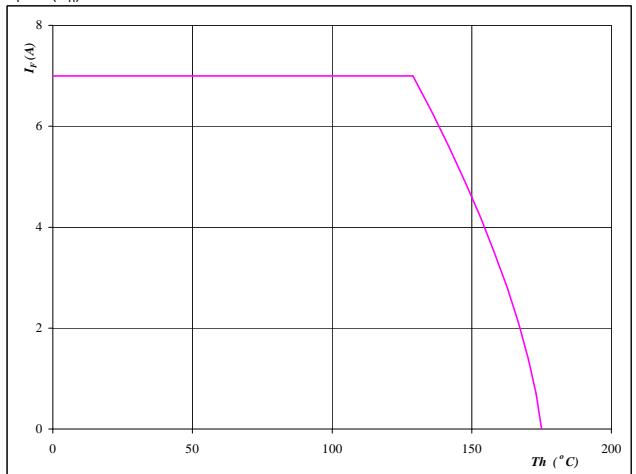

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake FWD
Figure 16

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 175 \quad {}^\circ\text{C}$$

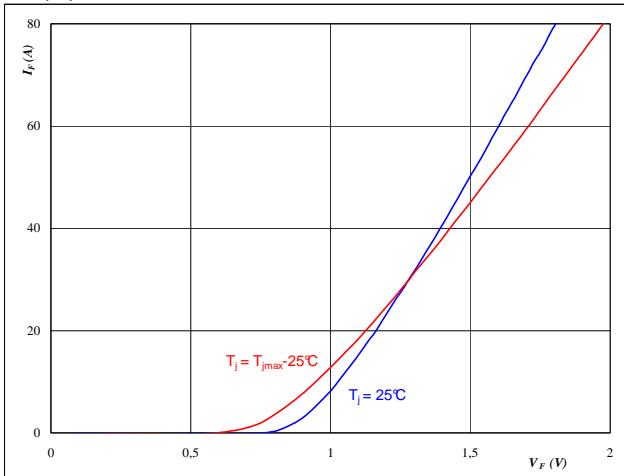
Input Rectifier Bridge

Figure 1

Rectifier diode

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$


At

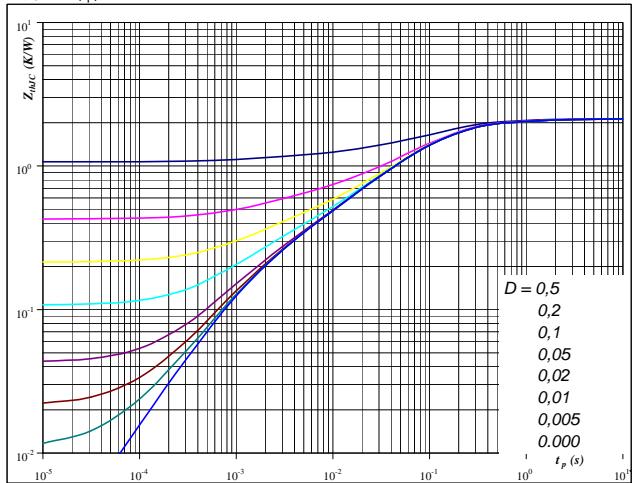
$$t_p = 250 \text{ } \mu\text{s}$$

Figure 2

Rectifier diode

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$$D = t_p / T$$

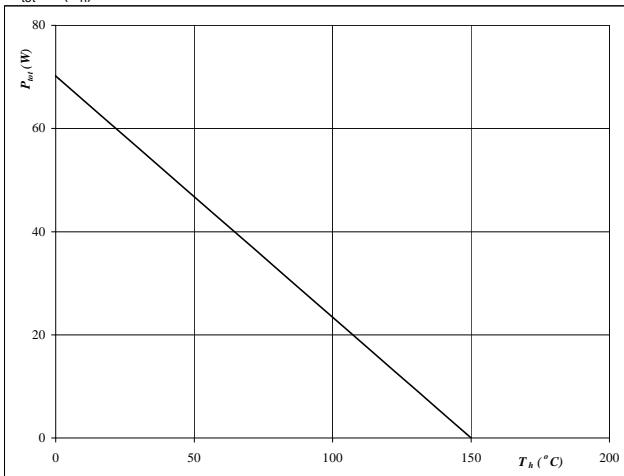
$$R_{thJH} = 2.10 \text{ K/W}$$

Figure 3

Rectifier diode

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$


At

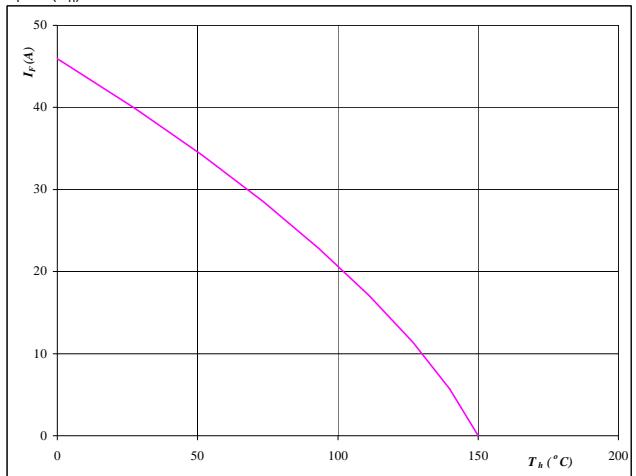
$$T_j = 150 \text{ } ^\circ\text{C}$$

Figure 4

Rectifier diode

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 150 \text{ } ^\circ\text{C}$$

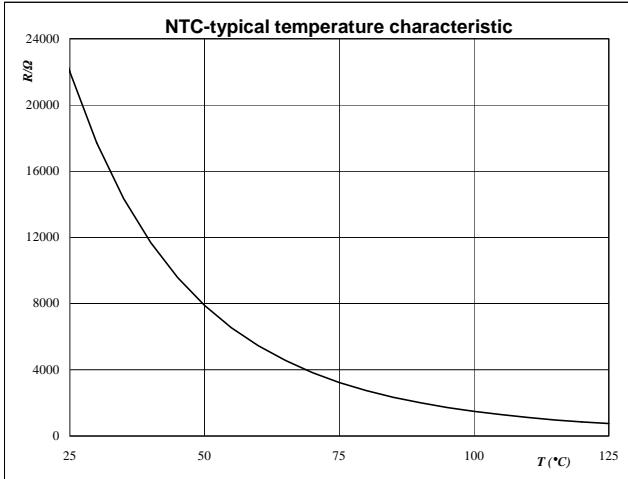
Thermistor

Figure 1

Thermistor

Typical NTC characteristic
as a function of temperature

$$R_T = f(T)$$



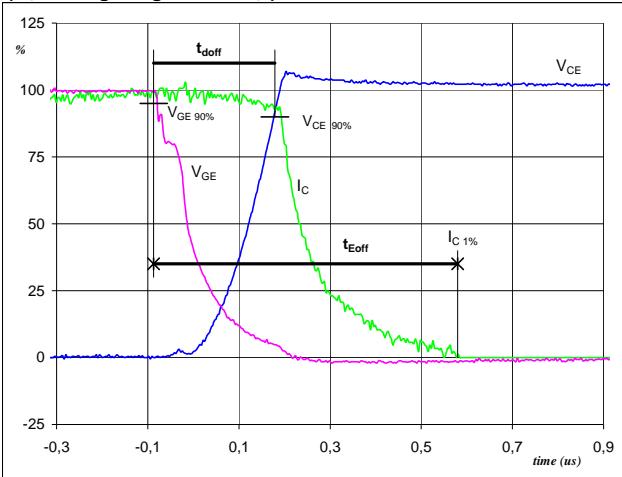
Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	64 Ω
R_{goff}	=	64 Ω

Figure 1

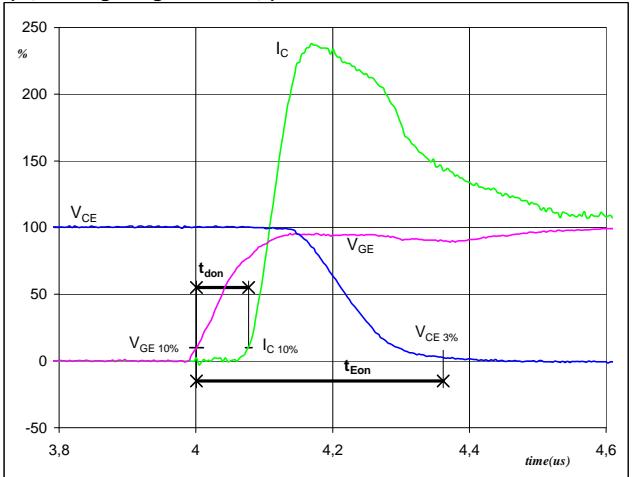
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 4$ A
 $t_{doff} = 0,25$ Šs
 $t_{Eoff} = 0,67$ Šs

Figure 2

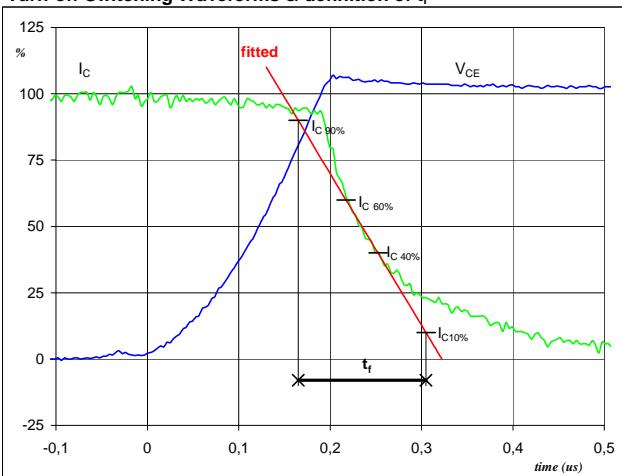
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
(t_{Eon} = integrating time for E_{on})



$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 4$ A
 $t_{don} = 0,08$ Šs
 $t_{Eon} = 0,36$ Šs

Figure 3

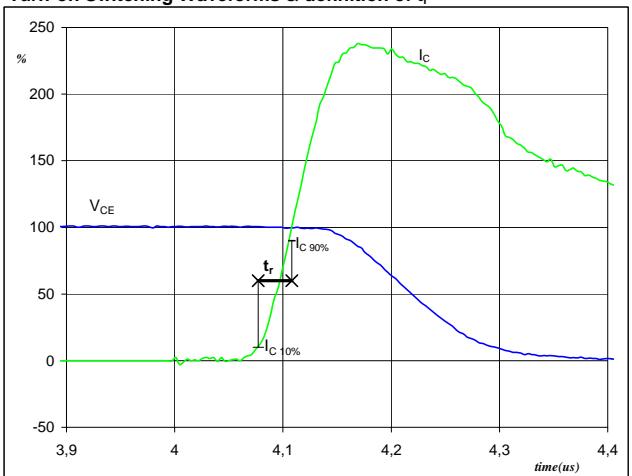
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) = 600$ V
 $I_C(100\%) = 4$ A
 $t_f = 0,12$ Šs

Figure 4

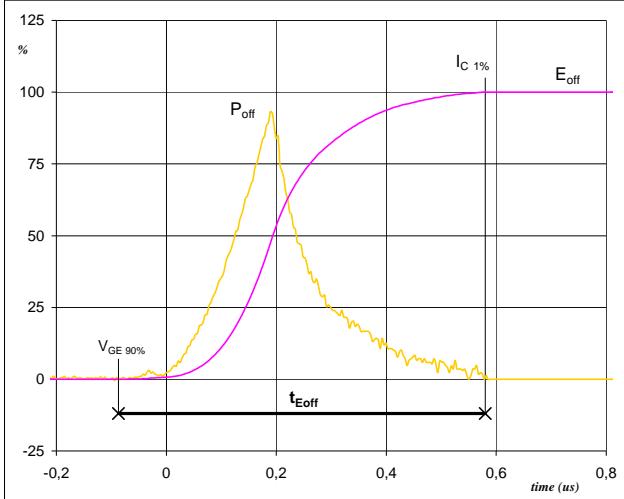
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_r



$V_C(100\%) = 600$ V
 $I_C(100\%) = 4$ A
 $t_r = 0,03$ Šs

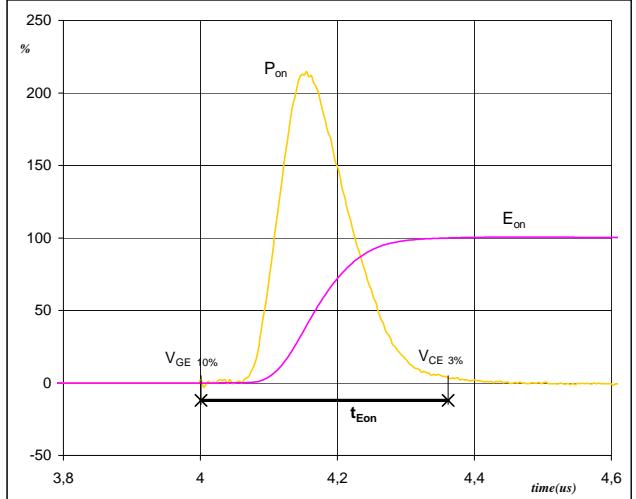
Switching Definitions Output Inverter

Figure 5 Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{Eoff}



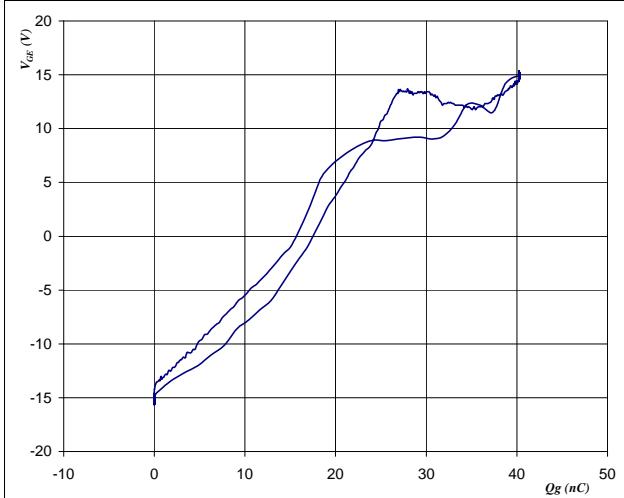
P_{off} (100%) = 2,41 kW
 E_{off} (100%) = 0,39 mJ
 t_{Eoff} = 0,67 us

Figure 6 Output inverter IGBT
Turn-on Switching Waveforms & definition of t_{Eon}



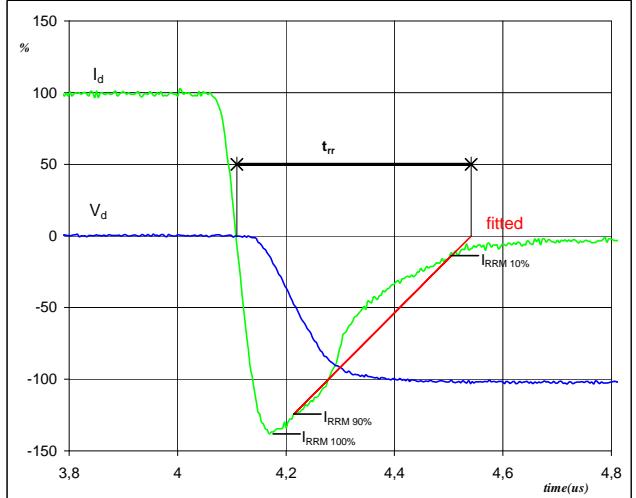
P_{on} (100%) = 2,41 kW
 E_{on} (100%) = 0,63 mJ
 t_{Eon} = 0,36 us

Figure 7 Output inverter FWD
Gate voltage vs Gate charge (measured)



V_{Goff} = -15 V
 V_{Gon} = 15 V
 V_C (100%) = 600 V
 I_C (100%) = 4 A
 Q_g = 40,33 nC

Figure 8 Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{rr}



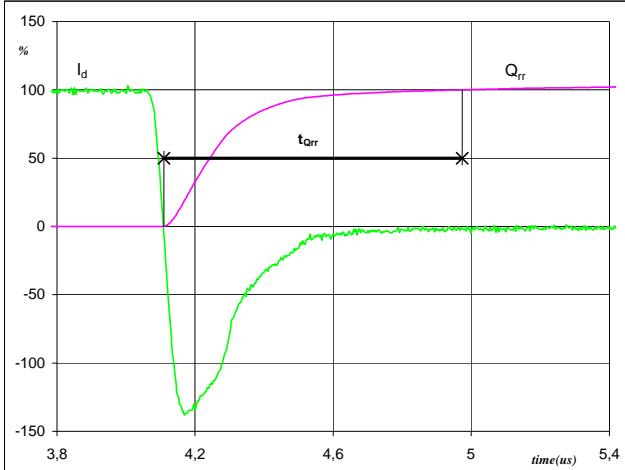
V_d (100%) = 600 V
 I_d (100%) = 4 A
 I_{RRM} (100%) = -6 A
 t_{rr} = 0,43 us

Switching Definitions Output Inverter

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

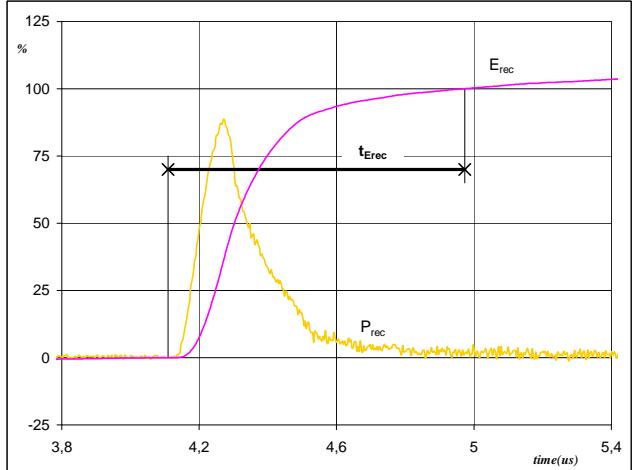


$I_d(100\%) =$ 4 A
 $Q_{rr}(100\%) =$ 1,20 C
 $t_{Qrr} =$ 0,86 s

Figure 10

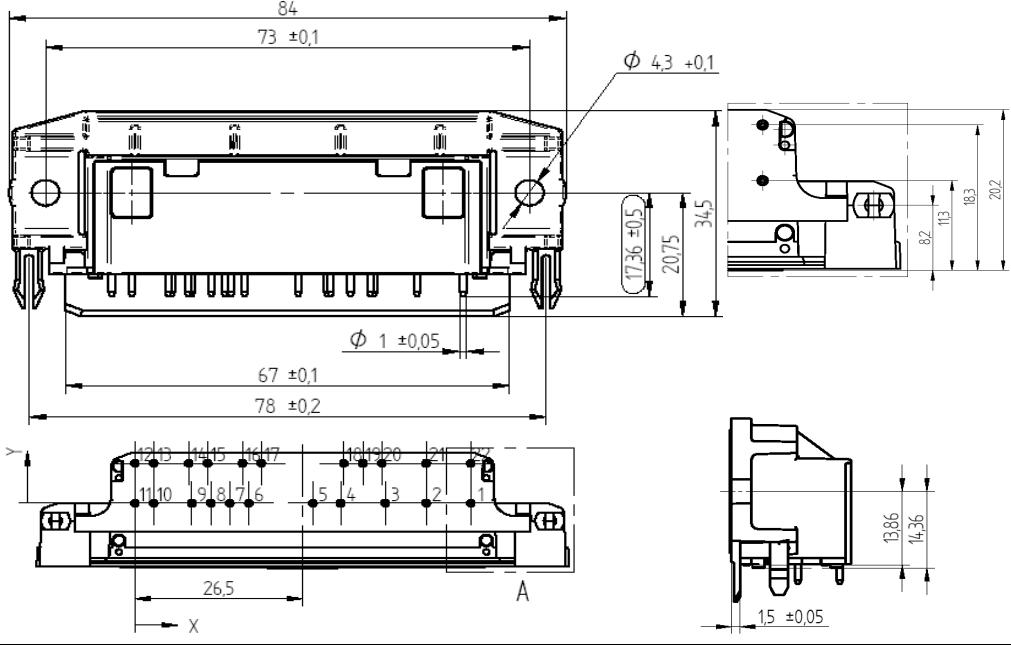
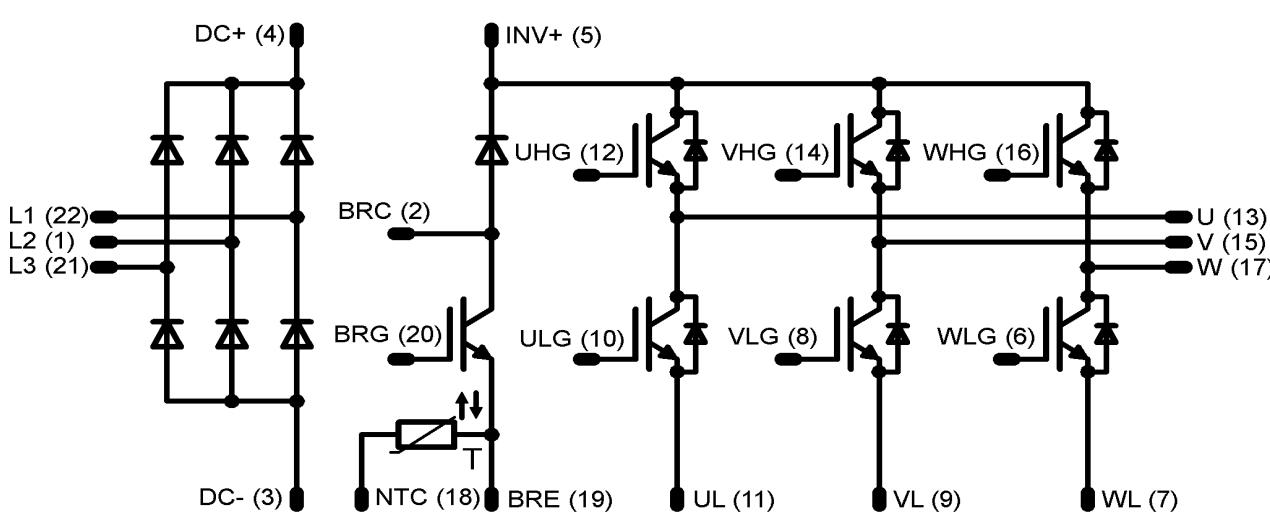
Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$P_{rec}(100\%) =$ 2,41 kW
 $E_{rec}(100\%) =$ 0,43 mJ
 $t_{Erec} =$ 0,86 s

Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking																																																																											
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without thermal paste	V23990-P638-A40-PM	P638-A40	P638-A40																																																																								
Outline																																																																											
<table border="1"> <thead> <tr> <th colspan="3">Pin table</th></tr> <tr> <th>Pin</th><th>X</th><th>Y</th></tr> </thead> <tbody> <tr><td>1</td><td>53</td><td>0</td></tr> <tr><td>2</td><td>46</td><td>0</td></tr> <tr><td>3</td><td>39,5</td><td>0</td></tr> <tr><td>4</td><td>32,5</td><td>0</td></tr> <tr><td>5</td><td>28,1</td><td>0</td></tr> <tr><td>6</td><td>18</td><td>0</td></tr> <tr><td>7</td><td>15</td><td>0</td></tr> <tr><td>8</td><td>12</td><td>0</td></tr> <tr><td>9</td><td>9</td><td>0</td></tr> <tr><td>10</td><td>3</td><td>0</td></tr> <tr><td>11</td><td>0</td><td>0</td></tr> <tr><td>12</td><td>0</td><td>7</td></tr> <tr><td>13</td><td>3</td><td>7</td></tr> <tr><td>14</td><td>8,5</td><td>7</td></tr> <tr><td>15</td><td>11,5</td><td>7</td></tr> <tr><td>16</td><td>17</td><td>7</td></tr> <tr><td>17</td><td>20</td><td>7</td></tr> <tr><td>18</td><td>33</td><td>7</td></tr> <tr><td>19</td><td>36</td><td>7</td></tr> <tr><td>20</td><td>39</td><td>7</td></tr> <tr><td>21</td><td>46</td><td>7</td></tr> <tr><td>22</td><td>53</td><td>7</td></tr> </tbody> </table> 				Pin table			Pin	X	Y	1	53	0	2	46	0	3	39,5	0	4	32,5	0	5	28,1	0	6	18	0	7	15	0	8	12	0	9	9	0	10	3	0	11	0	0	12	0	7	13	3	7	14	8,5	7	15	11,5	7	16	17	7	17	20	7	18	33	7	19	36	7	20	39	7	21	46	7	22	53	7
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DISCLAIMER

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As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.