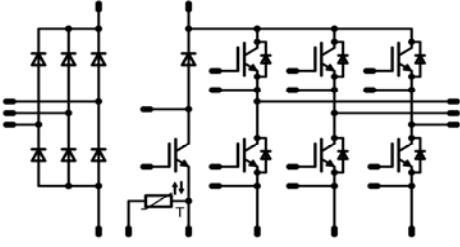


flow90PIM 1		1200V/25A
<p>Features</p> <ul style="list-style-type: none"> • Trench Fieldstop Technology IGBT4 for low saturation loss • Supports design with 90° mounting angle between heatsink and PCB • Clip-in PCB mounting • Clip or screw on heatsink mounting 		<p>flow90PIM 1</p> 
<p>Target Applications</p> <ul style="list-style-type: none"> • Industrial drives 		<p>Schematic</p> 
<p>Types</p> <ul style="list-style-type: none"> • V23990-P630-A44-PM 		

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Input Rectifier Diode				
Repetitive peak reverse voltage	V_{RRM}		1600	V
DC forward current	I_{FAV}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	27 37	A
Surge forward current	I_{FSM}		300	A
I^2t -value	I^2t	$t_p=10\text{ms}$ $T_j=45^\circ\text{C}$	450	A^2s
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 49	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$
Inverter Transistor				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	29 37	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$	75	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{j\max}$	75	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	77 116	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Inverter FWD

Peak Repetitive Reverse Voltage	V_{RRM}	$T_j=25^\circ\text{C}$	1200	V
DC forward current	I_F	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	27 35	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\text{max}$	50	A
Power dissipation per Diode	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	56 84	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Brake Transistor

Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	20 26	A
Repetitive peak collector current	I_{CPuls}	t_p limited by $T_j\text{max}$	45	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{op\ max}$	45	A
Power dissipation per IGBT	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	61 92	W
Gate-emitter peak voltage	V_{GE}		±20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Brake FWD

Peak Repetitive Reverse Voltage	V_{RRM}	$T_j=25^\circ\text{C}$	1200	V
DC forward current	I_F	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	20 20	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\text{max}$	20	A
Power dissipation per Diode	P_{tot}	$T_j=T_j\text{max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	49 75	W
Maximum Junction Temperature	$T_j\text{max}$		175	°C

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{op}		-40...+($T_{jmax} - 25$)	°C

Insulation Properties

Insulation voltage	V_{is}	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
			V _{GE} [V] or V _{GS} [V]	V _I [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _J	Min	Typ	Max	
Input Rectifier Diode										
Forward voltage	V _F				30	T _J =25°C T _J =125°C	0,8	1,20 1,17	1,5	V
Threshold voltage (for power loss calc. only)	V _{to}				30	T _J =25°C T _J =125°C		0,92 0,81		V
Slope resistance (for power loss calc. only)	r _t				30	T _J =25°C T _J =125°C		11 14		mΩ
Reverse current	I _r			1600		T _J =25°C T _J =125°C			0,01	mA
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,14		K/W
Inverter Transistor										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,00085	T _J =25°C T _J =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		25	T _J =25°C T _J =150°C	1,6	1,96 2,30	2,1	V
Collector-emitter cut-off current incl. Diode	I _{CES}		0	1200		T _J =25°C T _J =150°C			0,0024	mA
Gate-emitter leakage current	I _{GES}		20	0		T _J =25°C T _J =150°C			120	nA
Integrated Gate resistor	R _{gint}							none		Ω
Turn-on delay time	t _{d(on)}	R _{goff} =32 Ω R _{gon} =32 Ω	±15	600	25	T _J =25°C T _J =150°C		127 130		ns
Rise time	t _r					T _J =25°C T _J =150°C		45 46		
Turn-off delay time	t _{d(off)}					T _J =25°C T _J =150°C		240 318		
Fall time	t _f					T _J =25°C T _J =150°C		68 136		
Turn-on energy loss per pulse	E _{on}					T _J =25°C T _J =150°C		2,61 3,77		mWs
Turn-off energy loss per pulse	E _{off}					T _J =25°C T _J =150°C		1,42 2,45		
Input capacitance	C _{ies}	f=1MHz	0	25	T _J =25°C			1430		pF
Output capacitance	C _{oss}							115		
Reverse transfer capacitance	C _{rss}							85		
Gate charge	Q _{Gate}		±15	960	25	T _J =25°C		115		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,24		K/W
Inverter FWD										
Diode forward voltage	V _F				25	T _J =25°C T _J =150°C	1,35	1,86 1,81	2,05	V
Peak reverse recovery current	I _{RRM}	R _{gon} =32 Ω	±15	600	25	T _J =25°C T _J =150°C		12 16		A
Reverse recovery time	t _{rr}					T _J =25°C T _J =150°C		345 564		ns
Reverse recovered charge	Q _{rr}					T _J =25°C T _J =150°C		2,18 4,68		μC
Peak rate of fall of recovery current	di(rec)max /dt					T _J =25°C T _J =150°C		40 36		A/μs
Reverse recovered energy	E _{rec}					T _J =25°C T _J =150°C		0,77 1,74		mWs
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,71		K/W

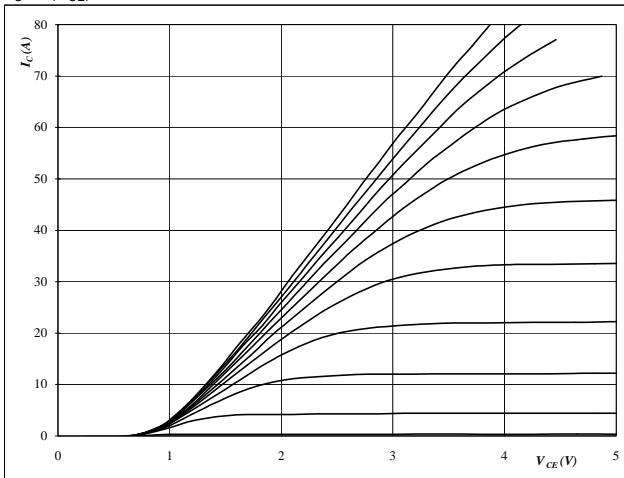
Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
			V _{GE} [V] or V _{GS} [V]	V _I [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _j	Min	Typ	Max	
Brake Transistor										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,00043	T _j =25°C T _j =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		15	T _j =25°C T _j =150°C	1,6	1,89 2,28	2,1	V
Collector-emitter cut-off incl diode	I _{CES}		0	1200		T _j =25°C T _j =150°C			0,002	mA
Gate-emitter leakage current	I _{GES}		20	0		T _j =25°C T _j =150°C			120	nA
Integrated Gate resistor	R _{gint}							none		Ω
Turn-on delay time	t _{d(on)}	R _{goff} =32 Ω R _{gon} =32 Ω	±15	600	15	T _j =25°C T _j =150°C	93			ns
Rise time	t _r					T _j =25°C T _j =150°C	37			
Turn-off delay time	t _{d(off)}					T _j =25°C T _j =150°C	199 267			
Fall time	t _f					T _j =25°C T _j =150°C	80			
Turn-on energy loss per pulse	E _{on}					T _j =25°C T _j =150°C	1,05 1,49			mWs
Turn-off energy loss per pulse	E _{off}					T _j =25°C T _j =150°C	0,86 1,44			
Input capacitance	C _{ies}	f=1MHz	0	25		T _j =25°C	900			pF
Output capacitance	C _{oss}						80			
Reverse transfer capacitance	C _{rss}						55			
Gate charge	Q _{Gate}		±15	960	25	T _j =25°C		92		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,56		K/W
Brake FWD										
Diode forward voltage	V _F				10	T _j =25°C T _j =150°C	1,35	1,87 1,79	2,05	V
Reverse leakage current	I _r			1200		T _j =25°C T _j =150°C			2,7	mA
Peak reverse recovery current	I _{RRM}	R _{gon} =32 Ω	±15	600	15	T _j =25°C T _j =150°C	8 11			A
Reverse recovery time	t _{rr}					T _j =25°C T _j =150°C	317 550			ns
Reverse recovered charge	Q _{rr}					T _j =25°C T _j =150°C	1,20 1,20			μC
Peak rate of fall of recovery current	di(rec)max /dt					T _j =25°C T _j =150°C	51 39			A/μs
Reverse recovery energy	E _{rec}					T _j =25°C T _j =150°C	0,49 1,08			mWs
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,92		K/W
Thermistor										
Rated resistance	R					T _j =25°C		22000		Ω
Deviation of R100	ΔR/R	R100=1486 Ω				T _c =100°C	-5		5	%
Power dissipation	P					T _c =25°C		200		mW
Power dissipation constant						T _j =25°C		2		mW/K
B-value	B _(25/50)	Tol. ±3%				T _j =25°C		3950		K
B-value	B _(25/100)	Tol. ±3%				T _j =25°C		3996		K
Vincotech NTC Reference									B	

Output Inverter

Figure 1
Typical output characteristics

$$I_C = f(V_{CE})$$


At

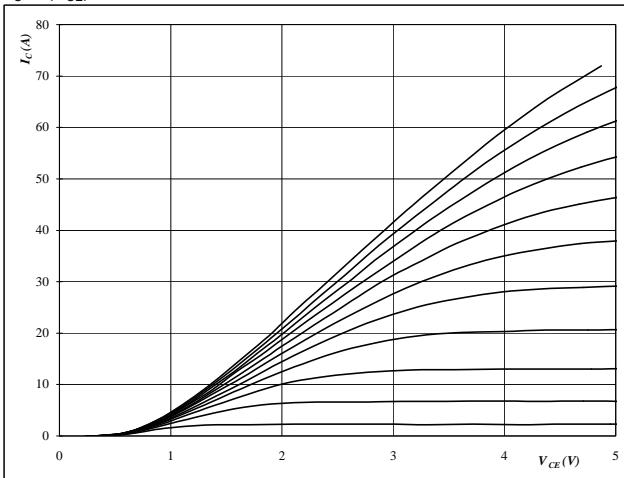
$$t_p = 250 \text{ } \check{\text{s}}$$

$$T_j = 25 \text{ } ^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics

$$I_C = f(V_{CE})$$


At

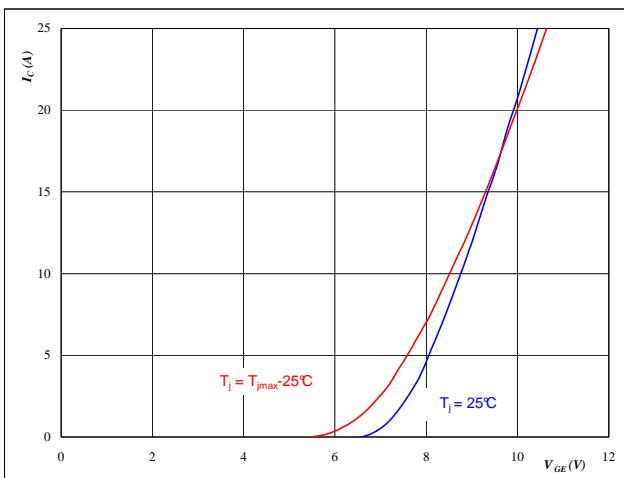
$$t_p = 250 \text{ } \check{\text{s}}$$

$$T_j = 150 \text{ } ^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics

$$I_C = f(V_{GE})$$

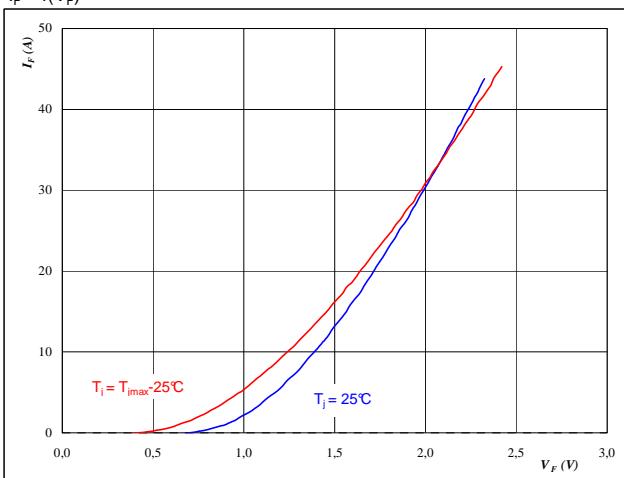

At

$$t_p = 250 \text{ } \check{\text{s}}$$

$$V_{CE} = 10 \text{ } \text{V}$$

Figure 4
Output inverter FWD
Typical diode forward current as a function of forward voltage

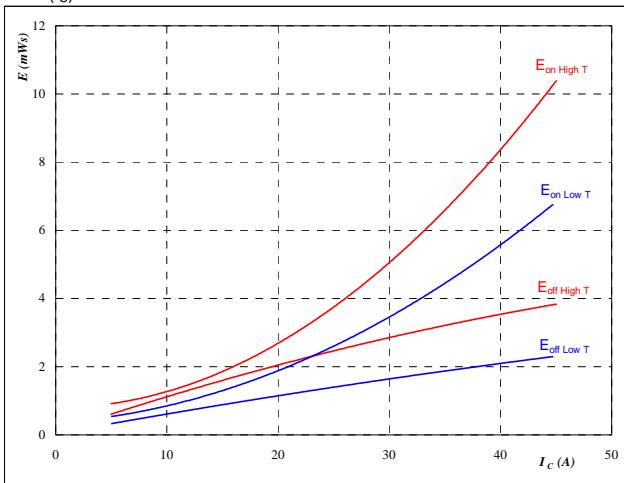
$$I_F = f(V_F)$$


At

$$t_p = 250 \text{ } \check{\text{s}}$$

Output Inverter

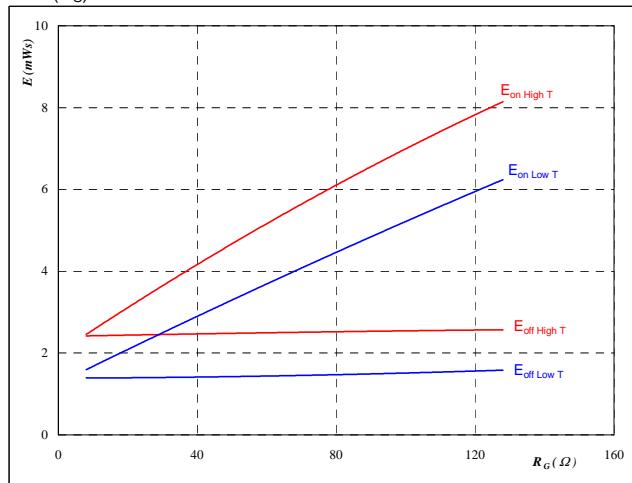
Figure 5
Typical switching energy losses as a function of collector current
 $E = f(I_C)$



With an inductive load at

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_{CE} = 600 \quad \text{V}$
 $V_{GE} = \pm 15 \quad \text{V}$
 $R_{gon} = 32 \quad \Omega$
 $R_{goff} = 32 \quad \Omega$

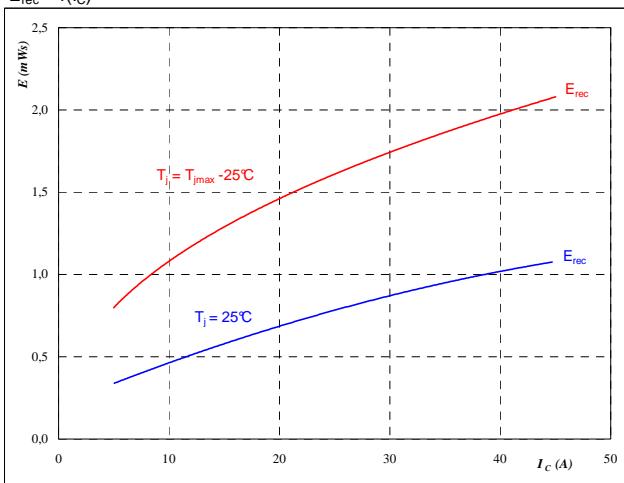
Figure 6
Typical switching energy losses as a function of gate resistor
 $E = f(R_G)$



With an inductive load at

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_{CE} = 600 \quad \text{V}$
 $V_{GE} = \pm 15 \quad \text{V}$
 $I_C = 25 \quad \text{A}$

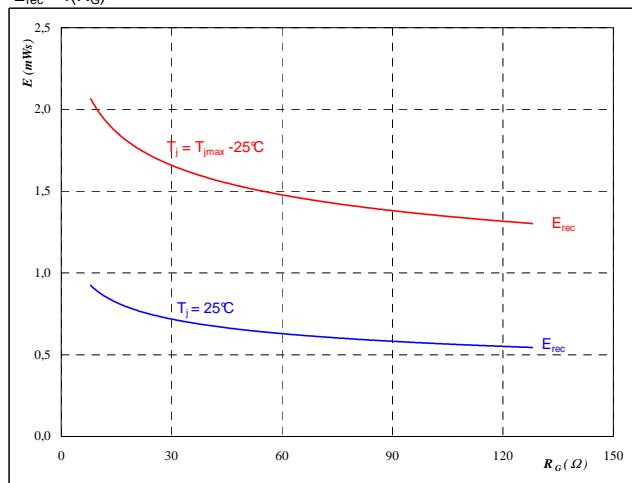
Figure 7
Typical reverse recovery energy loss as a function of collector current
 $E_{rec} = f(I_C)$



With an inductive load at

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_{CE} = 600 \quad \text{V}$
 $V_{GE} = \pm 15 \quad \text{V}$
 $R_{gon} = 32 \quad \Omega$

Figure 8
Typical reverse recovery energy loss as a function of gate resistor
 $E_{rec} = f(R_G)$



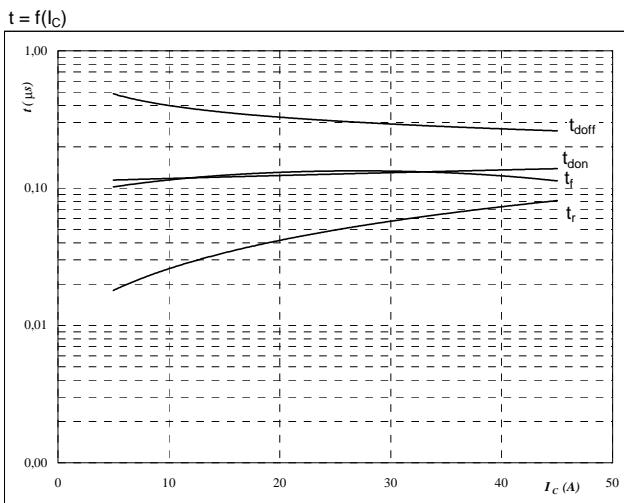
With an inductive load at

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_{CE} = 600 \quad \text{V}$
 $V_{GE} = \pm 15 \quad \text{V}$
 $I_C = 25 \quad \text{A}$

Output Inverter

Figure 9

Typical switching times as a function of collector current
 $t = f(I_C)$

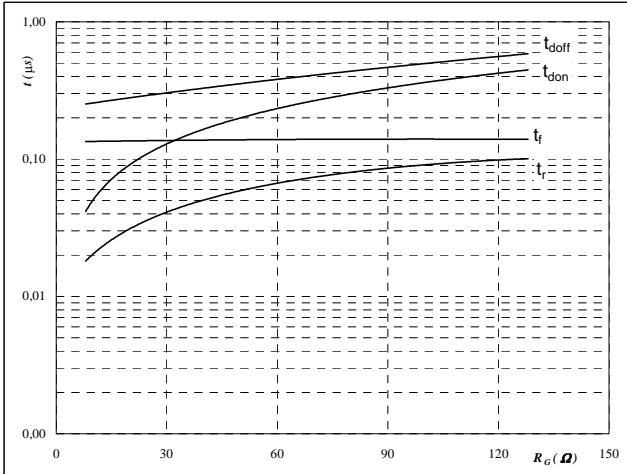


With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \Omega$
 $R_{goff} = 32 \Omega$

Figure 10

Typical switching times as a function of gate resistor
 $t = f(R_G)$



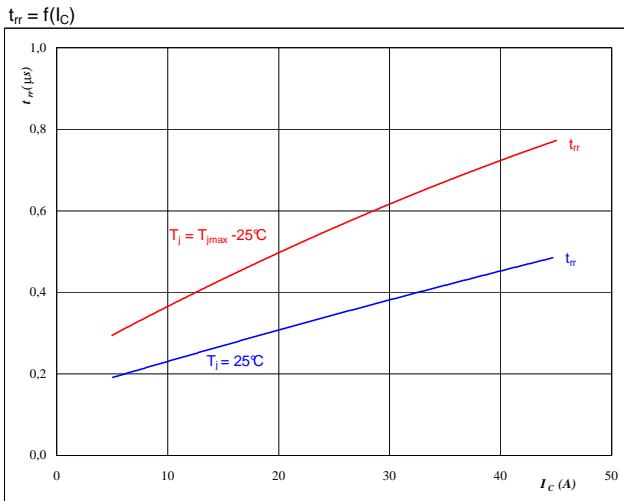
With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 25 \text{ A}$

Figure 11

Output inverter FWD

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$



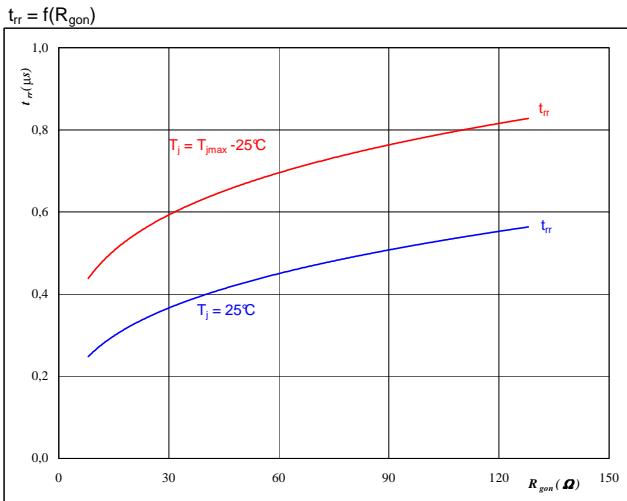
At

$T_j = 25/150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 32 \Omega$

Figure 12

Output inverter FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



At

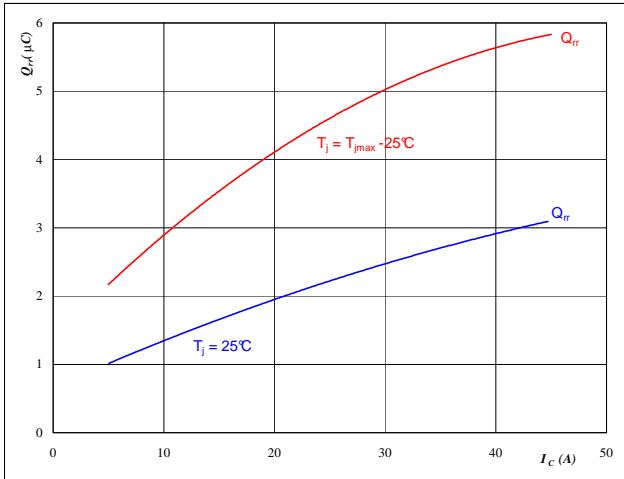
$T_j = 25/150 \text{ }^\circ\text{C}$
 $V_R = 600 \text{ V}$
 $I_F = 25 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

Output Inverter

Figure 13

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

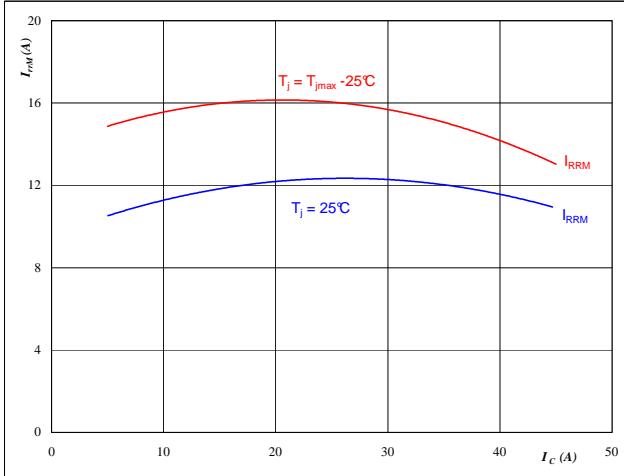

At

$T_j =$	25/150	$^\circ\text{C}$
$V_{CE} =$	600	V
$V_{GE} =$	± 15	V
$R_{gon} =$	32	Ω

Figure 15

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

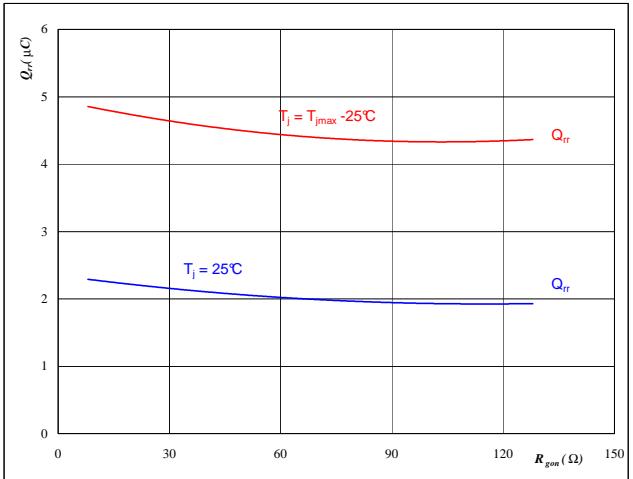

At

$T_j =$	25/150	$^\circ\text{C}$
$V_{CE} =$	600	V
$V_{GE} =$	± 15	V
$R_{gon} =$	32	Ω

Figure 14

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

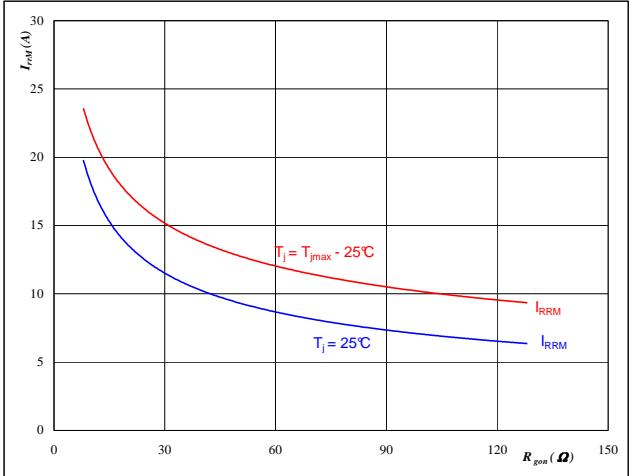

At

$T_j =$	25/150	$^\circ\text{C}$
$V_R =$	600	V
$I_F =$	25	A
$V_{GE} =$	± 15	V

Figure 16

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

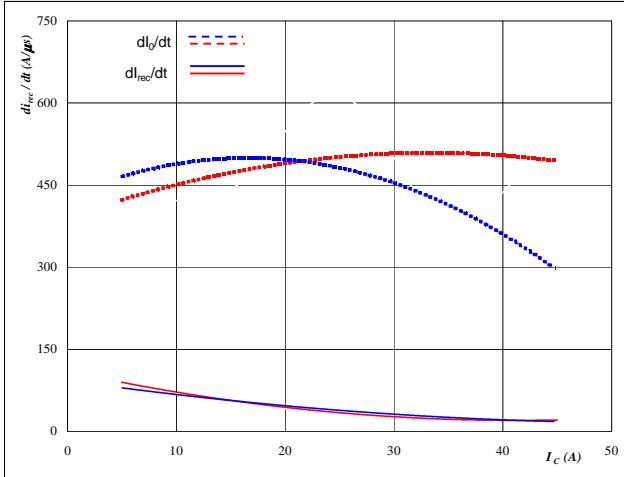

At

$T_j =$	25/150	$^\circ\text{C}$
$V_R =$	600	V
$I_F =$	25	A
$V_{GE} =$	± 15	V

Output Inverter

Figure 17

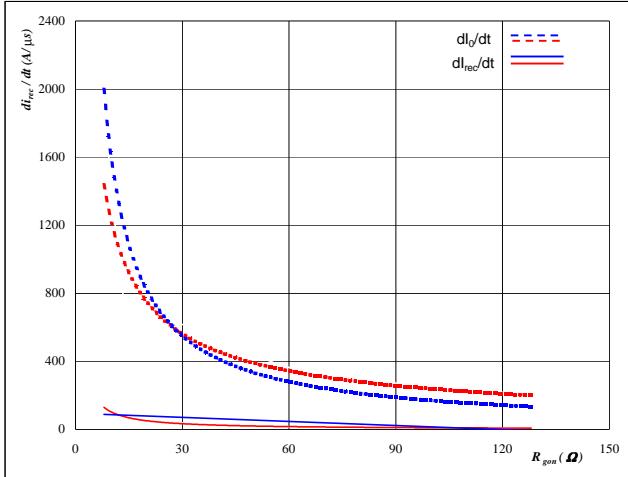
Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dl_0/dt, dl_{rec}/dt = f(I_C)$


At

$T_j = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 32$ Ω

Output inverter FWD
Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dl_0/dt, dl_{rec}/dt = f(R_{gon})$

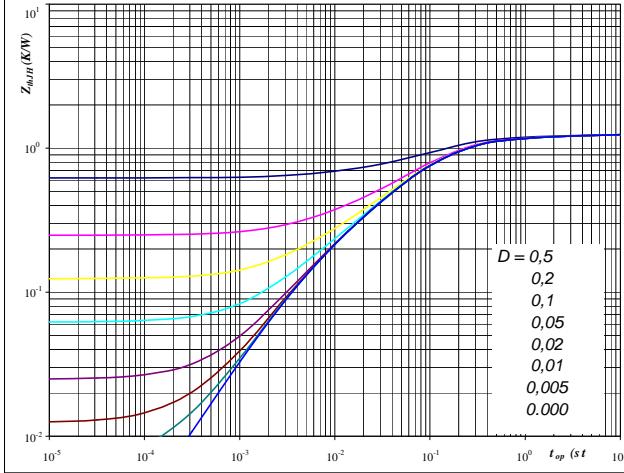

At

$T_j = 25/150$ °C
 $V_R = 600$ V
 $I_F = 25$ A
 $V_{GE} = \pm 15$ V

Figure 19

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$

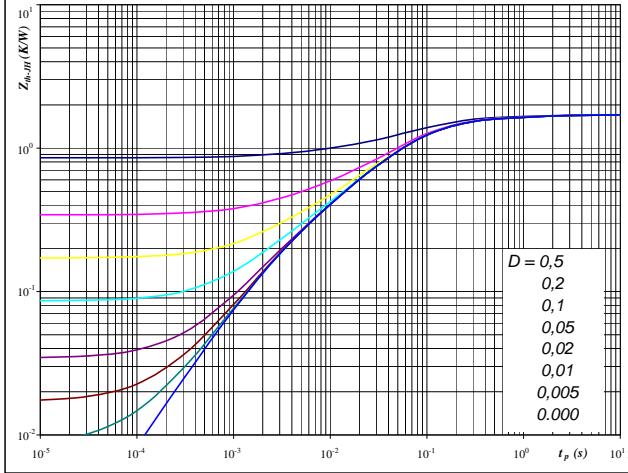

At

$D = t_p / T$
 $R_{thJH} = 1,24$ K/W $R_{thJH} = 1,01$

Output inverter IGBT
Figure 20

FWD transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$D = t_p / T$
 $R_{thJH} = 1,71$ K/W $R_{thJH} = 1,39$

IGBT thermal model values

Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,09	2,4E+00	0,07	2,0E+00
0,17	4,2E-01	0,14	3,4E-01
0,66	1,1E-01	0,53	8,8E-02
0,24	2,6E-02	0,20	2,1E-02
0,08	4,3E-03	0,07	3,5E-03

FWD thermal model values

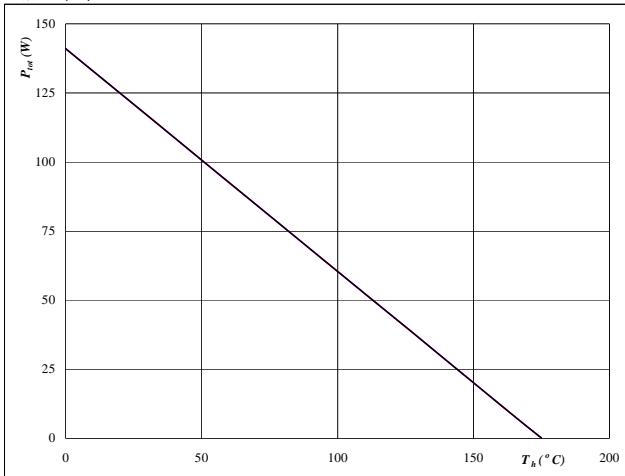
Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,03	6,7E+00	0,03	5,5E+00
0,11	1,0E+00	0,09	8,3E-01
0,36	1,7E-01	0,29	1,4E-01
0,87	5,6E-02	0,70	4,6E-02
0,24	1,1E-02	0,19	9,2E-03
0,10	2,3E-03	0,08	1,8E-03

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

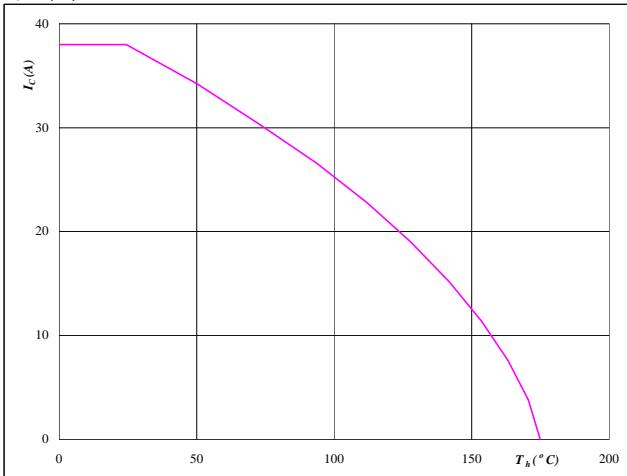

At

$$T_j = 175 \quad ^\circ\text{C}$$

Output inverter IGBT
Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$

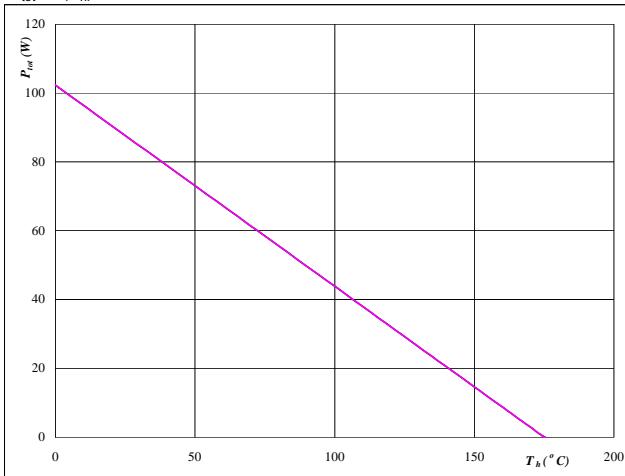

At

$$T_j = 175 \quad ^\circ\text{C}$$

Output inverter IGBT
Figure 23
Output inverter FWD

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

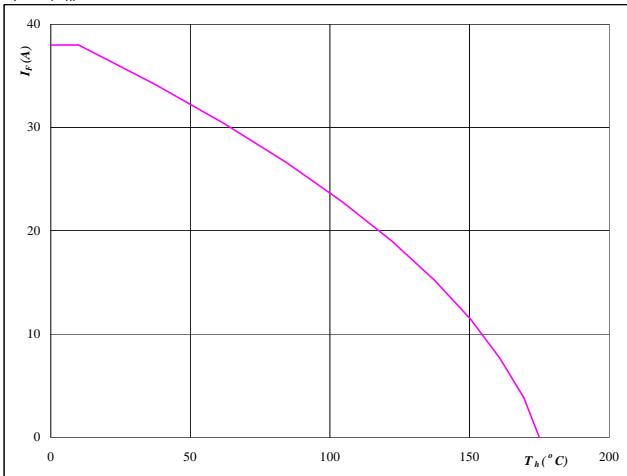

At

$$T_j = 175 \quad ^\circ\text{C}$$

Figure 24
Output inverter FWD

Forward current as a function of heatsink temperature

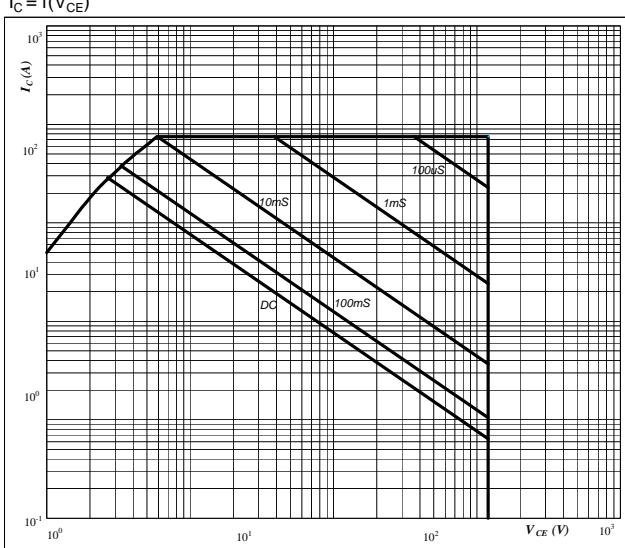
$$I_F = f(T_h)$$


At

$$T_j = 175 \quad ^\circ\text{C}$$

Output Inverter

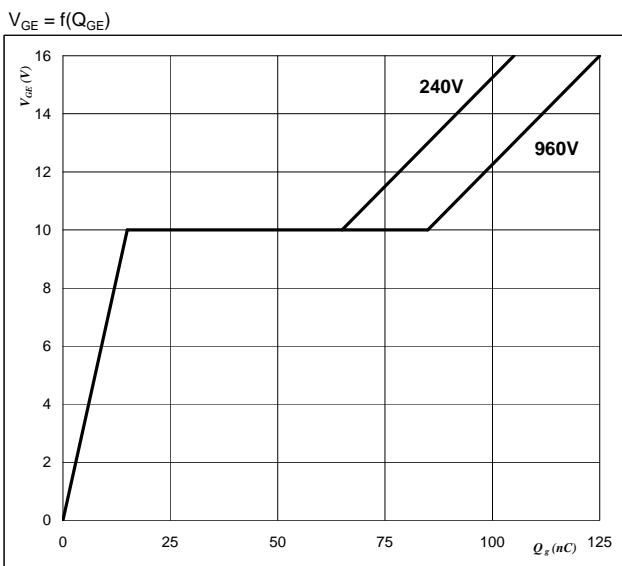
Figure 25
Safe operating area as a function
of collector-emitter voltage
 $I_C = f(V_{CE})$



At

D = single pulse
 $T_h = 80 \text{ } ^\circ\text{C}$
 $V_{GE} = \pm 15 \text{ V}$
 $T_j = T_{jmax} \text{ } ^\circ\text{C}$

Figure 26
Gate voltage vs Gate charge
 $V_{GE} = f(Q_{GE})$



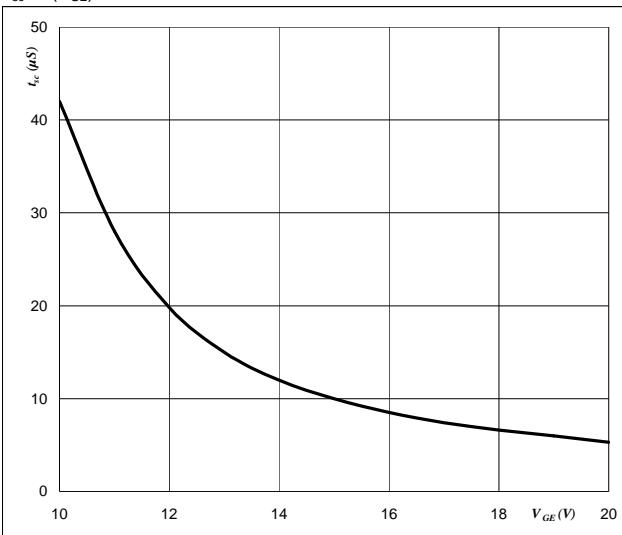
At

$I_C = 25 \text{ A}$

Figure 27

Short circuit withstand time as a function of gate-emitter voltage

$$t_{sc} = f(V_{GE})$$


At

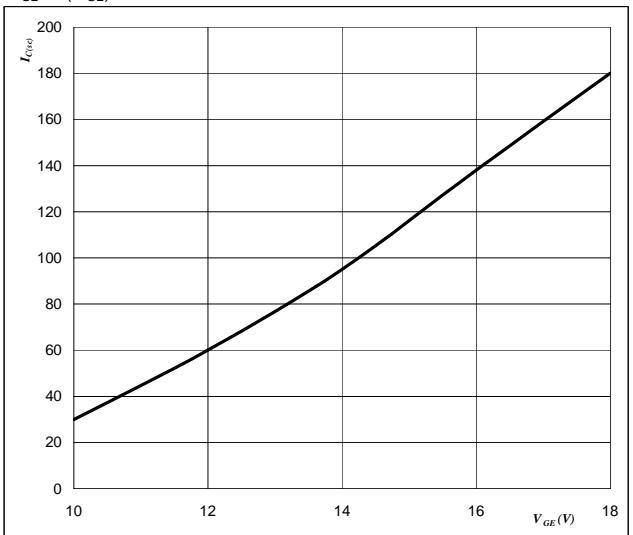
$$\begin{aligned} V_{CE} &= 1200 \quad V \\ T_j &\leq 175 \quad ^\circ C \end{aligned}$$

Figure 28

Output inverter IGBT

Typical short circuit collector current as a function of gate-emitter voltage

$$V_{GE} = f(Q_{GE})$$


At

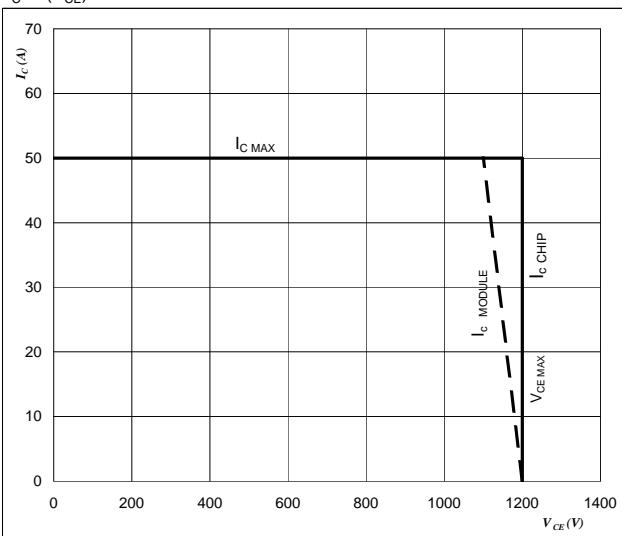
$$\begin{aligned} V_{CE} &\leq 1200 \quad V \\ T_j &= 175 \quad ^\circ C \end{aligned}$$

Figure 29

IGBT

Reverse bias safe operating area

$$I_C = f(V_{CE})$$


At

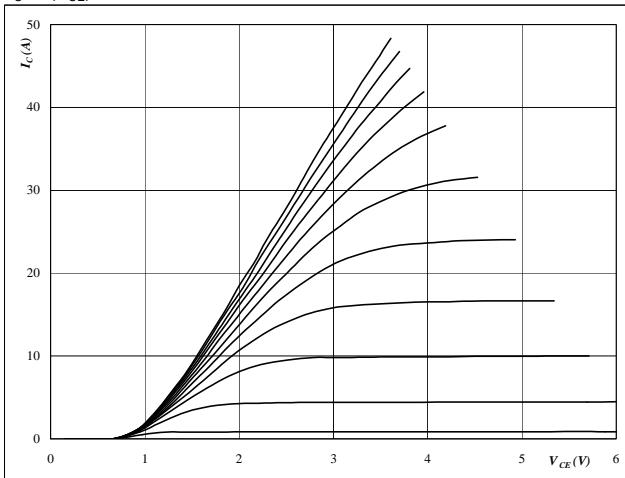
$$\begin{aligned} T_j &= T_{jmax} - 25 \quad ^\circ C \\ U_{ccminus} &= U_{ccplus} \end{aligned}$$

Switching mode : 3phase SPWM

Brake

Figure 1
Typical output characteristics

$$I_C = f(V_{CE})$$


At

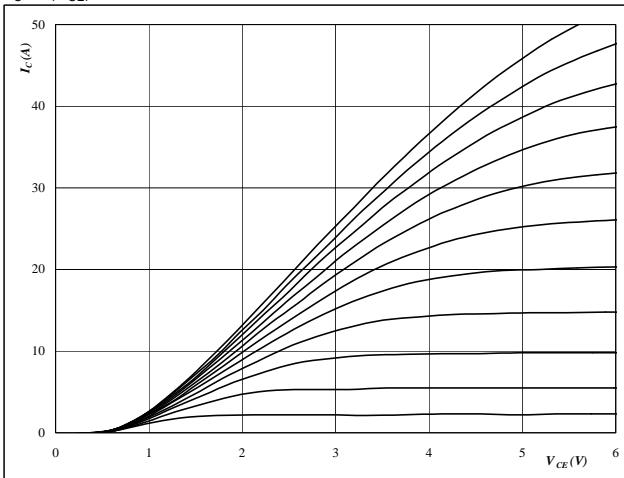
$$t_p = 250 \text{ } \check{\text{o}}\text{s}$$

$$T_j = 25 \text{ } ^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Brake IGBT
Figure 2
Typical output characteristics

$$I_C = f(V_{CE})$$


At

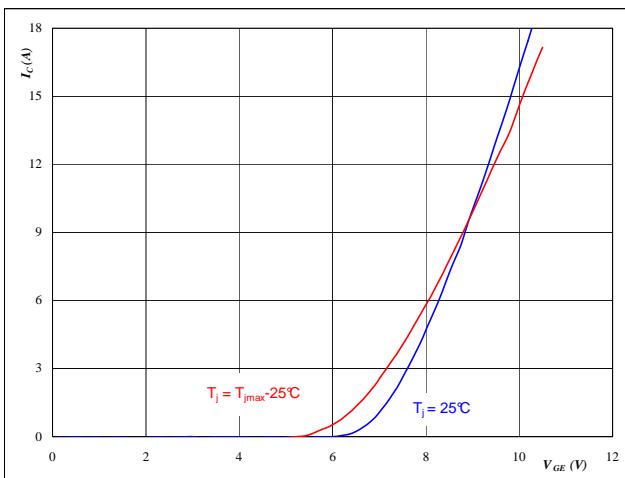
$$t_p = 250 \text{ } \check{\text{o}}\text{s}$$

$$T_j = 150 \text{ } ^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Brake IGBT
Typical transfer characteristics

$$I_C = f(V_{GE})$$

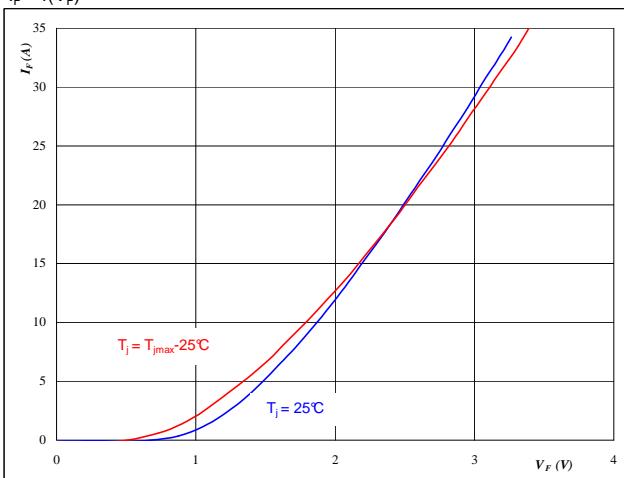

At

$$t_p = 250 \text{ } \check{\text{o}}\text{s}$$

$$V_{CE} = 10 \text{ } \text{V}$$

Figure 4
Brake FWD
Typical diode forward current as
a function of forward voltage

$$I_F = f(V_F)$$


At

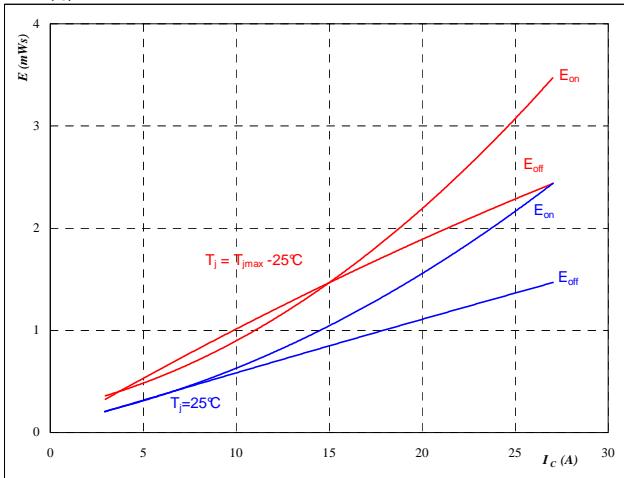
$$t_p = 250 \text{ } \check{\text{o}}\text{s}$$

Brake

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



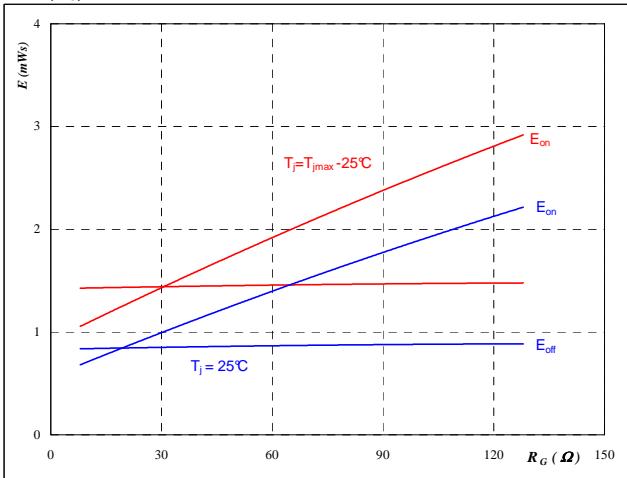
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 32 \quad \Omega \\ R_{goff} &= 32 \quad \Omega \end{aligned}$$

Brake IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



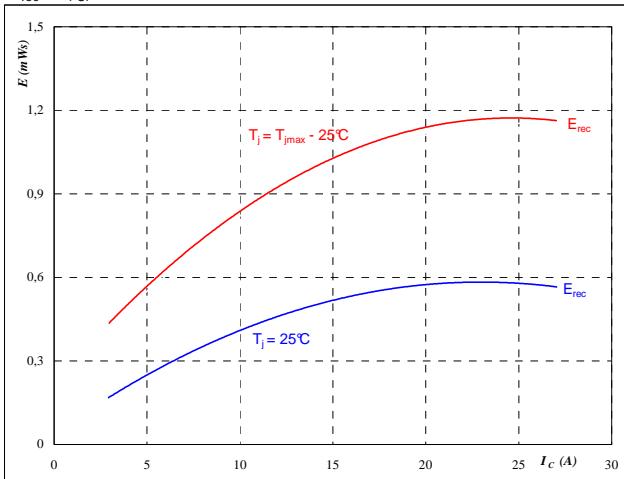
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 15 \quad A \end{aligned}$$

Figure 7

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



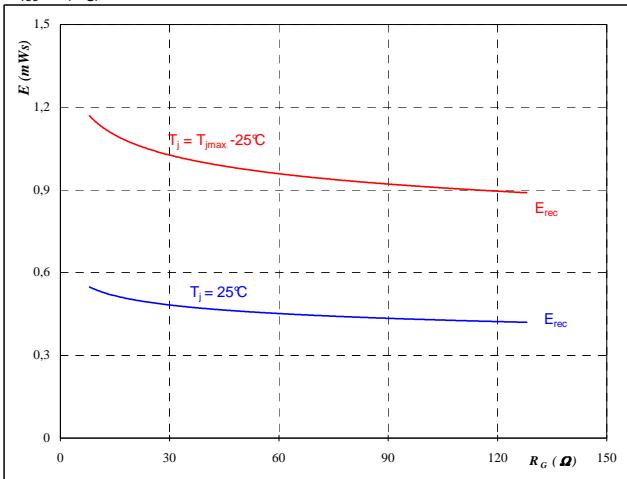
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

Brake FWD
Figure 8

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

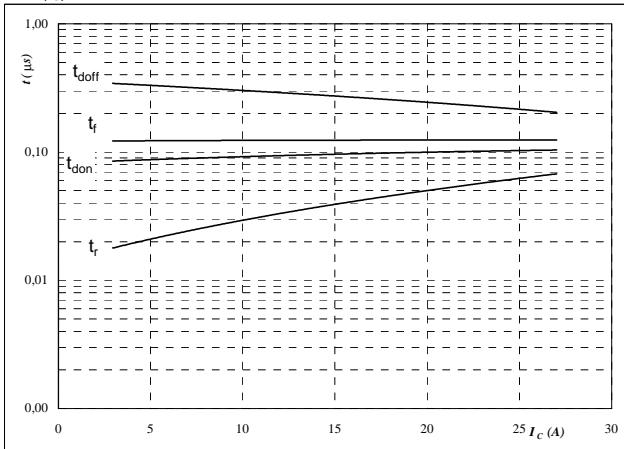
$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 15 \quad A \end{aligned}$$

Brake

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



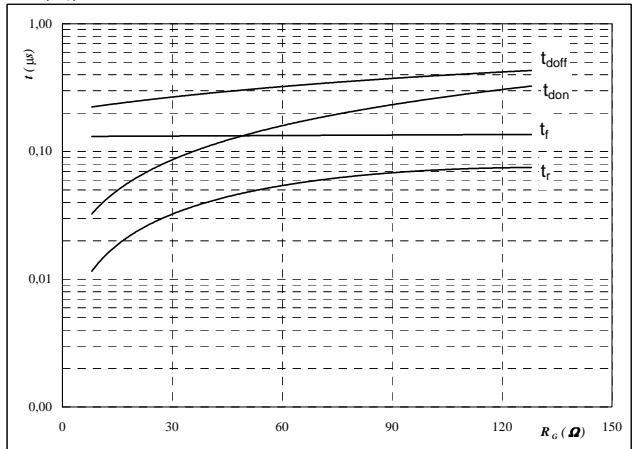
With an inductive load at

T _j =	25/150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
R _{gon} =	32	Ω
R _{goff} =	32	Ω

Brake IGBT
Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



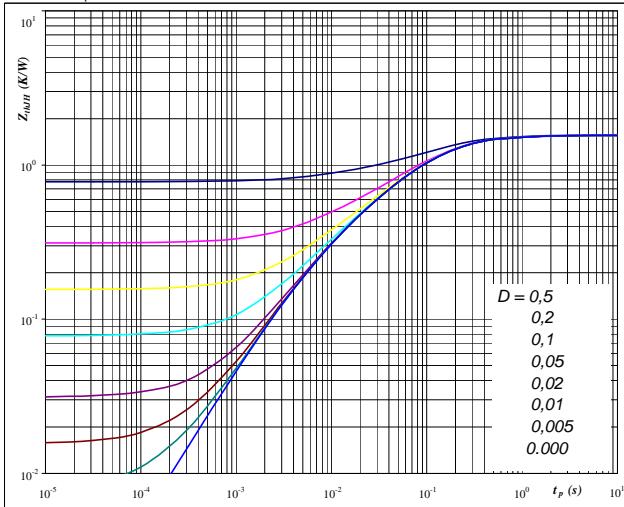
With an inductive load at

T _j =	25/150	°C
V _{CE} =	600	V
V _{GE} =	±15	V
I _C =	15	A

Figure 11
Brake IGBT

IGBT transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$

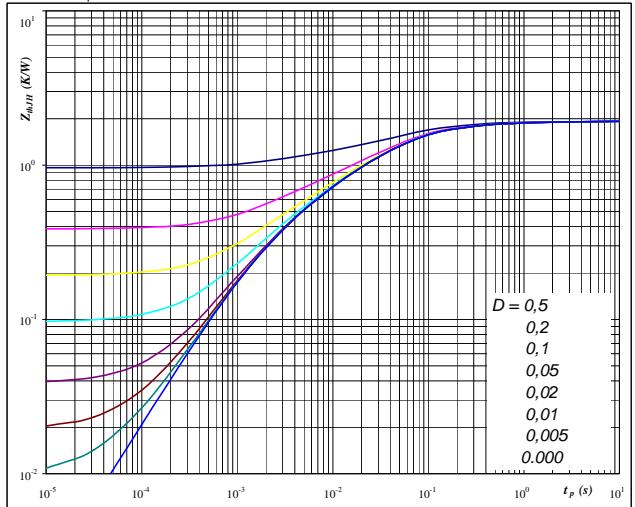


At	D =	tp / T	Phase change interface
Thermal grease	D = 0.5	tp / T = 1.56	Phase change interface

Figure 12
Brake FWD

FWD transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



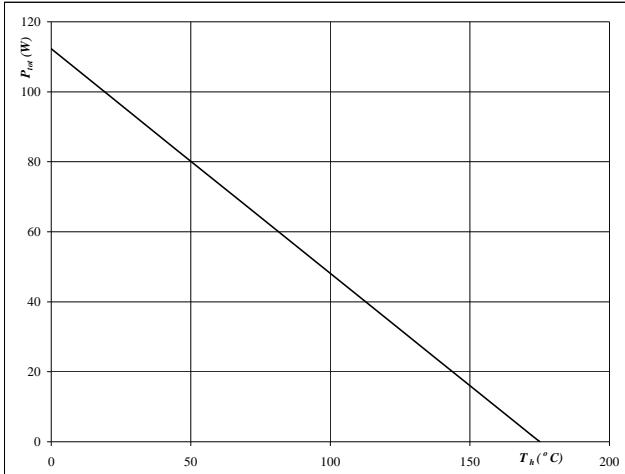
At	D =	tp / T	Phase change interface
Thermal grease	D = 0.5	tp / T = 1.92	Phase change interface

Brake

Figure 13

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

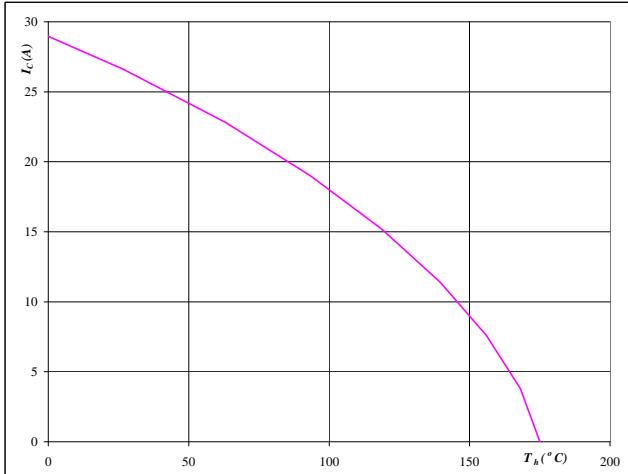

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake IGBT
Figure 14

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

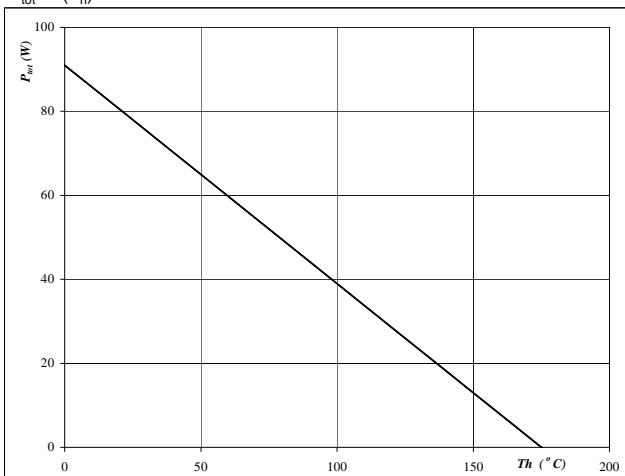
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 15

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

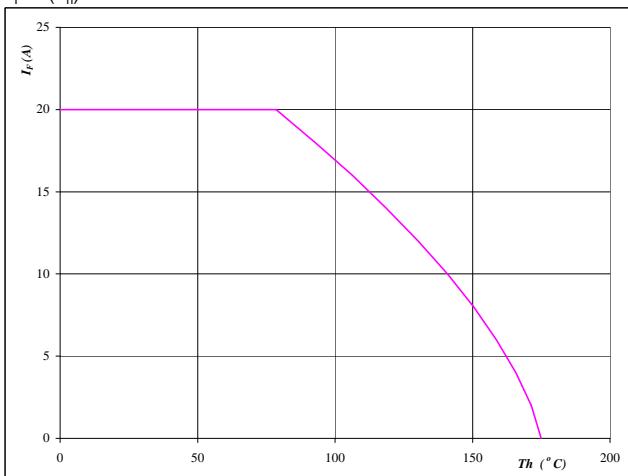

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake FWD
Figure 16

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 175 \quad {}^\circ\text{C}$$

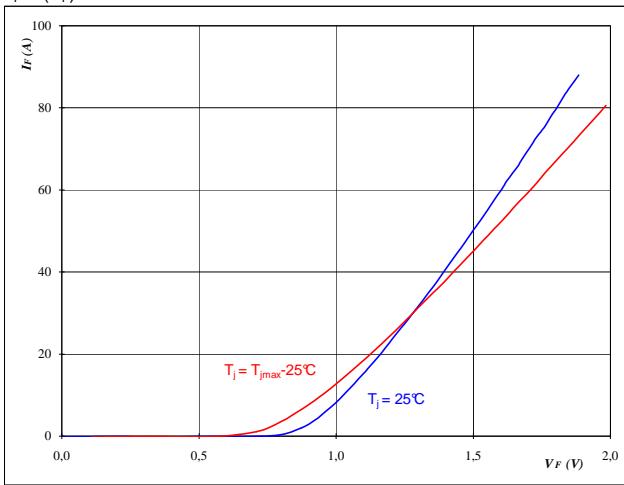
$$T_j = 175 \quad {}^\circ\text{C}$$

Input Rectifier Bridge

Figure 1

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

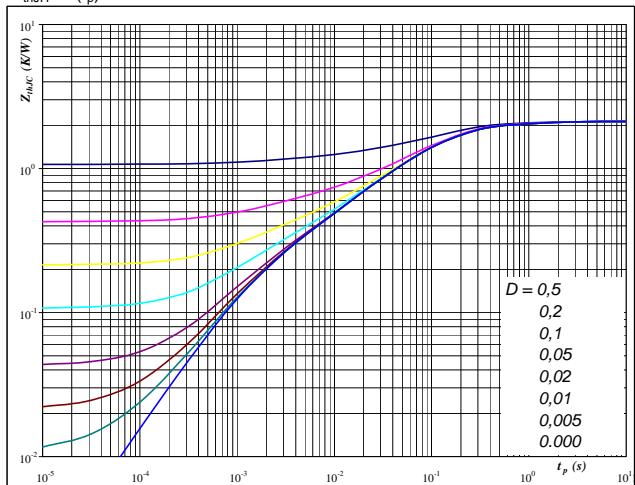

At

$$t_p = 250 \text{ } \mu\text{s}$$

Rectifier diode
Figure 2

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

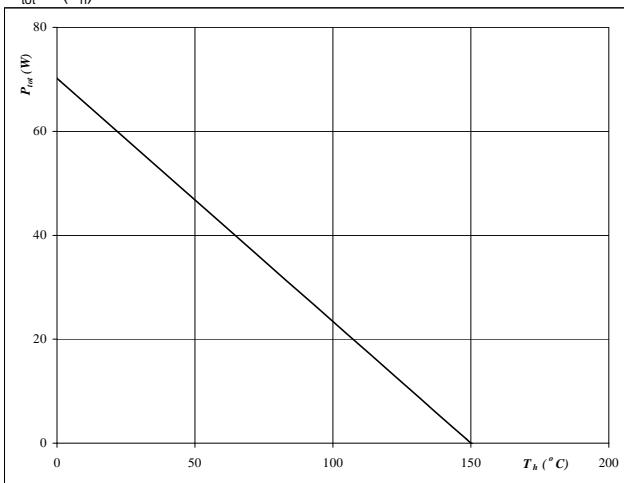
$$D = t_p / T$$

$$R_{thJH} = 2.14 \text{ K/W}$$

Figure 3

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

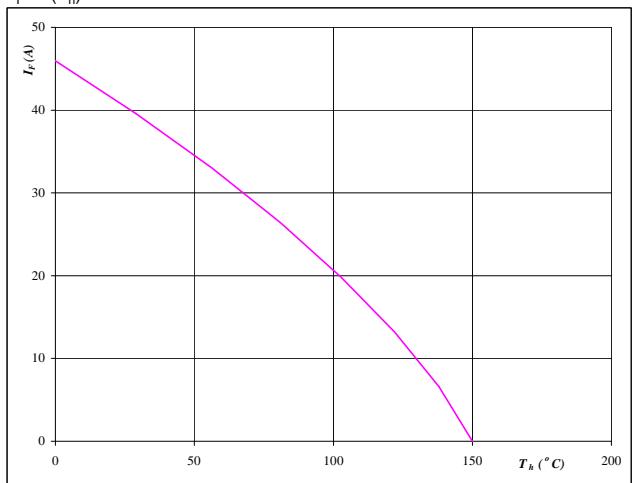

At

$$T_j = 150 \text{ } ^\circ\text{C}$$

Rectifier diode
Figure 4

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 150 \text{ } ^\circ\text{C}$$

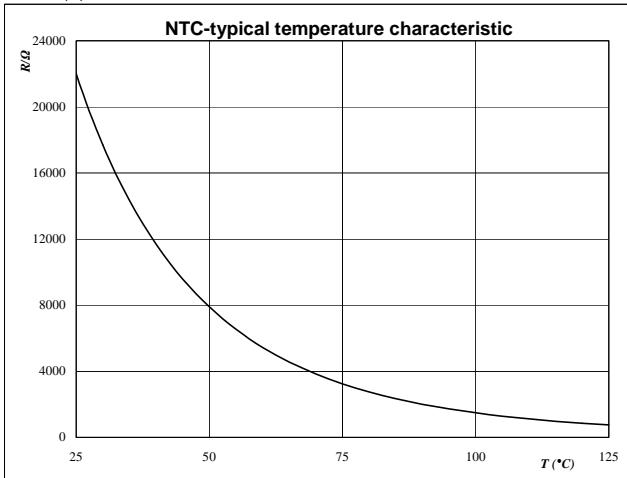
Thermistor

Figure 1

Thermistor

Typical NTC characteristic
as a function of temperature

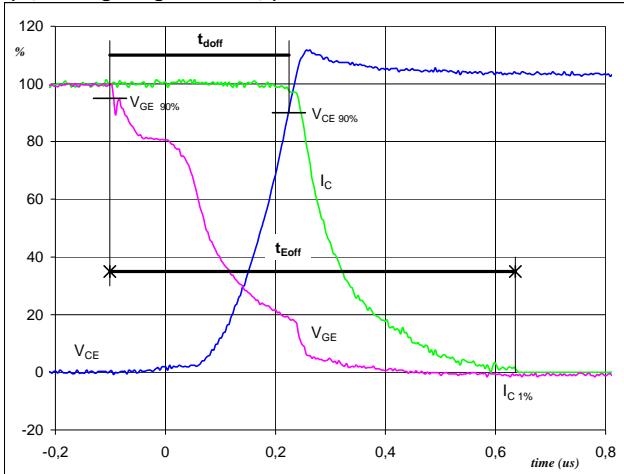
$$R_T = f(T)$$



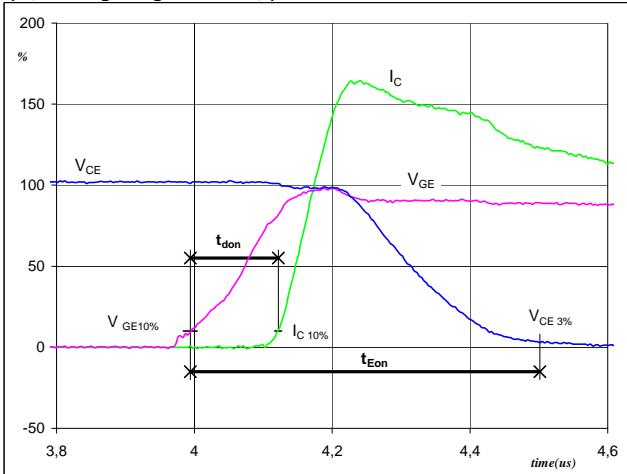
Switching Definitions Output Inverter

General conditions

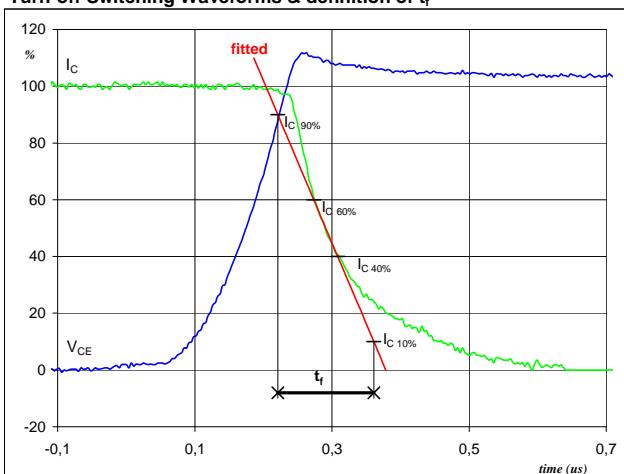
T_j	=	150 °C
R_{gon}	=	32 Ω
R_{goff}	=	32 Ω

Figure 1
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$


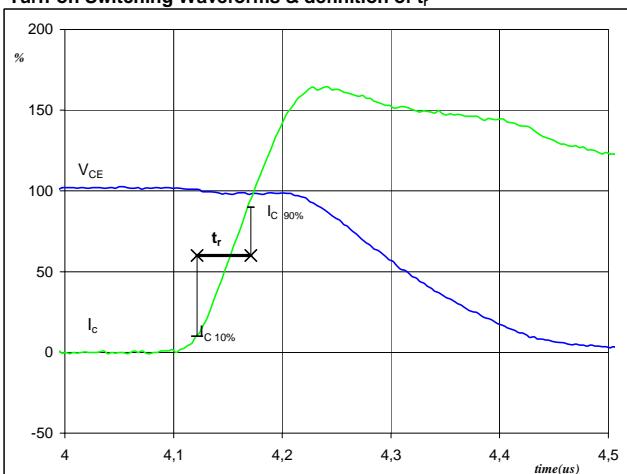
$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 25$ A
 $t_{doff} = 0,32$ Šs
 $t_{Eoff} = 0,74$ Šs

Figure 2
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 25$ A
 $t_{don} = 0,13$ Šs
 $t_{Eon} = 0,51$ Šs

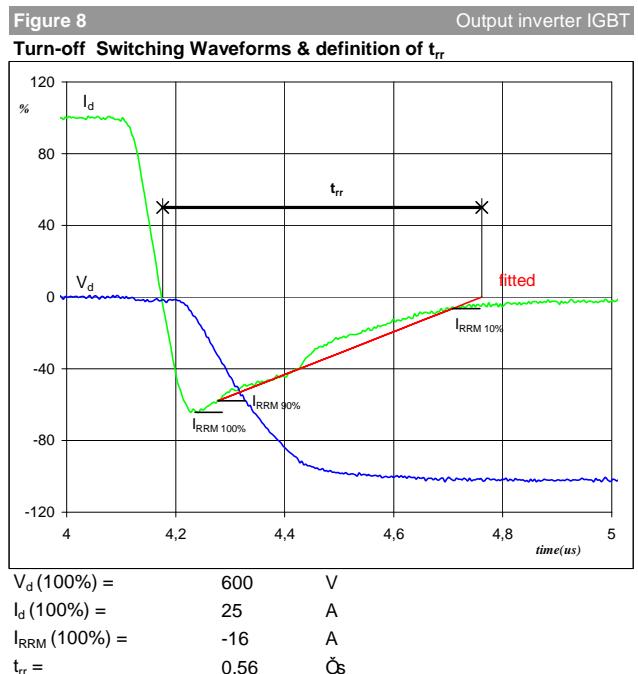
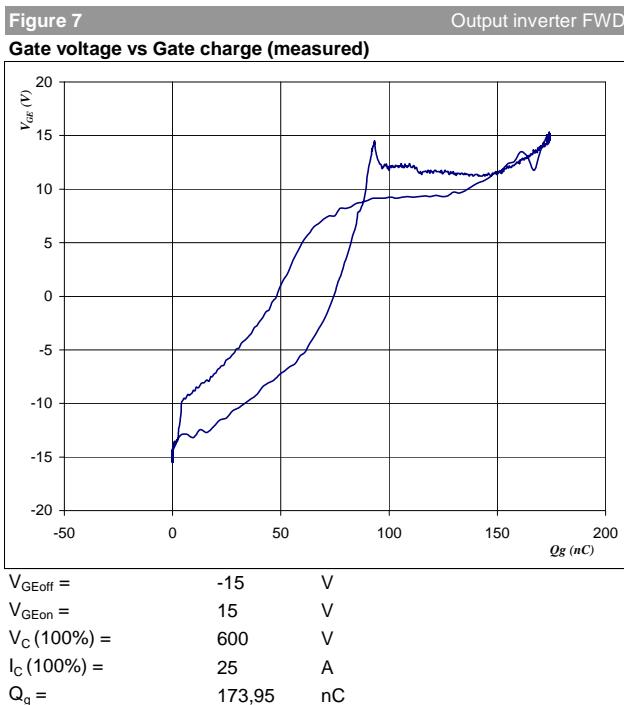
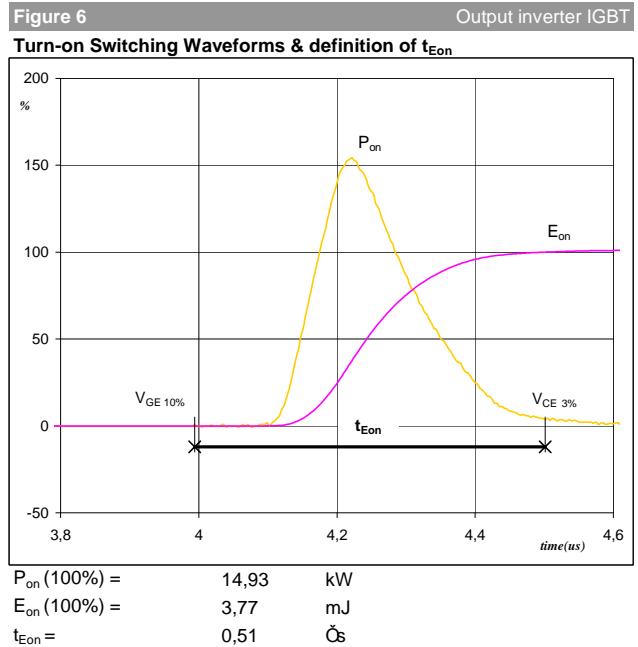
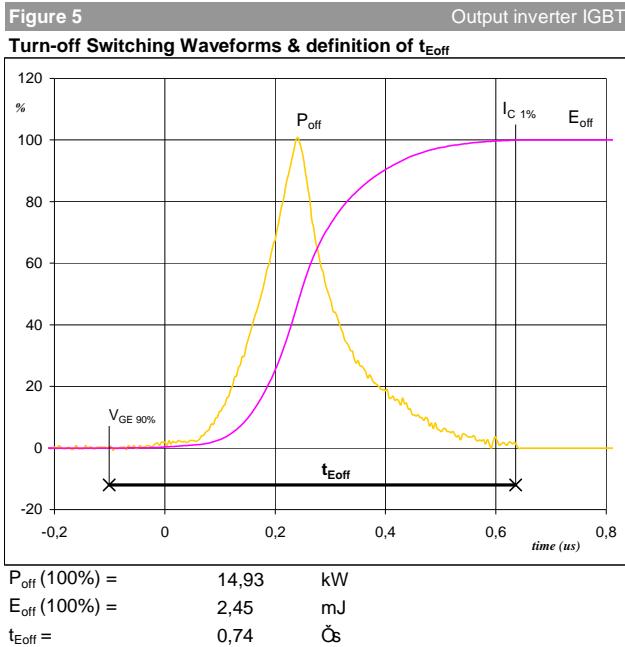
Figure 3
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_f


$V_C(100\%) = 600$ V
 $I_C(100\%) = 25$ A
 $t_f = 0,14$ Šs

Figure 4
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_r


$V_C(100\%) = 600$ V
 $I_C(100\%) = 25$ A
 $t_r = 0,05$ Šs

Switching Definitions Output Inverter

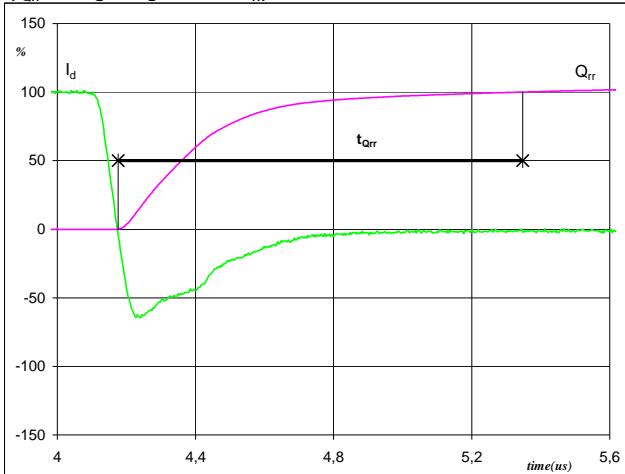


Switching Definitions Output Inverter

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

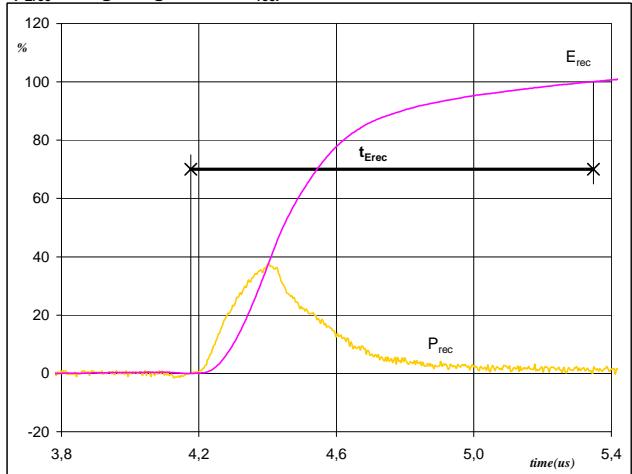


$I_d(100\%) = 25 \text{ A}$
 $Q_{rr}(100\%) = 4,68 \text{ }\ddot{\text{C}}\text{C}$
 $t_{Qrr} = 1,17 \text{ }\ddot{\text{C}}\text{s}$

Figure 10

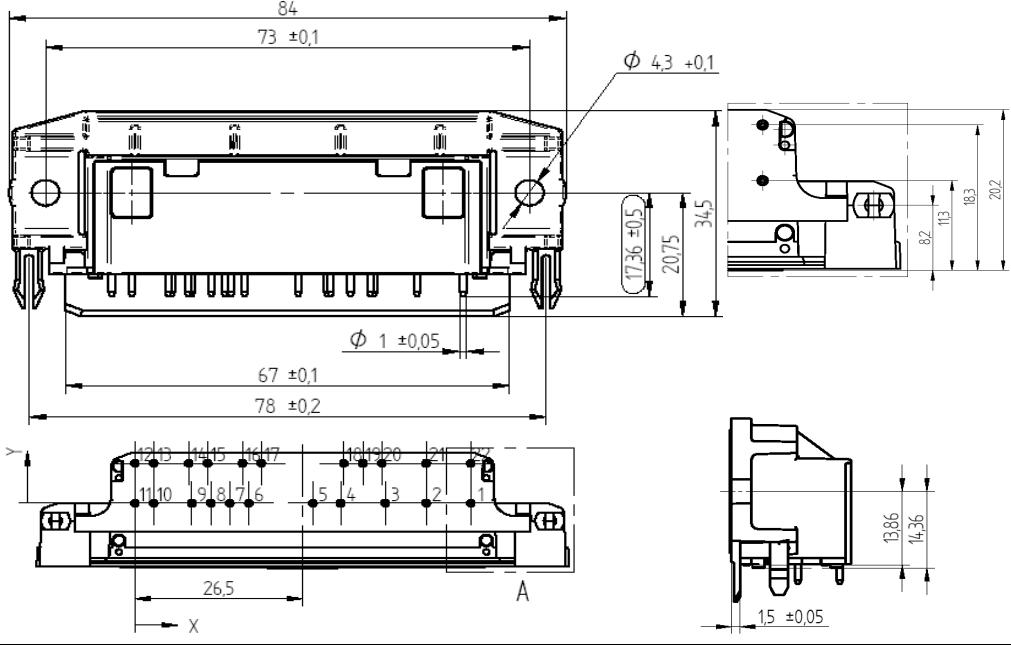
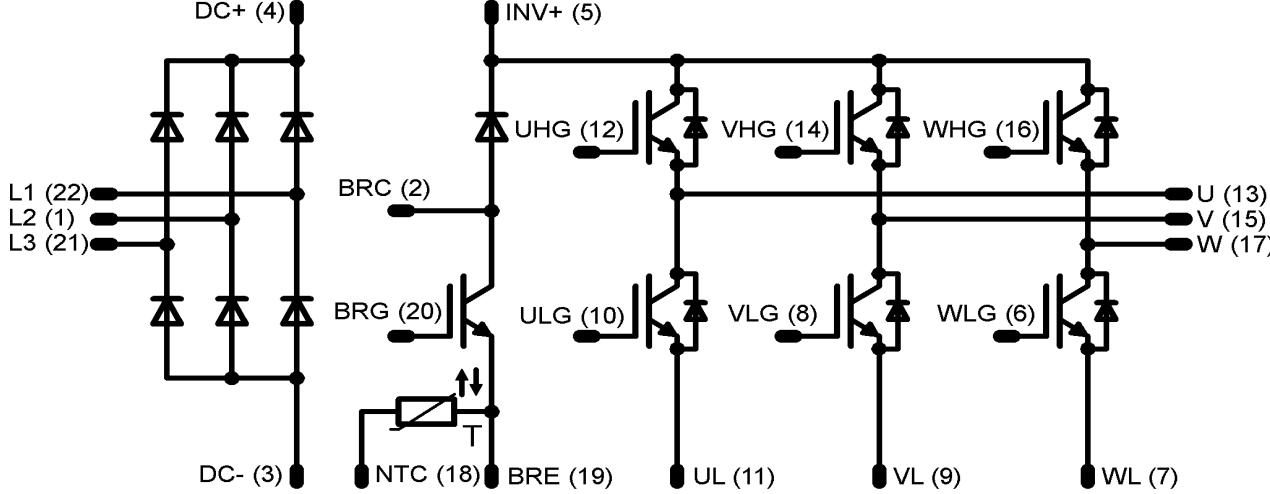
Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$P_{rec}(100\%) = 14,93 \text{ kW}$
 $E_{rec}(100\%) = 1,74 \text{ mJ}$
 $t_{Erec} = 1,17 \text{ }\ddot{\text{C}}\text{s}$

Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking																																																																											
Version	Ordering Code	in DataMatrix as	in packaging barcode as																																																																								
without thermal paste	V23990-P630-A44-PM	P630-A44	P630-A44																																																																								
Outline																																																																											
<table border="1"> <thead> <tr> <th colspan="3">Pin table</th></tr> <tr> <th>Pin</th><th>X</th><th>Y</th></tr> </thead> <tbody> <tr><td>1</td><td>53</td><td>0</td></tr> <tr><td>2</td><td>46</td><td>0</td></tr> <tr><td>3</td><td>39,5</td><td>0</td></tr> <tr><td>4</td><td>32,5</td><td>0</td></tr> <tr><td>5</td><td>28,1</td><td>0</td></tr> <tr><td>6</td><td>18</td><td>0</td></tr> <tr><td>7</td><td>15</td><td>0</td></tr> <tr><td>8</td><td>12</td><td>0</td></tr> <tr><td>9</td><td>9</td><td>0</td></tr> <tr><td>10</td><td>3</td><td>0</td></tr> <tr><td>11</td><td>0</td><td>0</td></tr> <tr><td>12</td><td>0</td><td>7</td></tr> <tr><td>13</td><td>3</td><td>7</td></tr> <tr><td>14</td><td>8,5</td><td>7</td></tr> <tr><td>15</td><td>11,5</td><td>7</td></tr> <tr><td>16</td><td>17</td><td>7</td></tr> <tr><td>17</td><td>20</td><td>7</td></tr> <tr><td>18</td><td>33</td><td>7</td></tr> <tr><td>19</td><td>36</td><td>7</td></tr> <tr><td>20</td><td>39</td><td>7</td></tr> <tr><td>21</td><td>46</td><td>7</td></tr> <tr><td>22</td><td>53</td><td>7</td></tr> </tbody> </table> 				Pin table			Pin	X	Y	1	53	0	2	46	0	3	39,5	0	4	32,5	0	5	28,1	0	6	18	0	7	15	0	8	12	0	9	9	0	10	3	0	11	0	0	12	0	7	13	3	7	14	8,5	7	15	11,5	7	16	17	7	17	20	7	18	33	7	19	36	7	20	39	7	21	46	7	22	53	7
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DISCLAIMER

The information given in this datasheet describes the type of component and does not represent assured characteristics. For tested values please contact Vincotech. Vincotech reserves the right to make changes without further notice to any products herein to improve reliability, function or design. Vincotech does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights, nor the rights of others.

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As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.