**Linear dimensions**

- Chamfers, radii:
  - $30 \leq x < 120 \pm 0.3 \, 
  - $6 \leq x < 30 \pm 0.2 \, 
  - $x < 0.5 \pm 0.05 \, 
  - $0.3 \leq x < 0.5 \pm 0.1 \, 
  - $0.5 \leq x < 3 \pm 0.2 \, 
  - $3 \leq x < 6 \pm 0.5 \, 
  - $120 < x \pm 0.5 \, 
  - $6 < x \pm 1 \, 

**Notes:**
- Proprietary data, company confidential. All rights reserved.
- PCB specification according to the actual handling instruction document.
- Refer to the module datasheet for actual pinout and 3D-CAD model.
- Tolerance of pin positions ±0.5mm at the end of the pins.
- All dimensions are considered under unmounted condition.

**References:**
- IP Development
- Vytech

**Design Details:**
- Driver PCB (only for illustration, not included)
- Capacitor area
- Pinout area
- Only convex: $0.25 \pm 0.15$ for each baseplate