Vincotech

V23990-P717-H10-PM
datasheet

*flow*90CON 1	1600 V / 36 A

Topology features

- Three-phase Half Controlled Converter

Component features

- High inrush current capability

Housing features

- Base isolation: Al_2O_3
- 90° mounting angle between heatsink and PCB
- Screw-on heatsink mounting
- Clip-in PCB mounting
- Thermo-mechanical push-and-pull force relief
- Solder pin

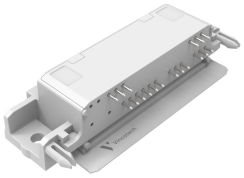
Target applications

- Motor drives
- Servo drives

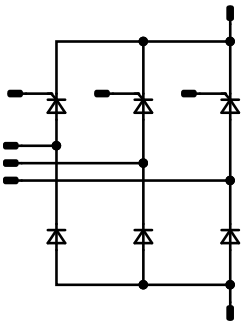
Types

- V23990-P717-H10-PM

*flow*90 1 housing



Schematic



Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Rectifier Thyristor					
Repetitive peak reverse voltage	V_{RRM}			1200	V
Maximum RMS on-state current	I_{TRMSM}	$T_j = T_{jmax}$	T_s = 80 °C	45	A
Surge on-state current	I_{TSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	360	A
I2t value	I^2t	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	650	A^2s
Mean total power loss	$P_{tot}(AV)$	$T_j = T_{jmax}$	T_s = 80 °C	68	W
Maximum Junction Temperature	T_{jmax}			150	°C
Rectifier Diode					
Peak repetitive reverse voltage	V_{RRM}			1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	63	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	520	A
Surge current capability	I^2t			1350	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	73	W
Maximum junction temperature	T_{jmax}			150	°C
Module Properties					
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	6000	V
Creepage distance				>12,7	mm
Clearance				11,84	mm
Comparative Tracking Index	CTI			≥ 200	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Rectifier Thyristor										
Static										
On-state voltage	V_T				45	25 125		1,54 1,66	1,4 1,45	V
Direct reverse current	I_{RD}	V_r = 1200 V				25 150			50 8000	µA
Holding current	I_H	I_T = A		6		25		100		mA
Latching current	I_L	t_p = 10 µs I_G = 0,3 A di_G/dt = 0,3 A/µs				25		150		mA
Gate trigger voltage	V_{GT}			6		25			1,5	V
Gate trigger current	I_{GT}			6		25			55	mA
Gate non-trigger current	I_{GD}					25			3000	nA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,02		K/W
Rectifier Diode										
Static										
Forward voltage	V_F				80	25 125 150		1,27 1,27	1,33(1) 1,31(1)	V
Reverse leakage current	I_R	V_r = 1600 V				25 150			20 1500	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,96		K/W

(1) Value at chip level

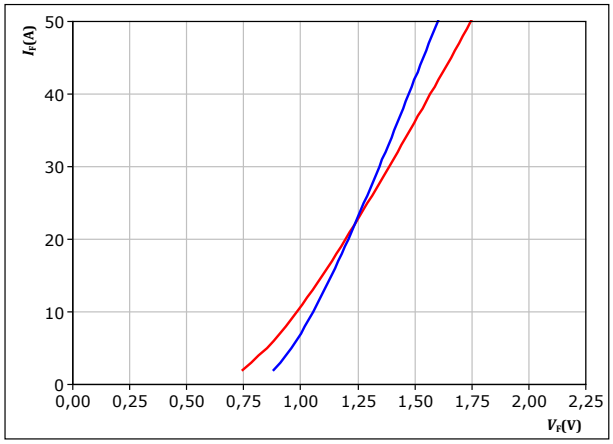
(2) Only valid with pre-applied Vincotech thermal interface material.

Rectifier Thyristor Characteristics

figure 1. **Thyristor**

Typical forward characteristics

$I_F = f(V_F)$



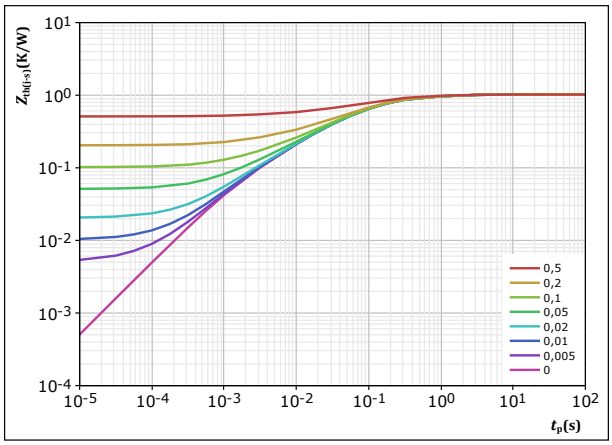
t_p = 250 µs

T_j: 25 °C, 125 °C

figure 2. **Thyristor**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T

$R_{th(j-s)}$ = 1,022 K/W

Thyristor thermal model values

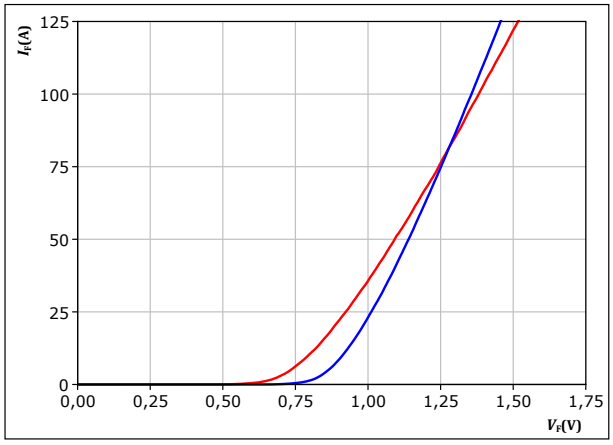
R (K/W)	τ (s)
1,01E-01	1,45E+00
2,12E-01	2,65E-01
4,76E-01	7,87E-02
1,89E-01	1,28E-02
4,45E-02	1,54E-03

Rectifier Diode Characteristics

figure 3. **Rectifier**

Typical forward characteristics

$I_F = f(V_F)$



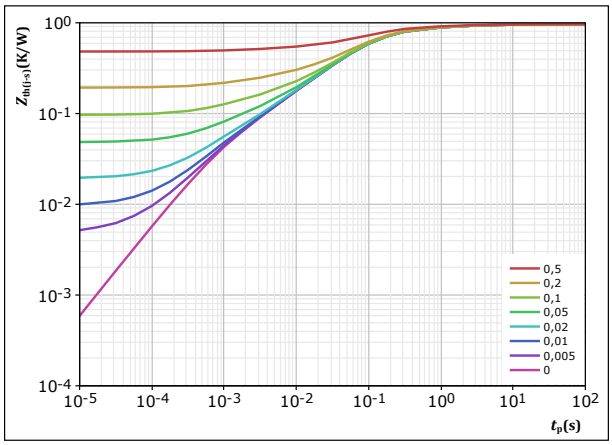
t_p = 250 µs

T_j: 25 °C, 125 °C

figure 4. **Rectifier**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T

$R_{th(j-s)}$ = 0,965 K/W

Rectifier thermal model values

R (K/W)	τ (s)
3,98E-02	7,88E+00
1,29E-01	8,64E-01
4,20E-01	1,32E-01
2,76E-01	4,24E-02
6,63E-02	5,80E-03
3,37E-02	8,90E-04

Ordering Code

Version	Ordering Code
Without thermal paste	V23990-P717-H10-PM
With thermal paste (3,4 W/mK, PSX-P7)	V23990-P717-H10-/3/-PM

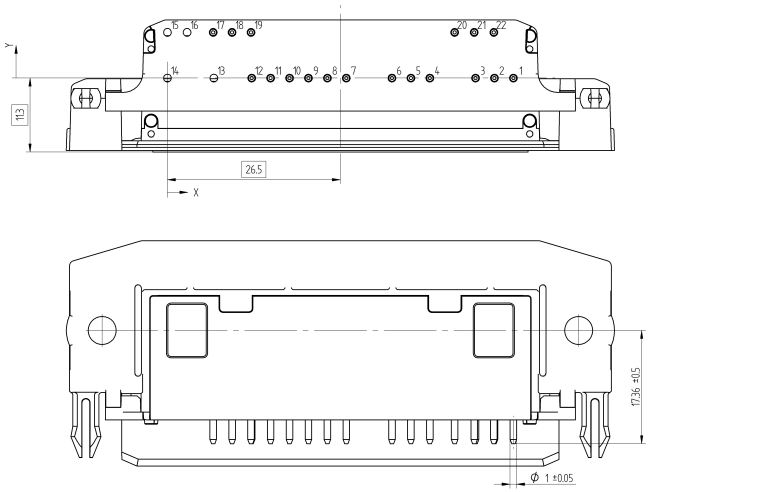
Marking

Text	**VIN**	**Date code**	**Type&Ver**	**UL**	**Lot**	**Serial**
	VIN	WWYY	TTTTTTVV	UL	LLLLL	SSSS
Datamatrix	**Type&Ver**	**Lot number**	**Serial**	**Date code**		
	TTTTTTVV	LLLLL	SSSS	WWYY		

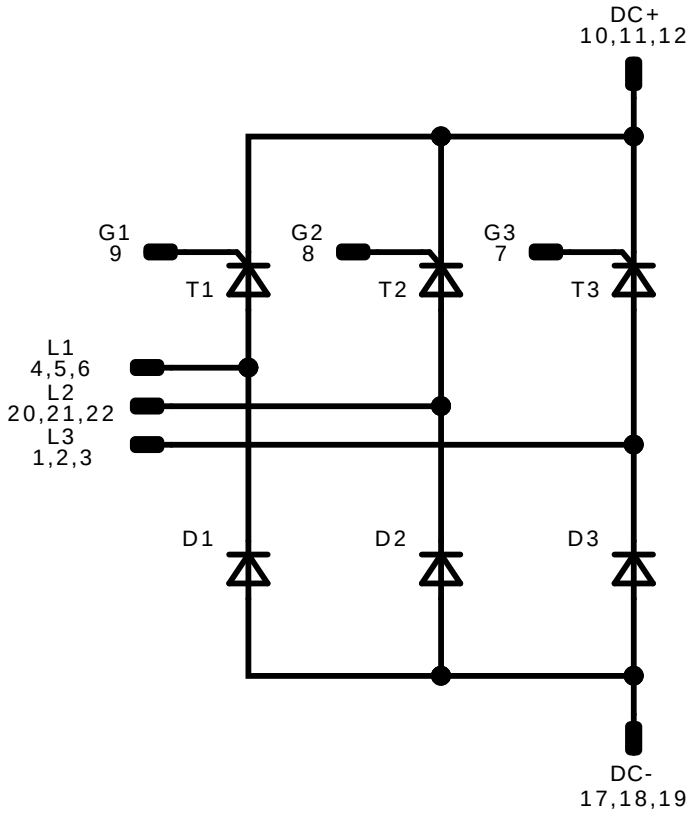
Outline

Pin table [mm]

Pin	X	Y	Function
1	53	0	L3
2	50,1	0	L3
3	47,2	0	L3
4	40,2	0	L1
5	37,3	0	L1
6	34,4	0	L1
7	27,4	0	G3
8	24,5	0	G2
9	21,6	0	G1
10	18,7	0	DC+
11	15,8	0	DC+
12	12,9	0	DC+
13	7,1	0	NA
14	0	0	NA
15	0	7	NA
16	3	7	NA
17	7	7	DC-
18	9,9	7	DC-
19	12,8	7	DC-
20	44	7	L2
21	47	7	L2
22	50	7	L2



Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T1, T2, T3	Thyristor	1200 V	45 A	Rectifier Thyristor	
D1, D2, D3	Rectifier	1600 V	42 A	Rectifier Diode	

Packaging instruction				
Standard packaging quantity (SPQ) 80	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow*90 1 packages see vincotech.com website.

Package data
Package data for *flow*90 1 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
V23990-P717-H10-PM-D5-14	25 Sep. 2022	New Datasheet format, module is unchanged Introduce Rth values with PSX-P7	

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