V23990-P634-A-PM
datasheet

***flow*90PIM 1** | **600 V / 20 A**

Topology features

- Converter+Brake+Inverter
- Open Emitter configuration
- Temperature sensor

Component features

- Easy paralleling
- Low turn-off losses
- Low collector emitter saturation voltage
- Positive temperature coefficient
- Short tail current

Housing features

- Base isolation: Al_2O_3
- 90° mounting angle between heatsink and PCB
- Screw-on heatsink mounting
- Clip-in PCB mounting
- Thermo-mechanical push-and-pull force relief
- Solder pin

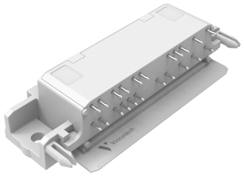
Target applications

- Industrial Drives

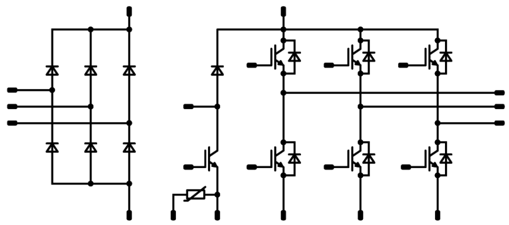
Types

- V23990-P634-A-PM

*flow*90 1 housing



Schematic



Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Inverter Switch					
Collector-emitter voltage	V_{CES}			600	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	26	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		60	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	56	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 360 V	T_j = 150 °C	6	µs
Maximum junction temperature	T_{jmax}			175	°C
Inverter Diode					
Peak repetitive reverse voltage	V_{RRM}			600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	23	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		40	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	37	W
Maximum junction temperature	T_{jmax}			175	°C
Brake Switch					
Collector-emitter voltage	V_{CES}			600	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	22	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		45	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	52	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 360 V	T_j = 150 °C	6	µs
Maximum junction temperature	T_{jmax}			175	°C

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Brake Diode					
Peak repetitive reverse voltage	V_{RRM}			600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s <= 80 °C	20 [1]	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		20	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	35	W
Maximum junction temperature	T_{jmax}			175	°C

(1) limited by I_{FRM}

Parameter	Symbol	Conditions		Value	Unit
Rectifier Diode					
Peak repetitive reverse voltage	V_{RRM}			1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	36	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	200	A
Surge current capability	I^2t	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	200	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	42	W
Maximum junction temperature	T_{jmax}			150	°C

Module Properties

Parameter	Symbol	Conditions		Value	Unit
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	4000	V
Creepage distance				>12,7	mm
Clearance				11,67	mm
Comparative Tracking Index	CTI			≥ 200	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions	V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	Unit
Inverter Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,00029	25	5	5,8	6,5	V
Collector-emitter saturation voltage	V_{CEsat}		15		20	25 125	1,1	1,55 1,75	1,9(2)	V
Collector-emitter cut-off current	I_{CES}		0	600		25			1,1	µA
Gate-emitter leakage current	I_{GES}		20	0		25			300	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		1100		pF
Output capacitance	C_{oes}							71		pF
Reverse transfer capacitance	C_{res}							32		pF
Gate charge	Q_g	V_{CC} = 480 V	0/15		20	25		120		nC
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,7		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 16 Ω R_{goff} = 16 Ω	±15	350	20	25 125 150		79,76 79,29 79,22		ns
Rise time	t_r					25 125 150		31,39 33,07 33,14		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		122,01 138,25 141,84		ns
Fall time	t_f					25 125 150		57,72 78,78 86,25		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=0,527 µC Q_{rFWD}=1,09 µC Q_{rFWD}=1,27 µC				25 125 150		0,43 0,579 0,618		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,485 0,631 0,666		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Diode										
Static										
Forward voltage	V_F				20	25 125 150	1,25	1,7 1,58 1,58	1,95(2)	V
Reverse leakage current	I_R	V_r = 600 V				25			27	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						2,6		K/W
Dynamic										
Peak recovery current	I_{RM}	di/dt=486 A/µs di/dt=529 A/µs di/dt=569 A/µs	±15	350	20	25 125 150		6,16 9,25 10,13		A
Reverse recovery time	t_{rr}					25 125 150		198,88 256,1 278,53		ns
Recovered charge	Q_r					25 125 150		0,527 1,09 1,27		µC
Reverse recovered energy	E_{rec}					25 125 150		0,128 0,264 0,306		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		29,25 64,12 74,21		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Brake Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,00021	25	5	5,8	6,5	V
Collector-emitter saturation voltage	V_{CEsat}		15		15	25 125	1,1	1,61 1,81	1,9(2)	V
Collector-emitter cut-off current	I_{CES}		0	600		25			0,85	μA
Gate-emitter leakage current	I_{GES}		20	0		25			300	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		800		pF
Output capacitance	C_{oes}							55		pF
Reverse transfer capacitance	C_{res}							24		pF
Gate charge	Q_g		0/15		0	25		87		nC
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,83		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 32 Ω R_{goff} = 32 Ω	0/15	400	15	25 125 150		38,5 37,1 36,6		ns
Rise time	t_r					25 125 150		26,64 27,89 28,32		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		292,59 306,37 312,41		ns
Fall time	t_f					25 125 150		54,88 68,53 72,6		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=0,366 μC Q_{rFWD}=0,588 μC Q_{rFWD}=0,69 μC				25 125 150		0,4 0,468 0,494		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,469 0,545 0,576		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Brake Diode										
Static										
Forward voltage	V_F				10	25 125	1,25	1,61 1,51	1,95(2)	V
Reverse leakage current	I_R	V_r = 600 V				25			27	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						2,7		K/W
Dynamic										
Peak recovery current	I_{RM}	d*i*/d*t*=470 A/µs d*i*/d*t*=588 A/µs d*i*/d*t*=515 A/µs	0/15	400	15	25 125 150		4,97 5,83 6,35		A
Reverse recovery time	t_{rr}					25 125 150		190,47 223,66 243		ns
Recovered charge	Q_r					25 125 150		0,366 0,588 0,69		µC
Reverse recovered energy	E_{rec}					25 125 150		0,094 0,153 0,181		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		17,98 17,57 19,52		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Rectifier Diode										
Static										
Forward voltage	V_F				18	25 125 150		1,06 0,994 0,973	1,5[2]	V
Reverse leakage current	I_R	V_r = 1600 V				25 150			100 1000	µA
Thermal										
Thermal resistance junction to sink[3]	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,65		K/W
Thermistor										
Static										
Rated resistance	R					25		22		kΩ
Deviation of R25	$\Delta_{R/R}$	R_{25} = 22 kΩ				25	-5		5	%
Deviation of R100	$\Delta_{R/R}$	R_{100} = 1486 Ω				100	-12		14	%
Power dissipation	P							200		mW
Power dissipation constant	d					25		2		mW/K
B-value	$B_{(25/50)}$	Tol. ±3 %						3950		K
B-value	$B_{(25/100)}$	Tol. ±3 %						3998		K
Vincotech Thermistor Reference									B	

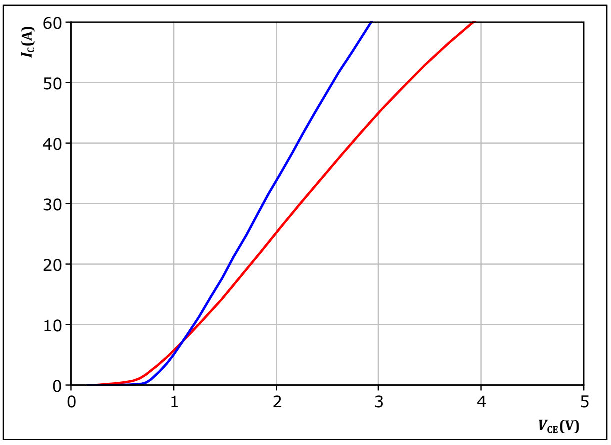
(2) Value at chip level
(3) Only valid with pre-applied Vincotech thermal interface material.

Inverter Switch Characteristics

figure 1. **IGBT**

Typical output characteristics

I_C = f(V_{CE})

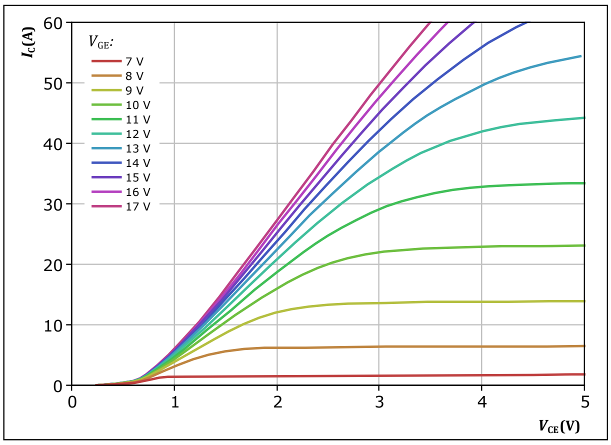


t_p =	250	μs	T_j:	25 °C
V_{GE} =	15	V		125 °C

figure 2. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



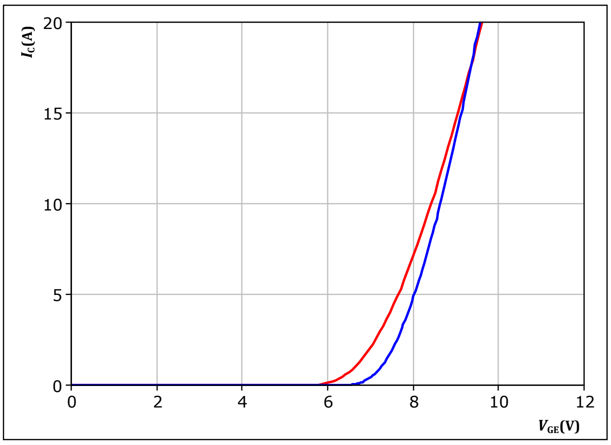
t_p =	250	μs
T_j =	125	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})

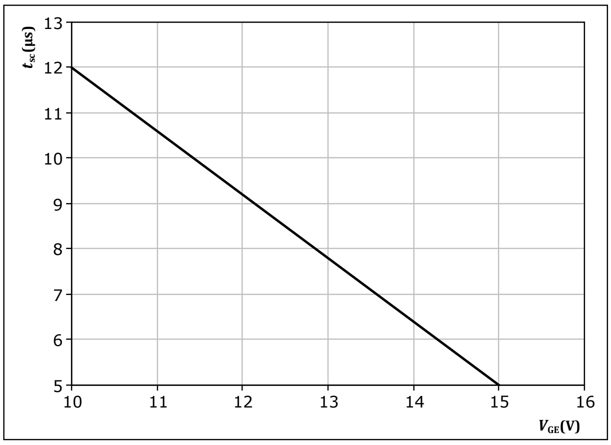


t_p =	250	μs	T_j:	25 °C
V_{CE} =	10	V		125 °C

figure 4. **IGBT**

Short circuit withstand time as a function of V_{GE}

t_{sc} = f(V_{GE})



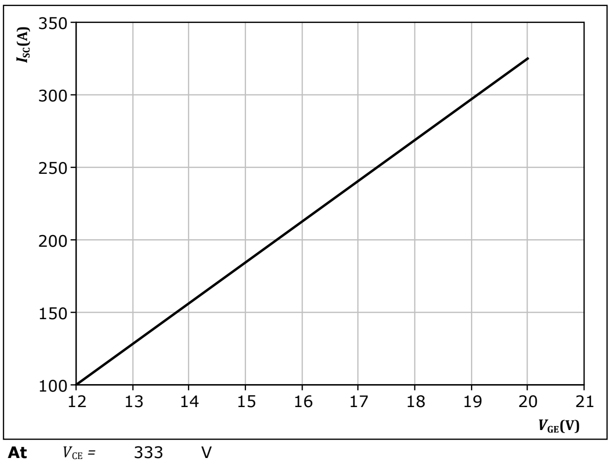
At	V_{CE} =	333	V
	$T_j \leq$	333	°C

Inverter Switch Characteristics

figure 5. **IGBT**

Typical short circuit current as a function of V_{GE}

I_{SC} = f(V_{GE})

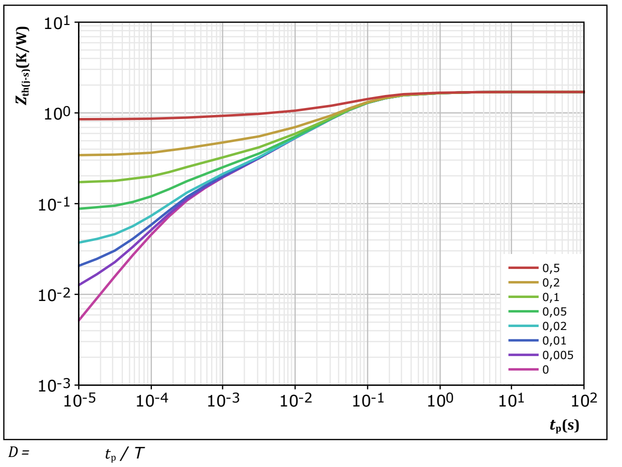


At	V_{CE} =	333	V
	T_j ≤	333	°C

figure 6. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,701 K/W

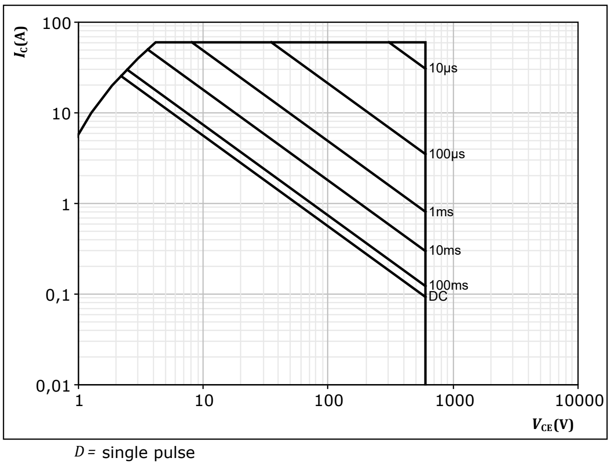
IGBT thermal model values

R (K/W)	τ (s)
9,97E-02	1,34E+00
3,46E-01	1,70E-01
8,15E-01	5,34E-02
2,54E-01	7,74E-03
7,70E-02	1,33E-03
1,09E-01	2,63E-04

figure 7. **IGBT**

Safe operating area

I_C = f(V_{CE})

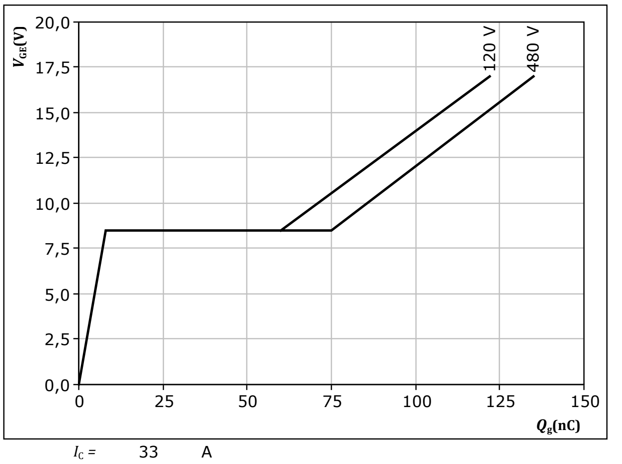


D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

figure 8. **IGBT**

Gate voltage vs gate charge

V_{GE} = f(Q_g)



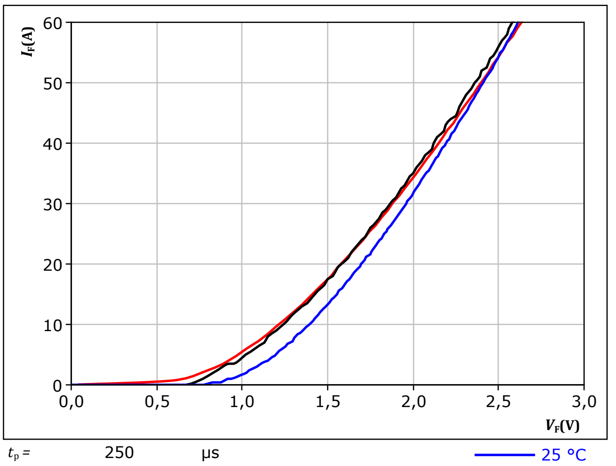
I_C =	33	A
T_j =	25	°C

Inverter Diode Characteristics

figure 9. **FWD**

Typical forward characteristics

I_F = f(V_F)



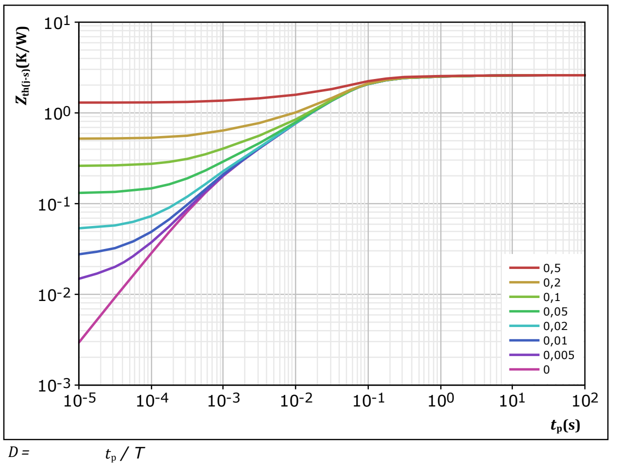
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 10. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 2,598 K/W

FWD thermal model values

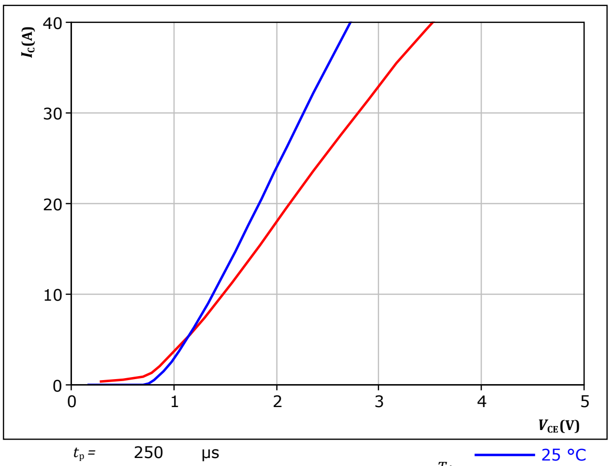
R (K/W)	τ (s)
6,56E-02	4,59E+00
1,58E-01	5,68E-01
8,97E-01	8,41E-02
1,05E+00	3,28E-02
2,75E-01	4,96E-03
1,51E-01	7,65E-04

Brake Switch Characteristics

figure 11. **IGBT**

Typical output characteristics

$I_C = f(V_{CE})$



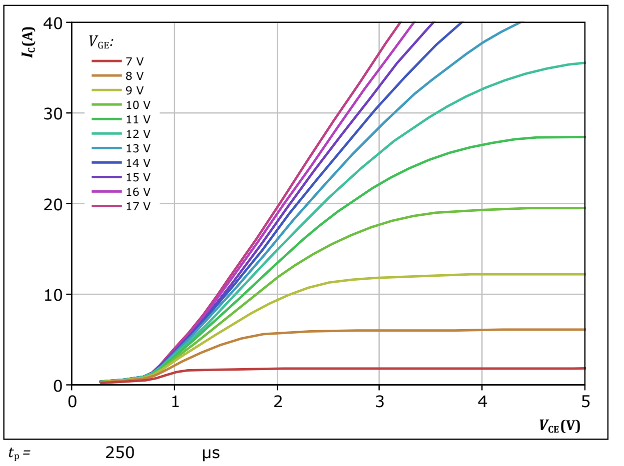
t_p =	250	µs
V_{GE} =	15	V

T_j: 25 °C (blue), 125 °C (red)

figure 12. **IGBT**

Typical output characteristics

$I_C = f(V_{CE})$



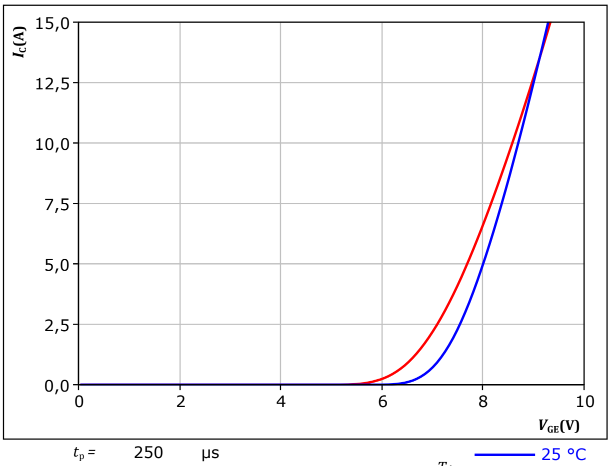
t_p =	250	µs
T_j =	125	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 13. **IGBT**

Typical transfer characteristics

$I_C = f(V_{GE})$



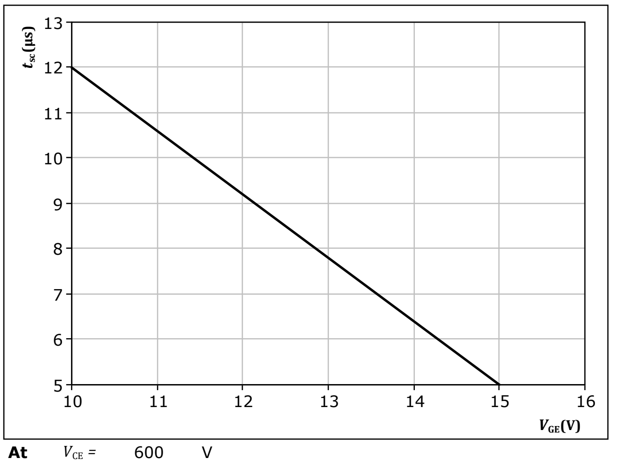
t_p =	250	µs
V_{CE} =	10	V

T_j: 25 °C (blue), 125 °C (red)

figure 14. **IGBT**

Short circuit withstand time as a function of V_{GE}

$t_{sc} = f(V_{GE})$



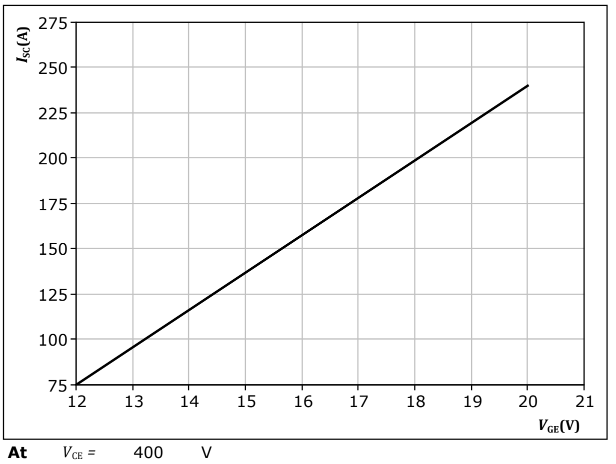
At	V_{CE} =	600	V
	T_j ≤	25	°C

Brake Switch Characteristics

figure 15. **IGBT**

Typical short circuit current as a function of V_{GE}

I_{SC} = f(V_{GE})

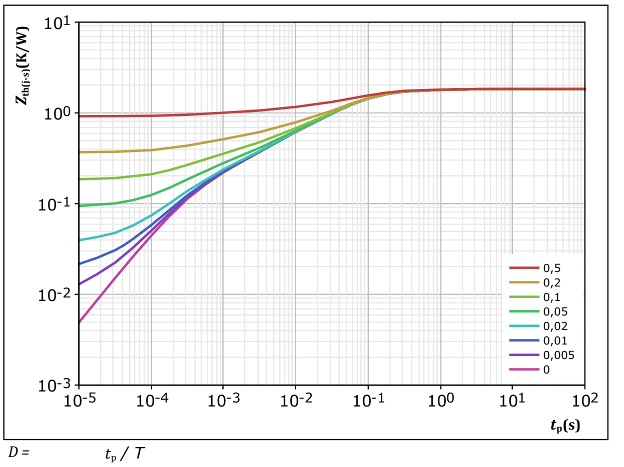


At	V_{CE} =	400	V
	T_j ≤	150	°C

figure 16. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,834 K/W

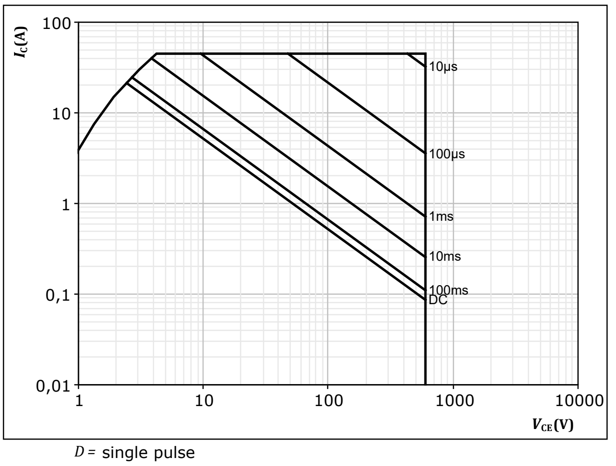
IGBT thermal model values

R (K/W)	τ (s)
8,30E-02	1,29E+00
3,76E-01	1,56E-01
8,46E-01	5,15E-02
2,81E-01	8,16E-03
1,16E-01	2,04E-03
1,32E-01	3,43E-04

figure 17. **IGBT**

Safe operating area

I_C = f(V_{CE})

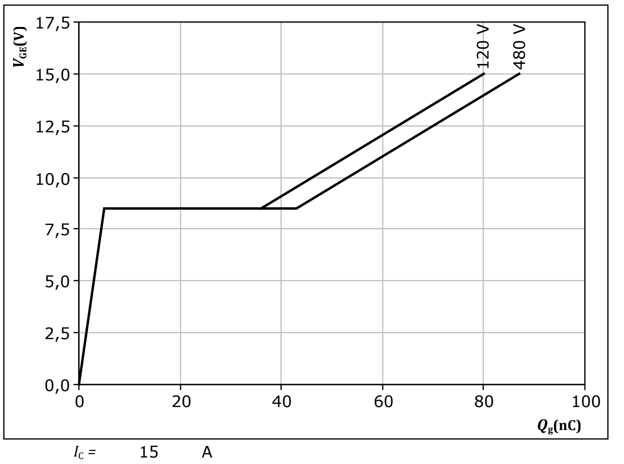


D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

figure 18. **IGBT**

Gate voltage vs gate charge

V_{GE} = f(Q_g)



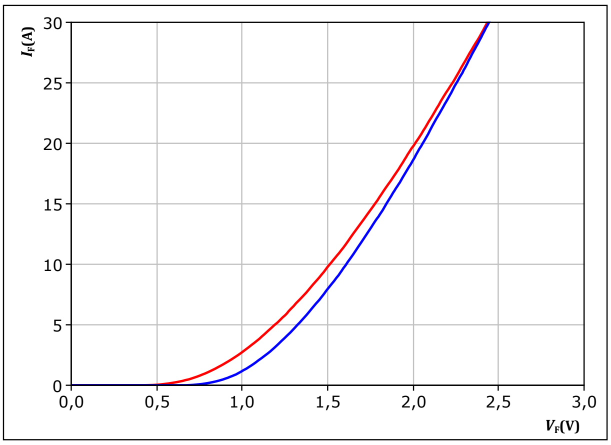
I_C =	15	A
T_j =	25	°C

Brake Diode Characteristics

figure 19. FWD

Typical forward characteristics

I_F = f(V_F)



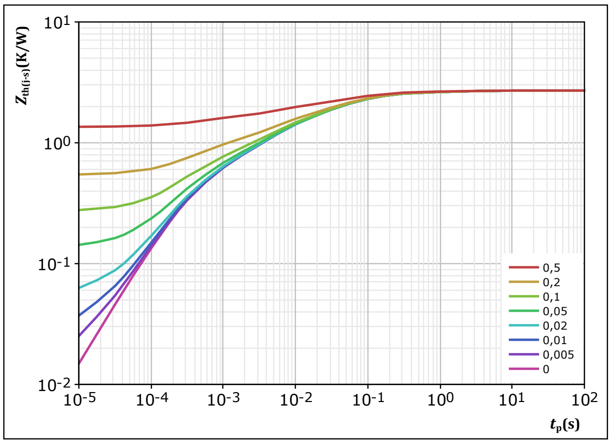
t_p = 250 μs

T_j: 25 °C, 125 °C

figure 20. FWD

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 2,705 K/W

FWD thermal model values

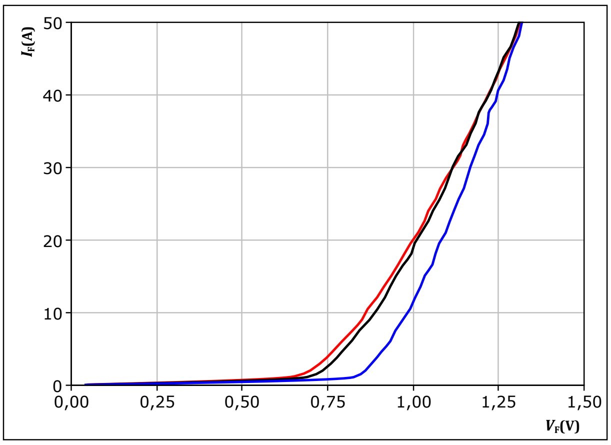
R (K/W)	τ (s)
1,24E-01	1,91E+00
5,91E-01	1,24E-01
8,55E-01	2,49E-02
6,96E-01	3,59E-03
4,39E-01	3,45E-04

Rectifier Diode Characteristics

figure 21. **Rectifier**

Typical forward characteristics

I_F = f(V_F)



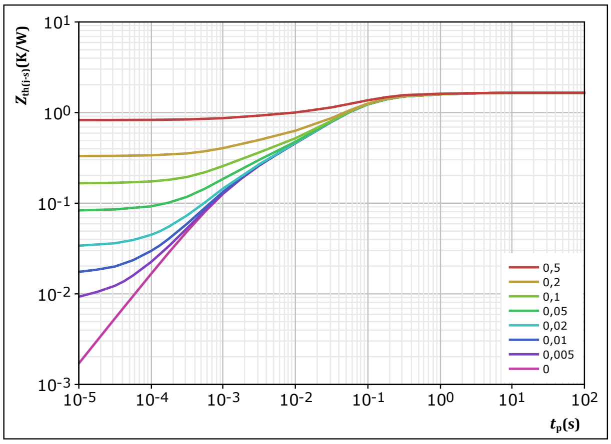
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 22. **Rectifier**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,654 K/W

Rectifier thermal model values

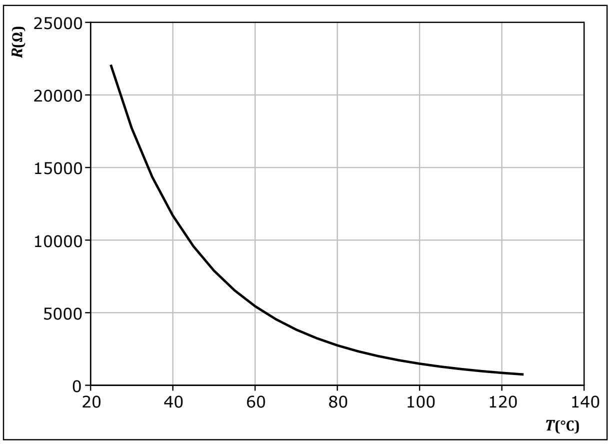
R (K/W)	τ (s)
1,01E-01	1,90E+00
2,72E-01	2,00E-01
9,08E-01	5,75E-02
2,21E-01	8,87E-03
1,52E-01	1,18E-03

Thermistor Characteristics

figure 23.	Thermistor

Typical NTC characteristic as function of temperature

R_T = f(T)

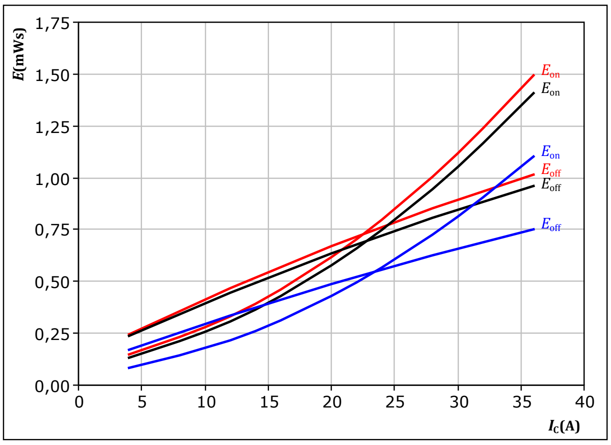


Inverter Switching Characteristics

figure 24. **IGBT**

Typical switching energy losses as a function of collector current

E = f(I_C)



With an inductive load at

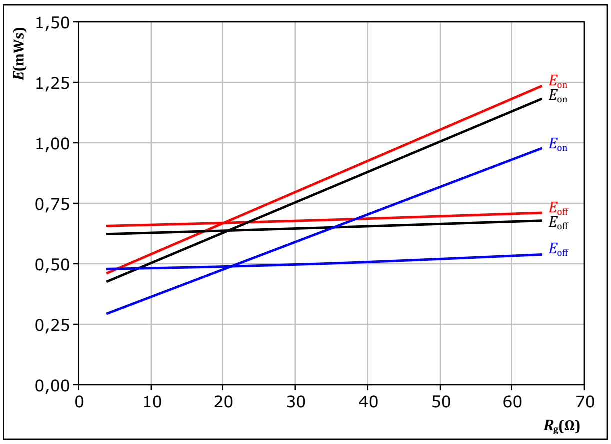
V_{CE} =	350	V	
V_{GE} =	±15	V	
R_{gon} =	16	Ω	
R_{goff} =	16	Ω	

T_j: 25 °C, 125 °C, 150 °C

figure 25. **IGBT**

Typical switching energy losses as a function of IGBT turn on gate resistor

E = f(R_g)



With an inductive load at

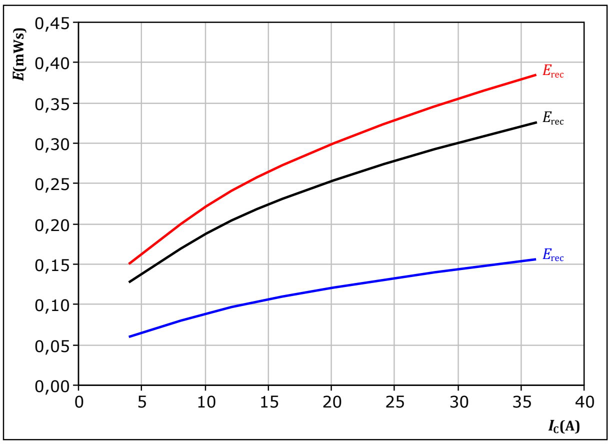
V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	20	A

T_j: 25 °C, 125 °C, 150 °C

figure 26. **FWD**

Typical reverse recovered energy loss as a function of collector current

E_{rec} = f(I_C)



With an inductive load at

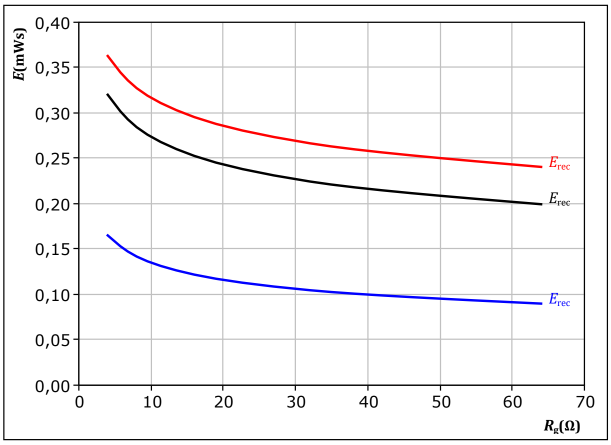
V_{CE} =	350	V
V_{GE} =	±15	V
R_{gon} =	16	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 27. **FWD**

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

E_{rec} = f(R_g)



With an inductive load at

V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	20	A

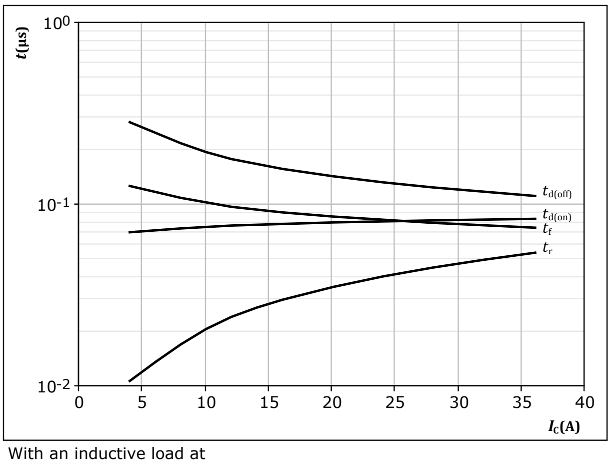
T_j: 25 °C, 125 °C, 150 °C

Inverter Switching Characteristics

figure 28. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



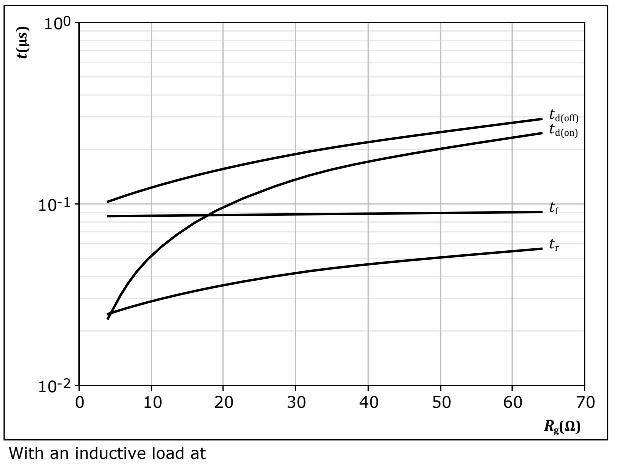
With an inductive load at

T_j =	150	°C
V_{CE} =	350	V
V_{GE} =	±15	V
R_{gon} =	16	Ω
R_{goff} =	16	Ω

figure 29. **IGBT**

Typical switching times as a function of IGBT turn on gate resistor

t = f(R_g)



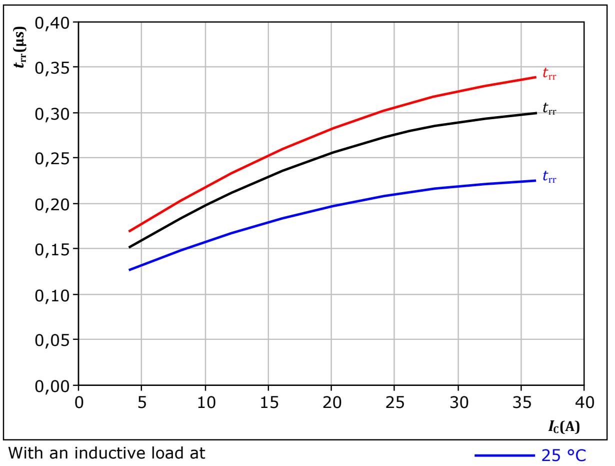
With an inductive load at

T_j =	150	°C
V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	20	A

figure 30. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

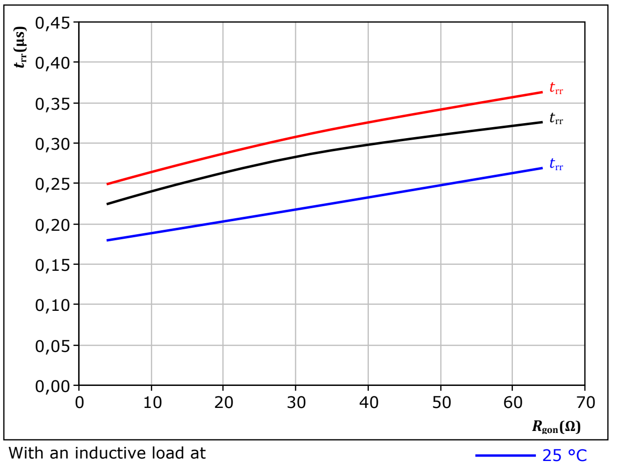
V_{CE} =	350	V
V_{GE} =	±15	V
R_{gon} =	16	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 31. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



With an inductive load at

V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	20	A

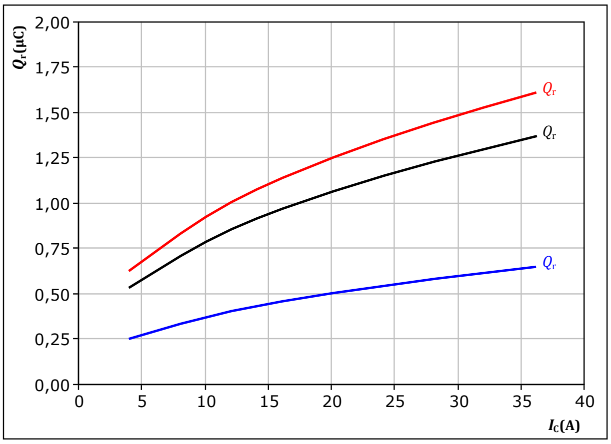
T_j: 25 °C, 125 °C, 150 °C

Inverter Switching Characteristics

figure 32. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



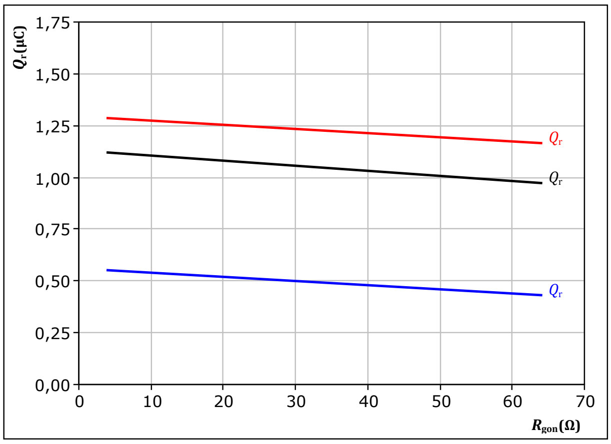
With an inductive load at

V_{CE} =	350	V		T_j:	25 °C
V_{GE} =	±15	V			125 °C
R_{gon} =	16	Ω			150 °C

figure 33. **FWD**

Typical recovered charge as a function of IGBT turn on gate resistor

Q_r = f(R_{gon})



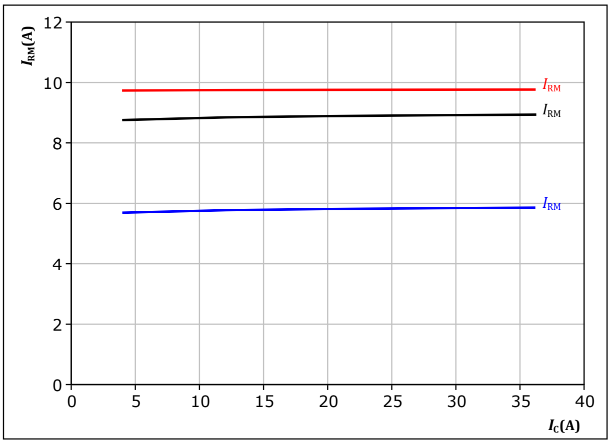
With an inductive load at

V_{CE} =	350	V		T_j:	25 °C
V_{GE} =	±15	V			125 °C
I_C =	20	A			150 °C

figure 34. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



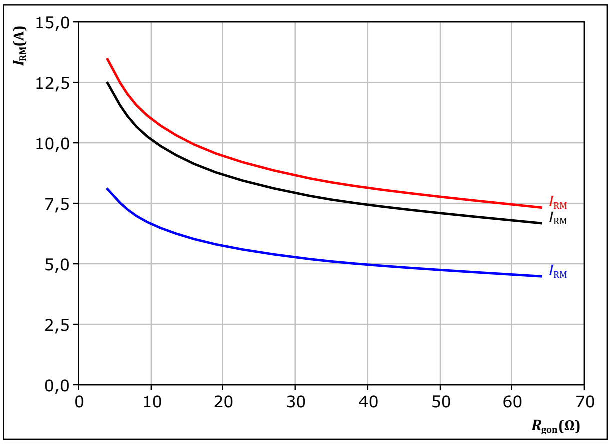
With an inductive load at

V_{CE} =	350	V		T_j:	25 °C
V_{GE} =	±15	V			125 °C
R_{gon} =	16	Ω			150 °C

figure 35. **FWD**

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

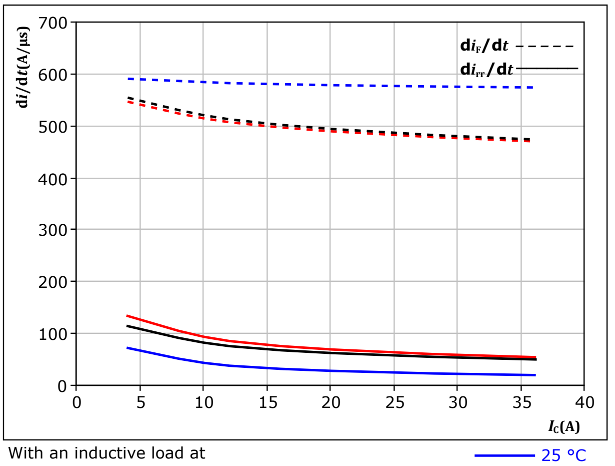
V_{CE} =	350	V		T_j:	25 °C
V_{GE} =	±15	V			125 °C
I_C =	20	A			150 °C

Inverter Switching Characteristics

figure 36. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



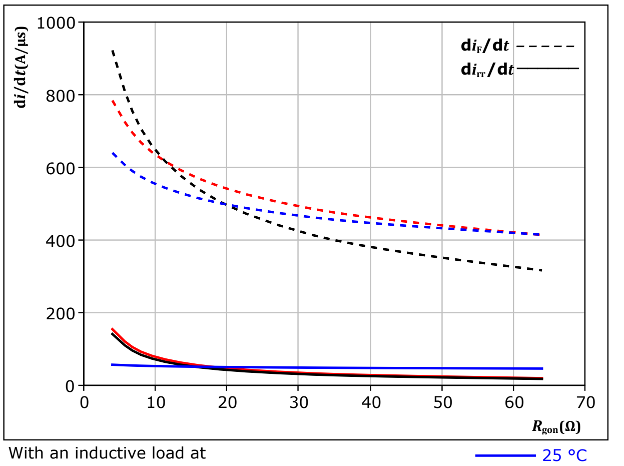
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 37. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

di_F/dt, di_{rr}/dt = f(R_{gon})



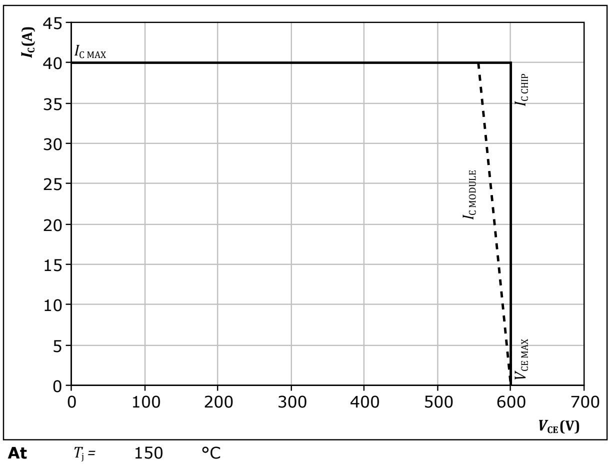
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	20	A		150 °C

figure 38. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



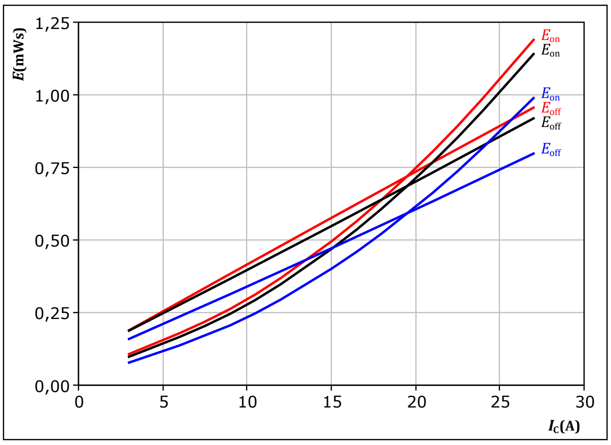
At	T_j =	150	°C
	R_{gon} =	16	Ω
	R_{goff} =	16	Ω

Brake Switching Characteristics

figure 39. IGBT

Typical switching energy losses as a function of collector current

E = f(I_C)



With an inductive load at

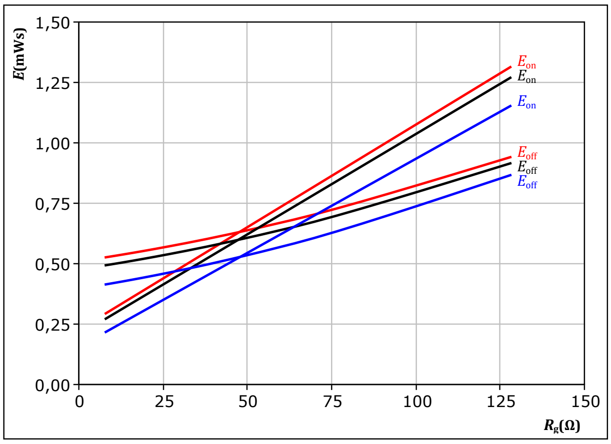
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	32	Ω
R_{goff} =	32	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 40. IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor

E = f(R_g)



With an inductive load at

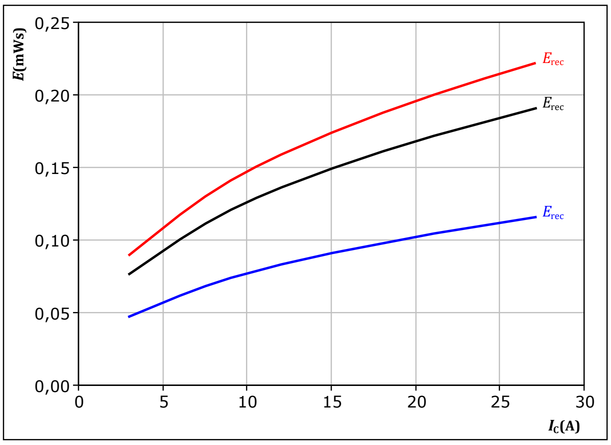
V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	15	A

T_j: 25 °C, 125 °C, 150 °C

figure 41. FWD

Typical reverse recovered energy loss as a function of collector current

E_{rec} = f(I_C)



With an inductive load at

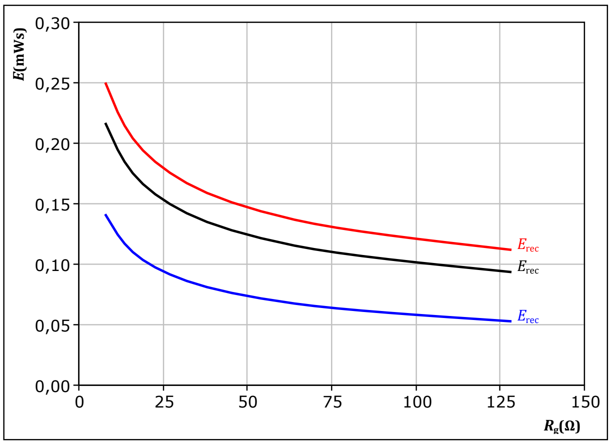
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	32	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 42. FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

E_{rec} = f(R_g)



With an inductive load at

V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	15	A

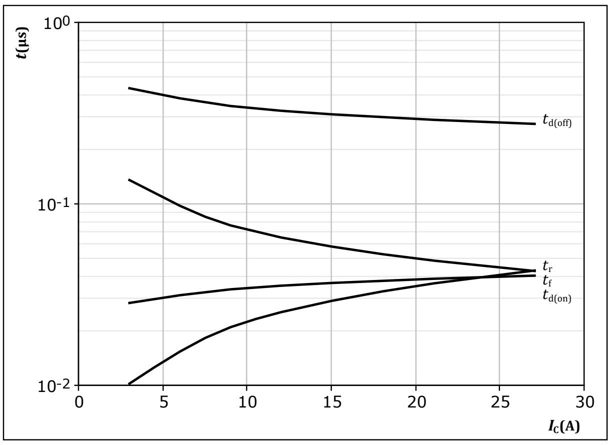
T_j: 25 °C, 125 °C, 150 °C

Brake Switching Characteristics

figure 43. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



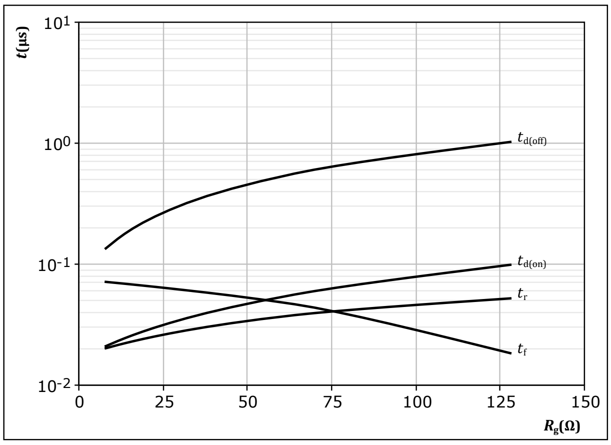
With an inductive load at

T_j =	150	°C
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	32	Ω
R_{goff} =	32	Ω

figure 44. **IGBT**

Typical switching times as a function of IGBT turn on gate resistor

t = f(R_g)



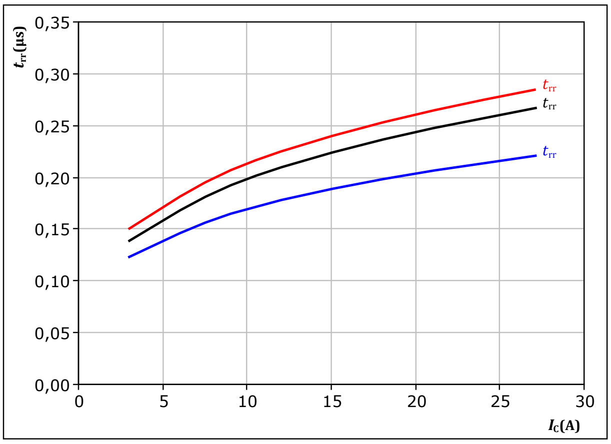
With an inductive load at

T_j =	150	°C
V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	15	A

figure 45. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

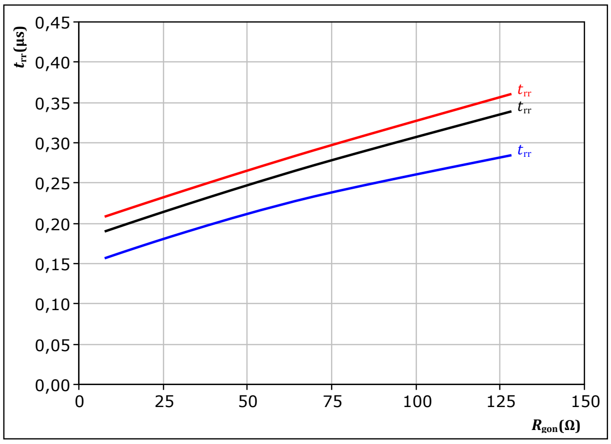
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	32	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 46. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



With an inductive load at

V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	15	A

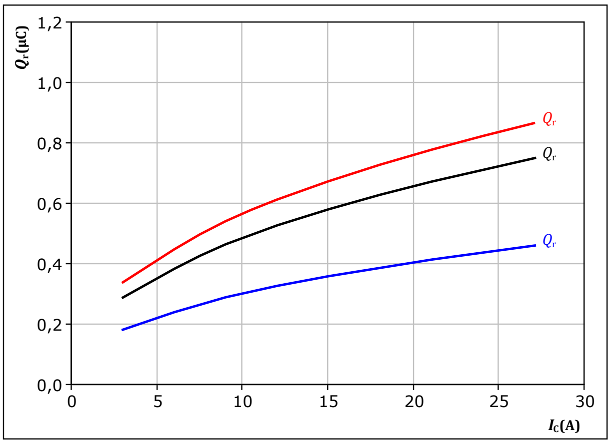
T_j: 25 °C, 125 °C, 150 °C

Brake Switching Characteristics

figure 47. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



With an inductive load at

V_{CE} = 400 V

V_{GE} = 0/15 V

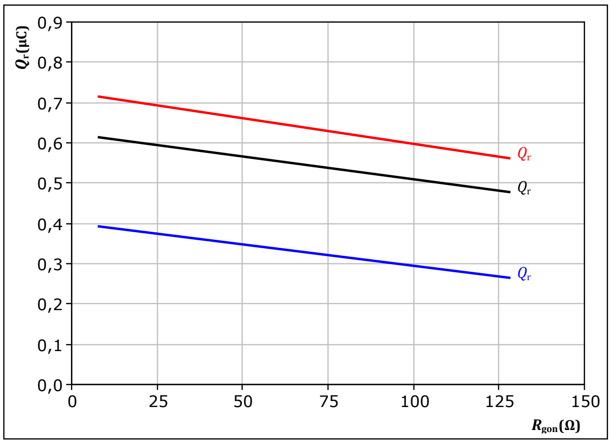
R_{gon} = 32 Ω

T_j: 25 °C, 125 °C, 150 °C

figure 48. **FWD**

Typical recovered charge as a function of IGBT turn on gate resistor

Q_r = f(R_{gon})



With an inductive load at

V_{CE} = 400 V

V_{GE} = 0/15 V

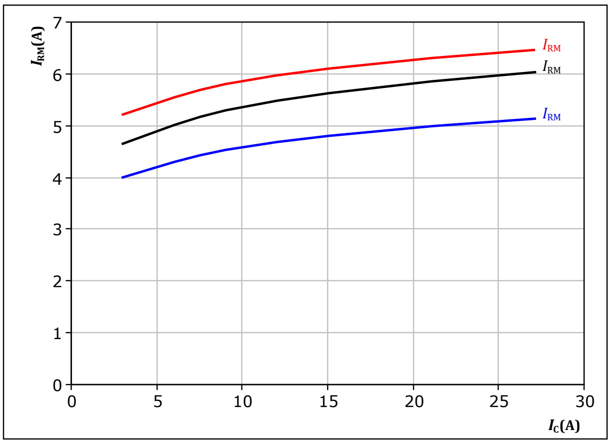
I_C = 15 A

T_j: 25 °C, 125 °C, 150 °C

figure 49. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



With an inductive load at

V_{CE} = 400 V

V_{GE} = 0/15 V

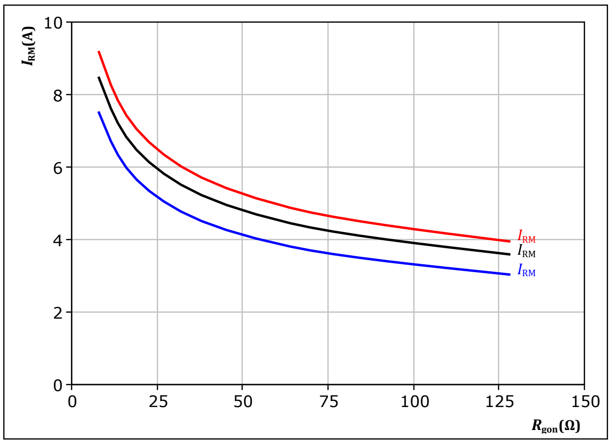
R_{gon} = 32 Ω

T_j: 25 °C, 125 °C, 150 °C

figure 50. **FWD**

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

V_{CE} = 400 V

V_{GE} = 0/15 V

I_C = 15 A

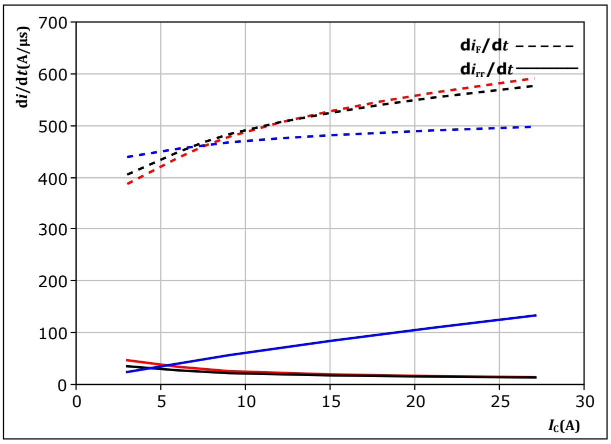
T_j: 25 °C, 125 °C, 150 °C

Brake Switching Characteristics

figure 51. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

$di_F/dt,\ di_{rr}/dt = f(I_C)$



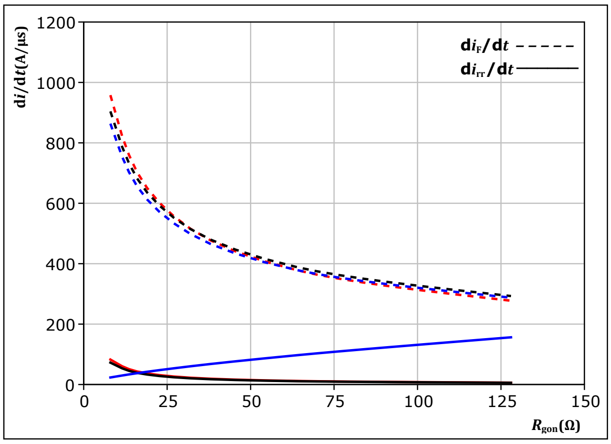
With an inductive load at

V_{CE} =	400	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
R_{gon} =	32	Ω		150 °C

figure 52. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

$di_F/dt,\ di_{rr}/dt = f(R_{gon})$



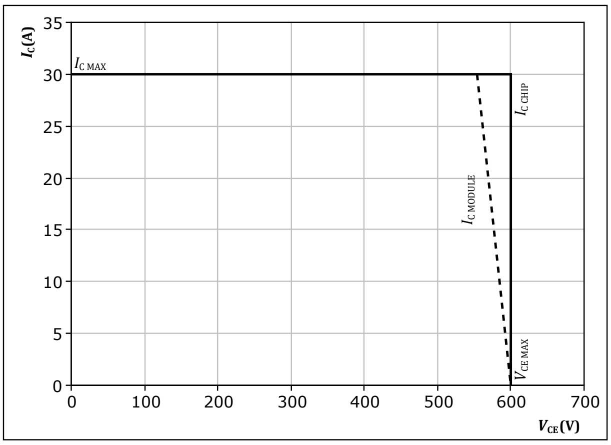
With an inductive load at

V_{CE} =	400	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
I_C =	15	A		150 °C

figure 53. **IGBT**

Reverse bias safe operating area

$I_C = f(V_{CE})$



At	T_j =	150	°C
	R_{gon} =	32	Ω
	R_{goff} =	32	Ω

Switching Definitions

figure 54. **IGBT**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{tEoff} = integrating time for E_{off})

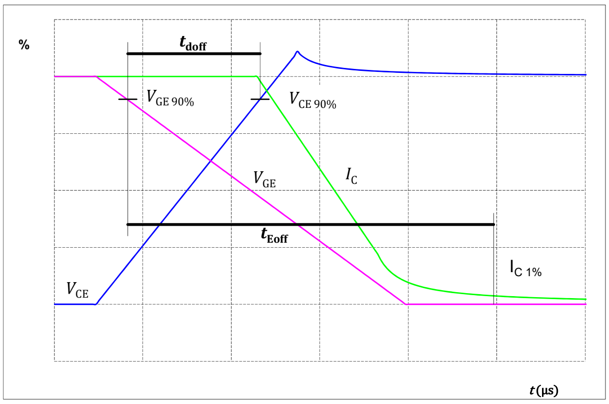


figure 55. **IGBT**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} = integrating time for E_{on})

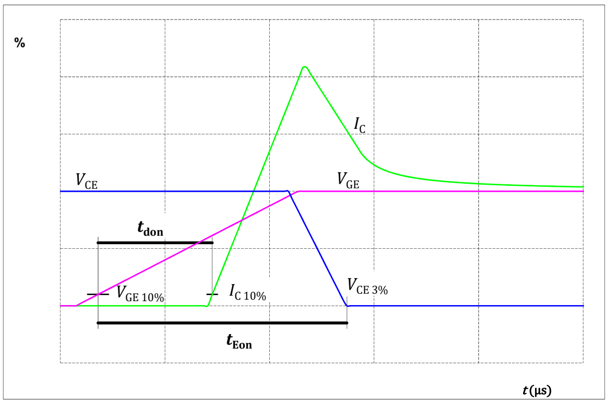


figure 56. **IGBT**

Turn-off Switching Waveforms & definition of t_f

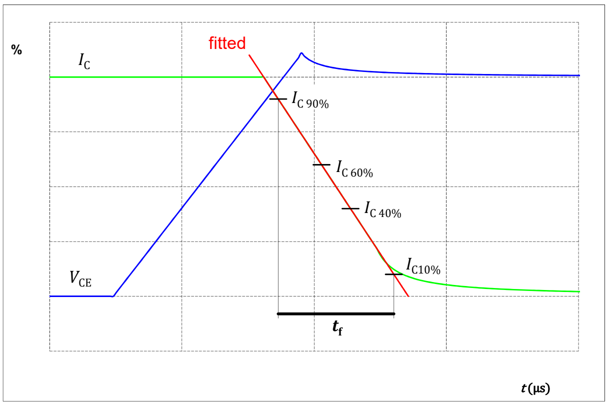
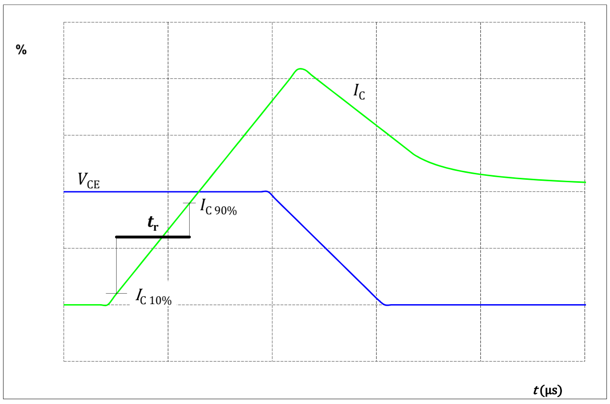


figure 57. **IGBT**

Turn-on Switching Waveforms & definition of t_r



Switching Definitions

figure 58. **FWD**

Turn-off Switching Waveforms & definition of t_{rr}

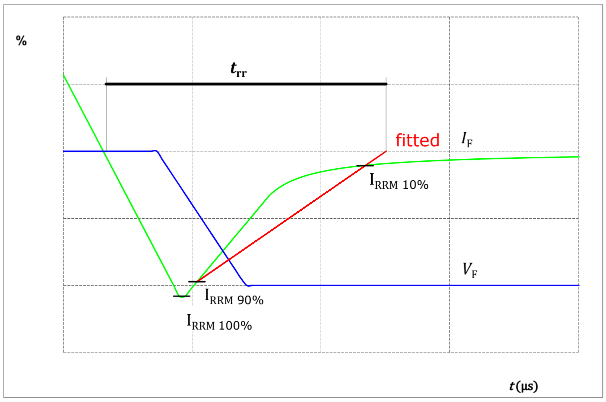
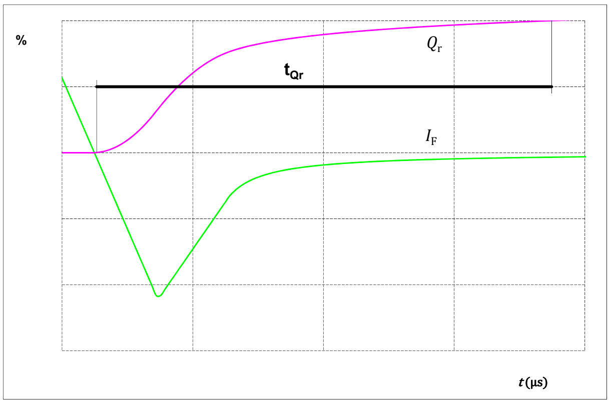


figure 59. **FWD**

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



Vincotech

Ordering Code

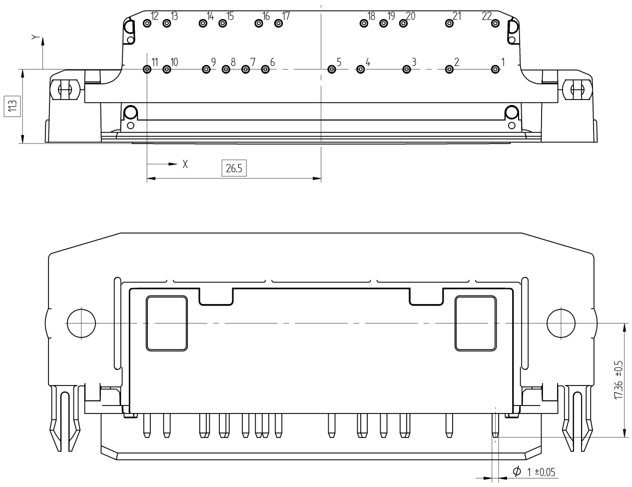
Version	Ordering Code
Without thermal paste	V23990-P634-A-PM
With thermal paste (5,2 W/mK, PTM6000HV)	V23990-P634-A-/7/-PM

Marking

	**Text**	**VIN**	**Date code**	**Type&Ver**	**UL**	**Lot**	**Serial**
		VIN	WWYY	TTTTTTTVV	UL	LLLLL	SSSS
	Datamatrix	**Type&Ver**	**Lot number**	**Serial**	**Date code**		
		TTTTTTTVV	LLLLL	SSSS	WWYY		

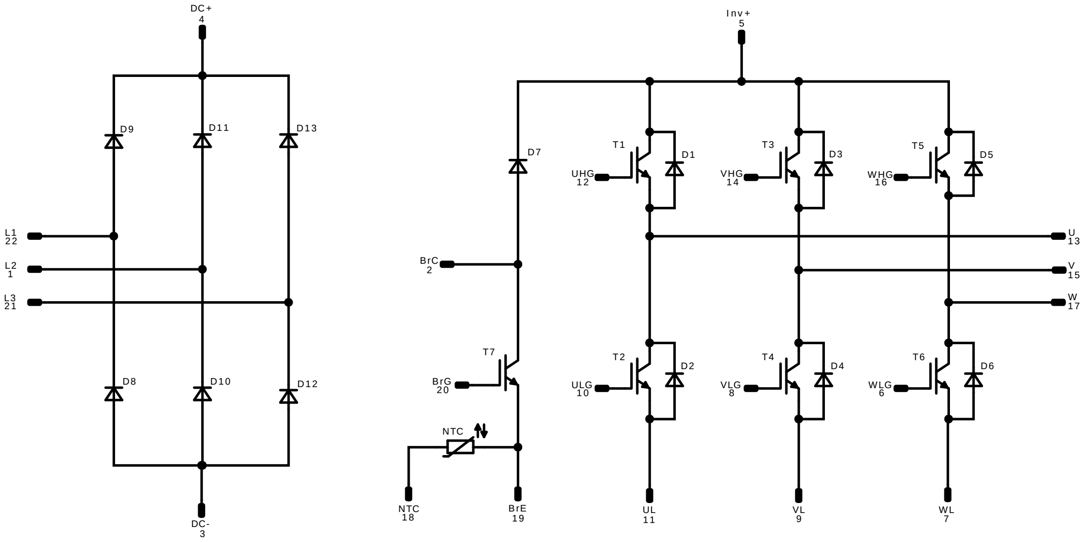
Outline

Pin table [mm]			
Pin	X	Y	Function
1	53	0	L2
2	46	0	BrC
3	39,5	0	DC-
4	32,5	0	DC+
5	28,1	0	Inv+
6	18	0	WLG
7	15	0	WL
8	12	0	VLG
9	9	0	VL
10	3	0	ULG
11	0	0	UL
12	0	7	UHG
13	3	7	U
14	8,5	7	VHG
15	11,5	7	V
16	17	7	WHG
17	20	7	W
18	33	7	NTC
19	36	7	BrE
20	39	7	BrG
21	46	7	L3
22	53	7	L1



Tolerance of pinpositions: ±0,5mm at the end of pins
Dimension of coordinate axis is only offset without tolerance

Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T2, T1, T4, T3, T6, T5	IGBT	600 V	20 A	Inverter Switch	
D2, D1, D4, D3, D6, D5	FWD	600 V	20 A	Inverter Diode	
T7	IGBT	600 V	15 A	Brake Switch	
D7	FWD	600 V	10 A	Brake Diode	
D8, D9, D10, D11, D12, D13	Rectifier	1600 V	18 A	Rectifier Diode	
Rt	Thermistor			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 80	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow*90 1 packages see vincotech.com website.

Package data
Package data for *flow*90 1 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}$=175°C and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
V23990-P634-A-PM-D6-14	16 Apr. 2025	New datasheet format, no change in module	