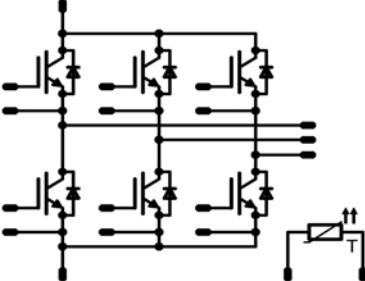


MiniSKiiP®2 PACK		1200V/70A
Features	MiniSKiiP® 2 housing	
<ul style="list-style-type: none"> Solderless interconnection Trench Fieldstop IGBT⁴ technology 		
Target Applications	Schematic	
<ul style="list-style-type: none"> Servo Drives Industrial Motor Drives UPS 		
Types		
<ul style="list-style-type: none"> V23990-K230-F40-PM 		

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
T1,T2,T3,T4,T5,T6				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	71	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$	210	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{op\max}$	140	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	173	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

D1,D2,D3,D4,D5,D6

Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
DC forward current	I_F	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	60	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_{j\max}$	121	A
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	126	W
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{op}		-40...+($T_{j\max} - 25$)	°C

Insulation Properties

Insulation voltage	V_{is}	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12.7	mm
Clearance				min 12.7	mm

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	

T1,T2,T3,T4,T5,T6

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0024	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		70	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,36	1,95 2,31	2,2	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0,12	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			240	nA
Integrated Gate resistor	R_{gint}									Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=8\Omega$ $R_{gon}=8\Omega$	± 15	600	70	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		98 98		ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		21 27		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		217 285		
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		87 126		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		3,74 6,39		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		4,09 6,63		
Input capacitance	C_{ies}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		3900		pF
Output capacitance	C_{oss}							310		
Reverse transfer capacitance	C_{rss}							230		
Gate charge	Q_{Gate}		± 15			$T_j=25^\circ\text{C}$		540		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=1\text{W/mK}$						0,55		K/W

D1,D2,D3,D4,D5,D6

Diode forward voltage	V_F				70	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,3	2,28 2,18	2,6	V
Peak reverse recovery current	I_{RRM}	$R_{gon}=8\Omega$	± 15	600	70	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		66,9 85,4		A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		129 312		ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		4,46 11,6		μC
Peak rate of fall of recovery current	$di(\text{rec})\text{max}/dt$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		3099 606		$\text{A}/\mu\text{s}$
Reverse recovered energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1,59 4,43		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=1\text{W/mK}$						0,75		K/W

Thermistor

Rated resistance	R					$T=25^\circ\text{C}$		1000		Ω
Deviation of R100	$\Delta R/R$	$R_{100}=1670\ \Omega$				$T=100^\circ\text{C}$	-3		3	%
R100	P					$T=100^\circ\text{C}$		1670,313		Ω
A-value	B(25/50)	Tol. %				$T=25^\circ\text{C}$		7,635*10-3		$1/\text{K}$
B-value	B(25/100)	Tol. %				$T=25^\circ\text{C}$		1,731*10-5		$1/\text{K}^2$
Vincotech NTC Reference									E	

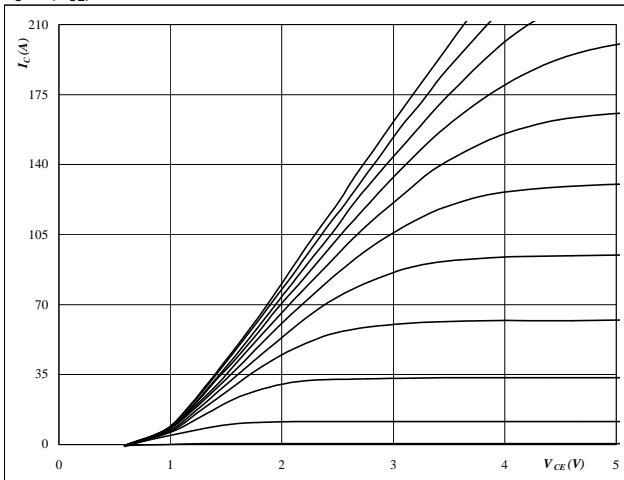
T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 1

T1,T2,T3,T4,T5,T6 IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$


At

$$t_p = 350 \mu\text{s}$$

$$T_j = 25^\circ\text{C}$$

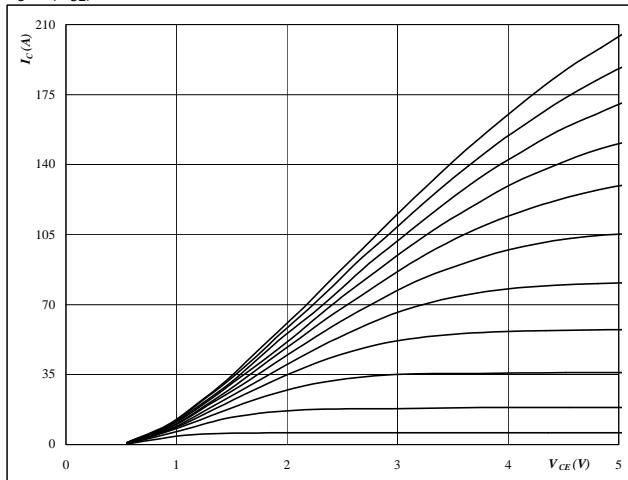
V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2

T1,T2,T3,T4,T5,T6 IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$


At

$$t_p = 350 \mu\text{s}$$

$$T_j = 150^\circ\text{C}$$

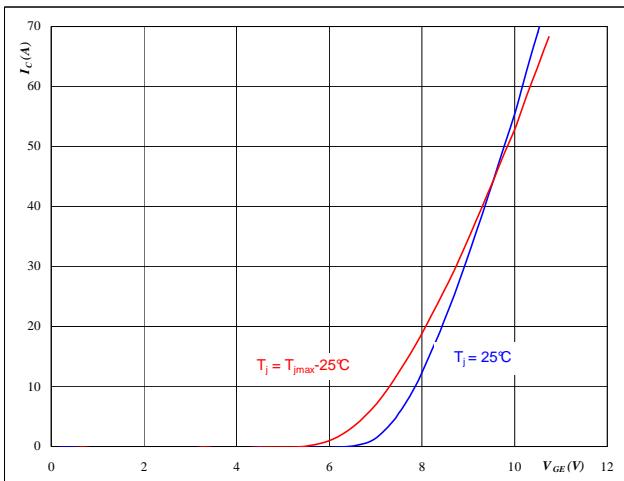
V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3

T1,T2,T3,T4,T5,T6 IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$


At

$$T_j = 25/150^\circ\text{C}$$

$$t_p = 350 \mu\text{s}$$

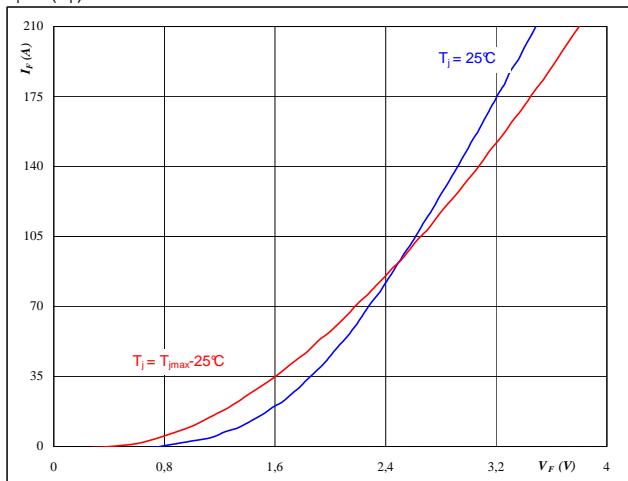
$$V_{CE} = 10 \text{ V}$$

Figure 4

D1,D2,D3,D4,D5,D6 FWD

Typical diode forward current as a function of forward voltage

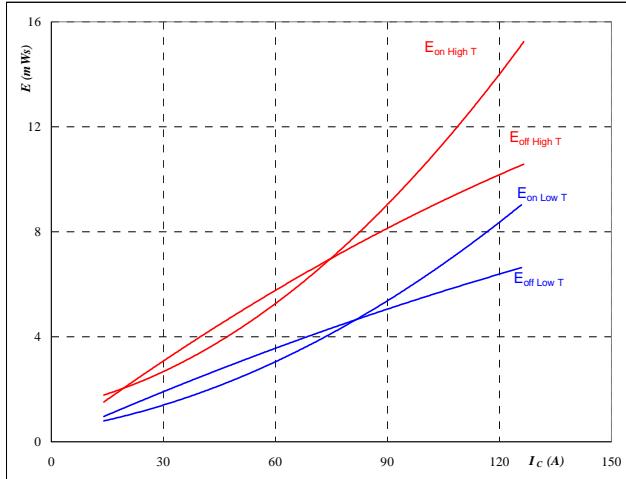
$$I_F = f(V_F)$$


At

$$t_p = 350 \mu\text{s}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 5
T1,T2,T3,T4,T5,T6 IGBT
**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$

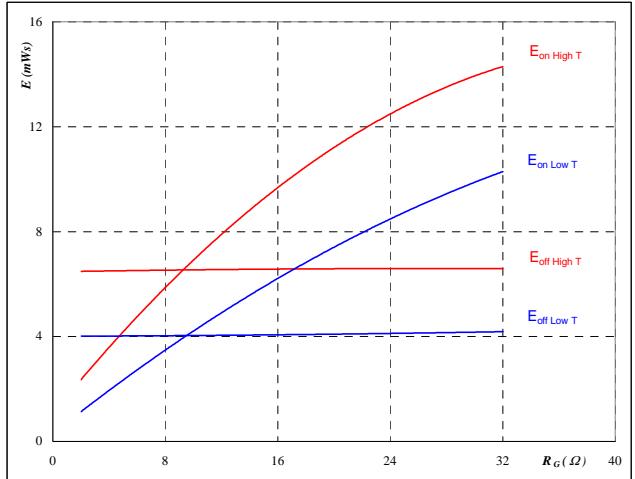


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 8 \quad \Omega \\ R_{goff} &= 8 \quad \Omega \end{aligned}$$

Figure 6
T1,T2,T3,T4,T5,T6 IGBT
**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$

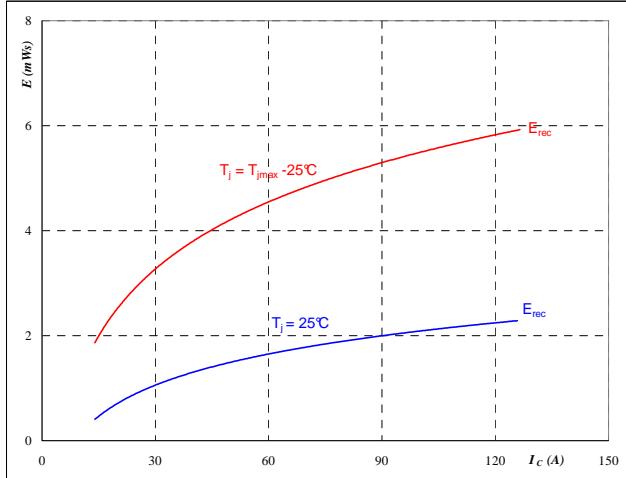


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 70 \quad A \end{aligned}$$

Figure 7
D1,D2,D3,D4,D5,D6 FWD
**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$

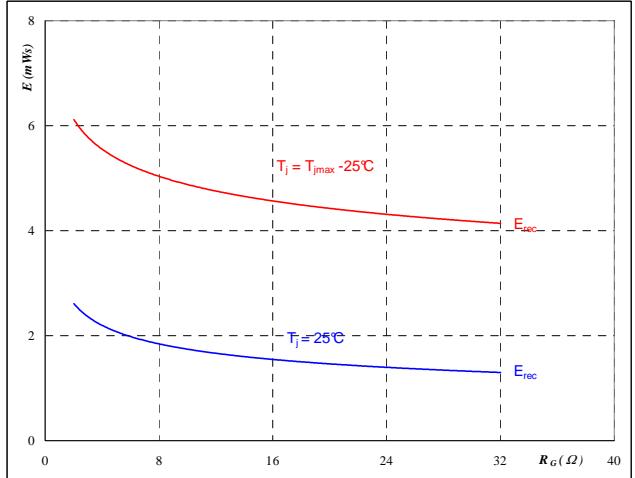


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

Figure 8
D1,D2,D3,D4,D5,D6 FWD
**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



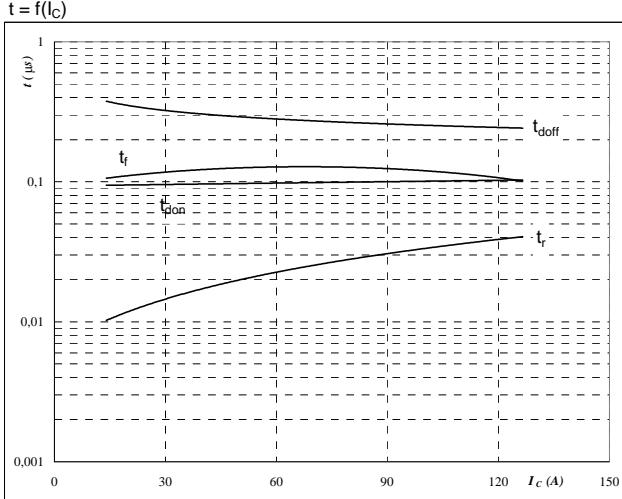
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 70 \quad A \end{aligned}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 9

Typical switching times as a function of collector current
 $t = f(I_C)$

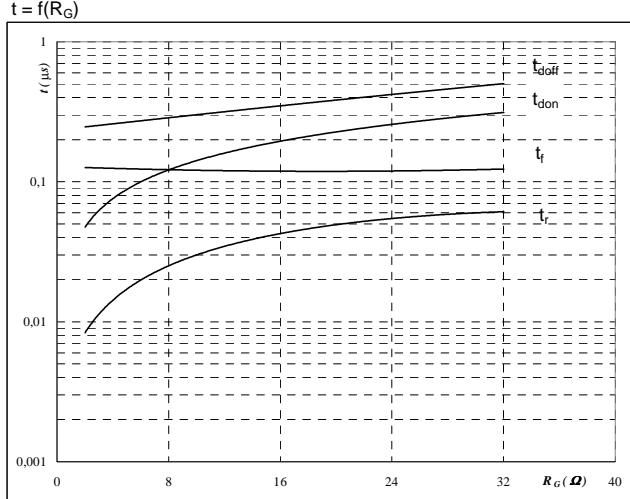


With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \Omega$
 $R_{goff} = 8 \Omega$

T1,T2,T3,T4,T5,T6 IGBT
Figure 10

Typical switching times as a function of gate resistor
 $t = f(R_G)$

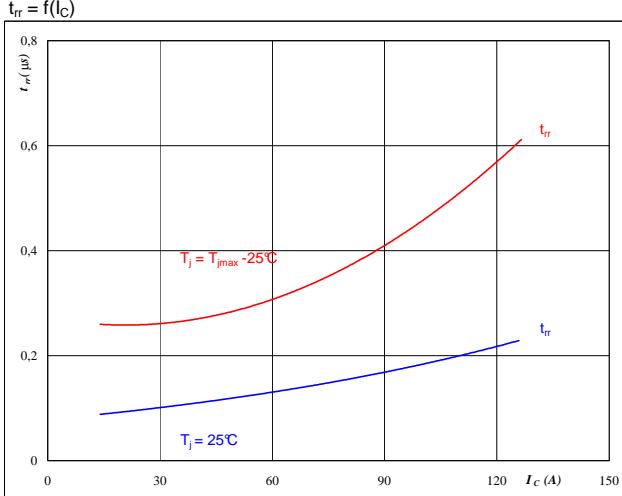


With an inductive load at

$T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 70 \text{ A}$

Figure 11
D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$

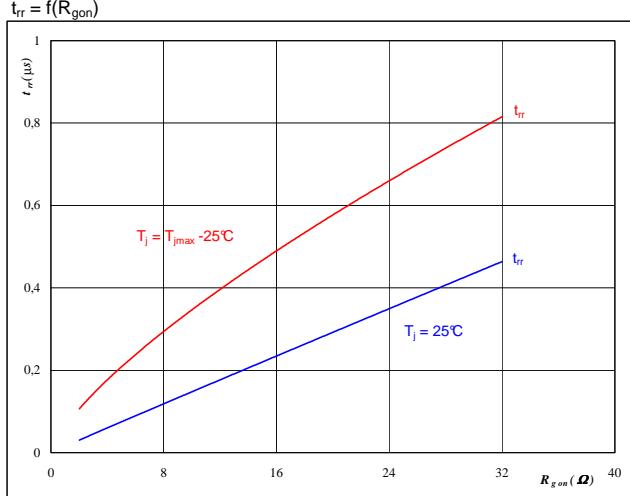


At

$T_j = 25/150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \Omega$

Figure 12
D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



At

$T_j = 25/150 \text{ }^\circ\text{C}$
 $V_R = 600 \text{ V}$
 $I_F = 70 \text{ A}$
 $V_{GE} = \pm 15 \text{ V}$

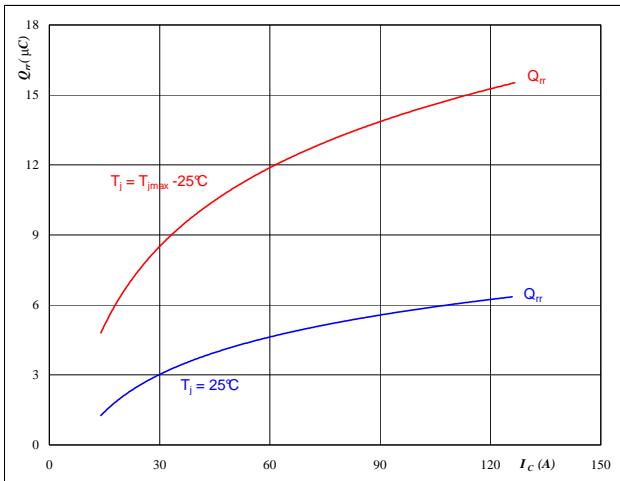
T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 13

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


At

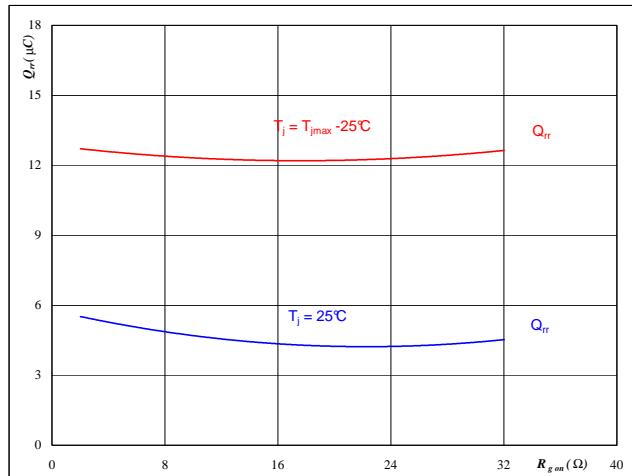
$$\begin{aligned} T_j &= 25/150 \quad {}^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

Figure 14

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


At

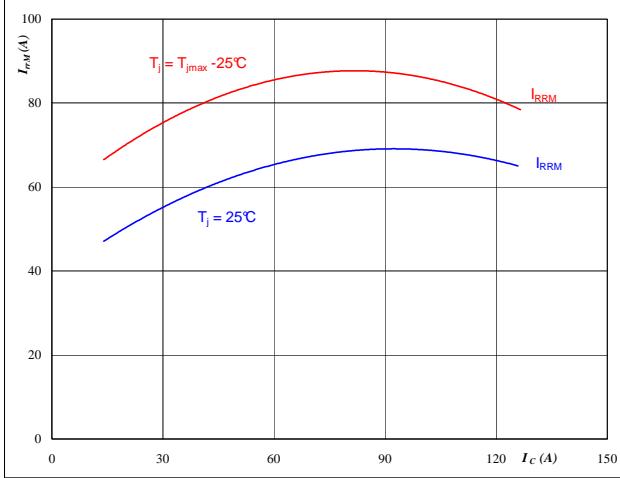
$$\begin{aligned} T_j &= 25/150 \quad {}^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 70 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

Figure 15

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


At

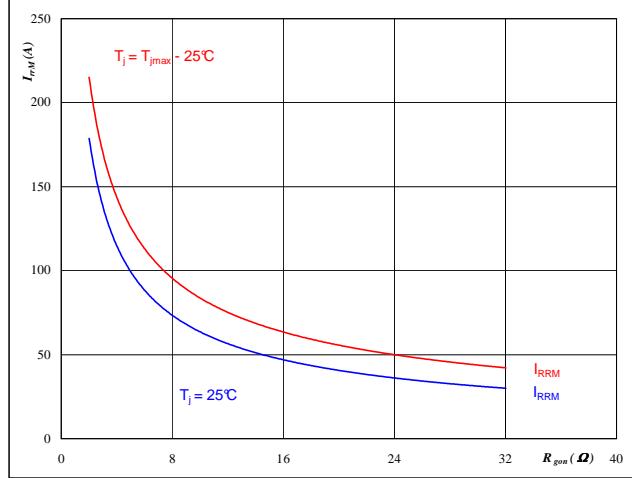
$$\begin{aligned} T_j &= 25/150 \quad {}^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 8 \quad \Omega \end{aligned}$$

Figure 16

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$

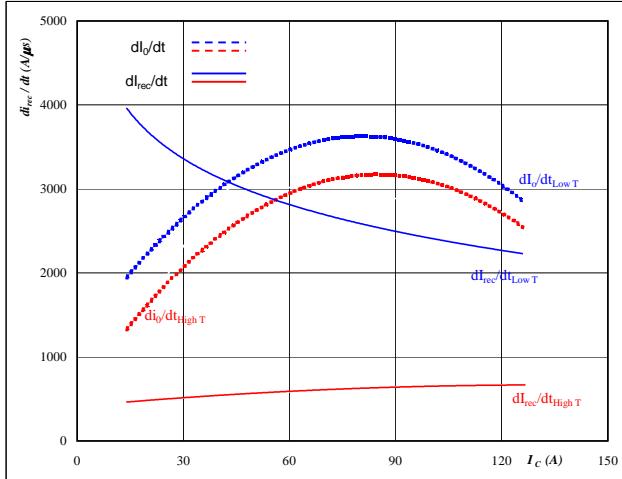

At

$$\begin{aligned} T_j &= 25/150 \quad {}^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 70 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 17

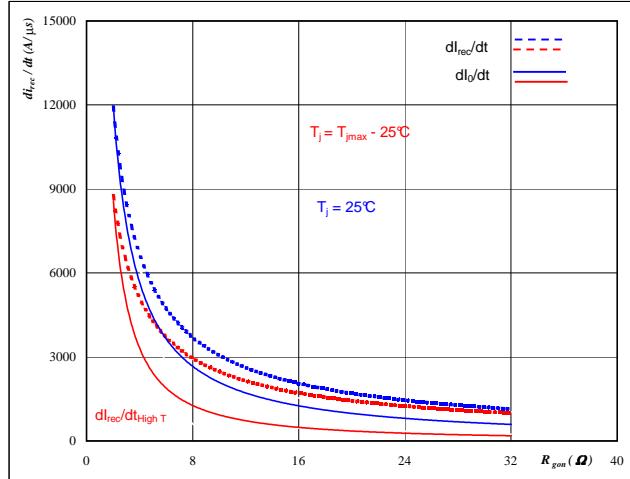
**Typical rate of fall of forward
and reverse recovery current as a
function of collector current**
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


At

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_{CE} = 600 \quad \text{V}$
 $V_{GE} = \pm 15 \quad \text{V}$
 $R_{gon} = 8 \quad \Omega$

D1,D2,D3,D4,D5,D6 FWD
Figure 18

**Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor**
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

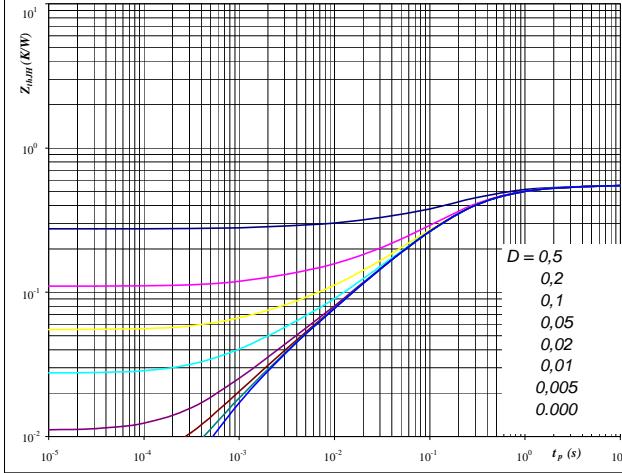

At

$T_j = 25/150 \quad ^\circ\text{C}$
 $V_R = 600 \quad \text{V}$
 $I_F = 70 \quad \text{A}$
 $V_{GE} = \pm 15 \quad \text{V}$

Figure 19
T1,T2,T3,T4,T5,T6 IGBT

**IGBT transient thermal impedance
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


At

$D = t_p / T$
 $R_{thJH} = 0,55 \quad \text{K/W}$

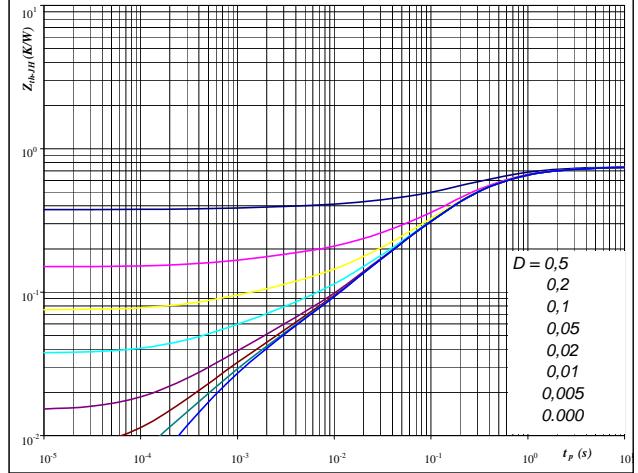
IGBT thermal model values

R (C/W)	Tau (s)
0,07	2,1E+00
0,24	3,0E-01
0,17	8,1E-02
0,05	1,1E-02
0,02	1,3E-03

Figure 20
D1,D2,D3,D4,D5,D6 FWD

**FWD transient thermal impedance
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


At

$D = t_p / T$
 $R_{thJH} = 0,75 \quad \text{K/W}$

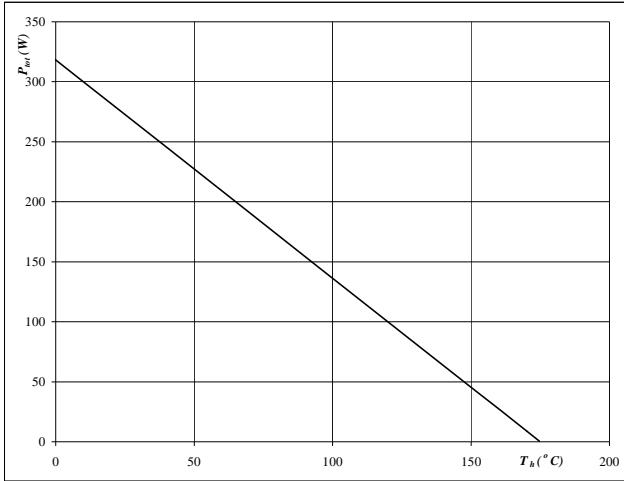
FWD thermal model values

R (C/W)	Tau (s)
0,05	5,7E+00
0,16	8,7E-01
0,37	2,2E-01
0,10	5,5E-02
0,05	1,1E-02
0,02	1,3E-03
0,01	2,9E-04

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

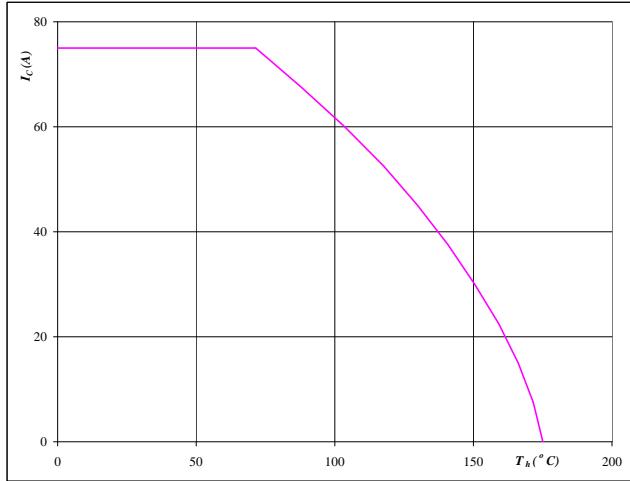

At

$$T_j = 175 \quad ^{\circ}\text{C}$$

T1,T2,T3,T4,T5,T6 IGBT
Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

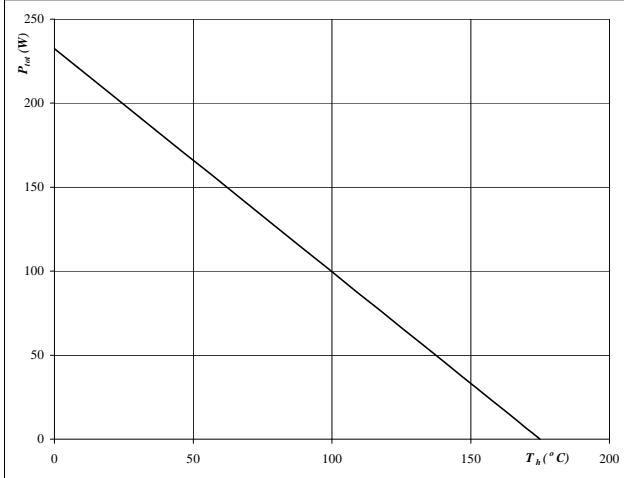
$$T_j = 175 \quad ^{\circ}\text{C}$$

$$V_{\text{GE}} = 15 \quad \text{V}$$

Figure 23
D1,D2,D3,D4,D5,D6 FWD

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

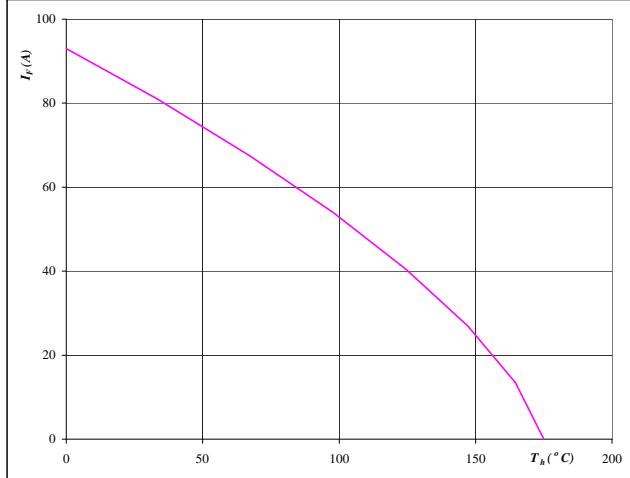

At

$$T_j = 175 \quad ^{\circ}\text{C}$$

Figure 24
D1,D2,D3,D4,D5,D6 FWD

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$

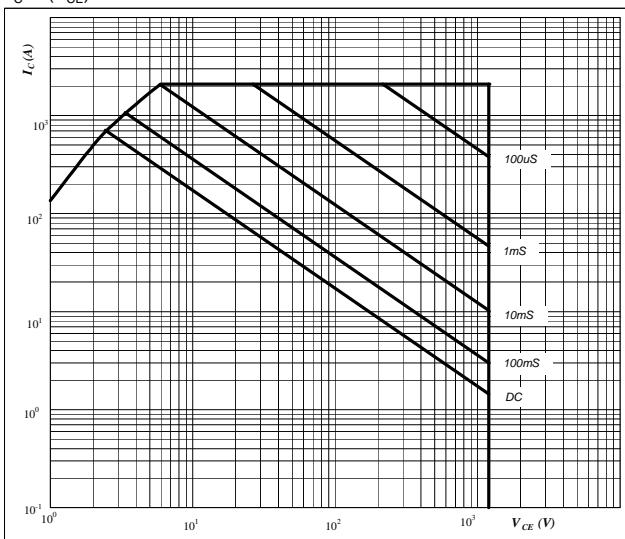

At

$$T_j = 175 \quad ^{\circ}\text{C}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 25
T1,T2,T3,T4,T5,T6 IGBT
**Safe operating area as a function
of collector-emitter voltage**

$$I_C = f(V_{CE})$$


At

D = single pulse

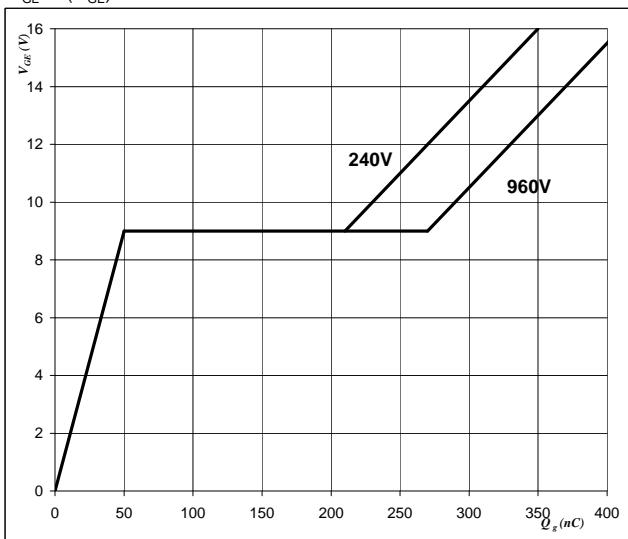
T_h = 80 °C

V_{GE} = ±15 V

T_j = T_{jmax} °C

Figure 26
T1,T2,T3,T4,T5,T6 IGBT
Gate voltage vs Gate charge

$$V_{GE} = f(Q_{GE})$$

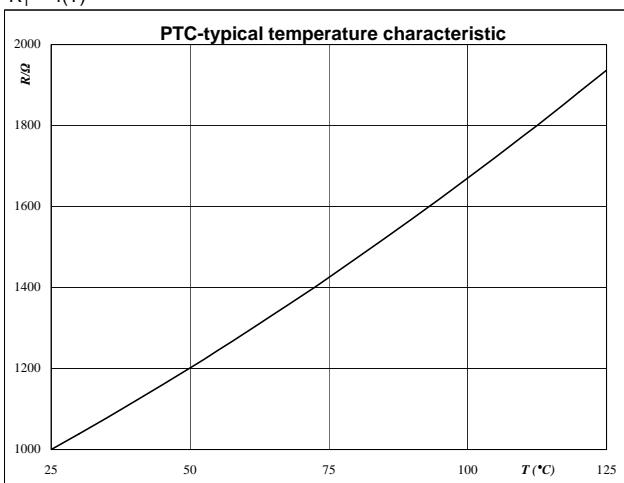

At

I_C = 70 A

Thermistor

Figure 1
Thermistor
Typical PTC characteristic
as a function of temperature

$$R_T = f(T)$$



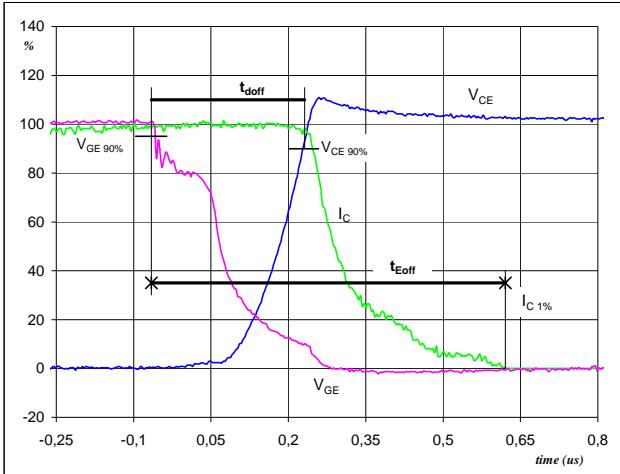
Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	8 Ω
R_{goff}	=	8 Ω

Figure 1
Output inverter IGBT

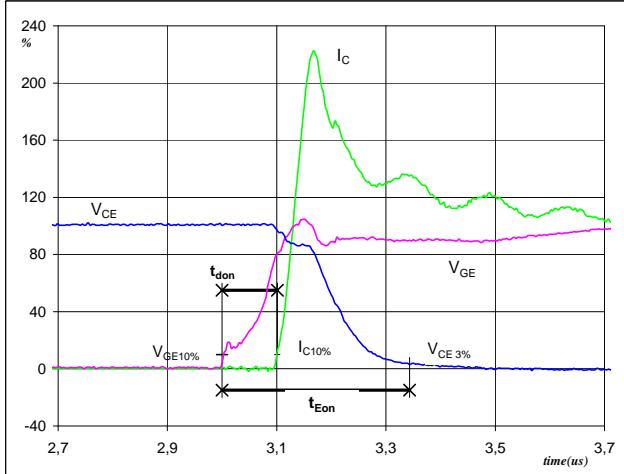
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$



$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 70$ A
 $t_{doff} = 0,29$ μs
 $t_{Eoff} = 0,69$ μs

Figure 2
Output inverter IGBT

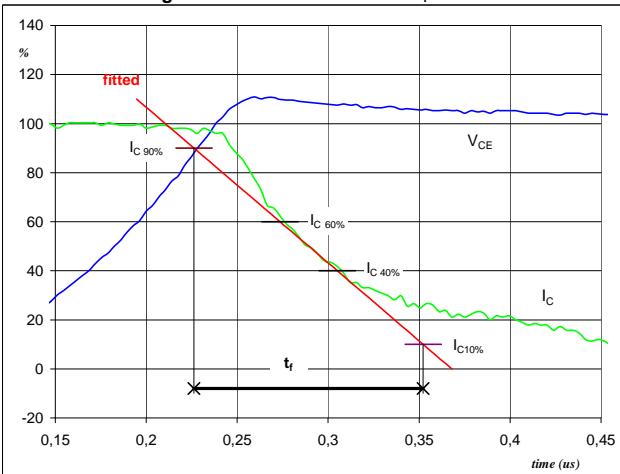
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$



$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 70$ A
 $t_{don} = 0,10$ μs
 $t_{Eon} = 0,34$ μs

Figure 3
Output inverter IGBT

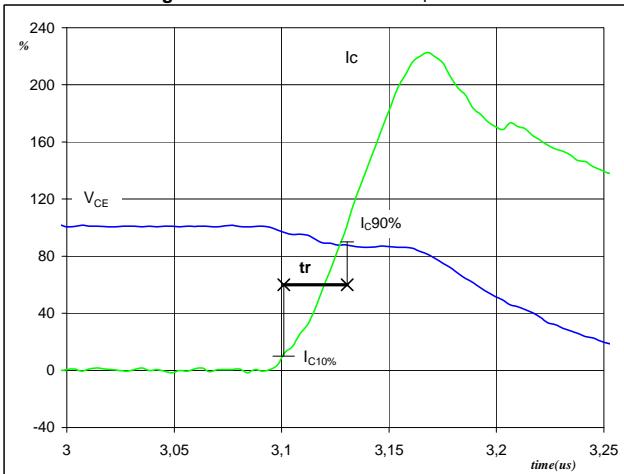
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) = 600$ V
 $I_C(100\%) = 70$ A
 $t_f = 0,13$ μs

Figure 4
Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

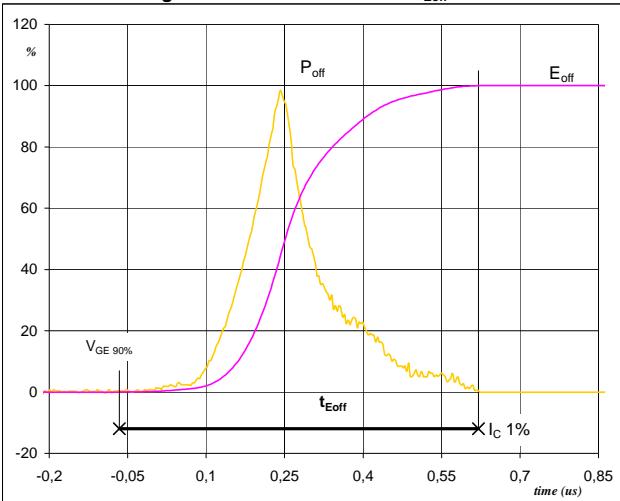


$V_C(100\%) = 600$ V
 $I_C(100\%) = 70$ A
 $t_r = 0,03$ μs

Switching Definitions Output Inverter

Figure 5

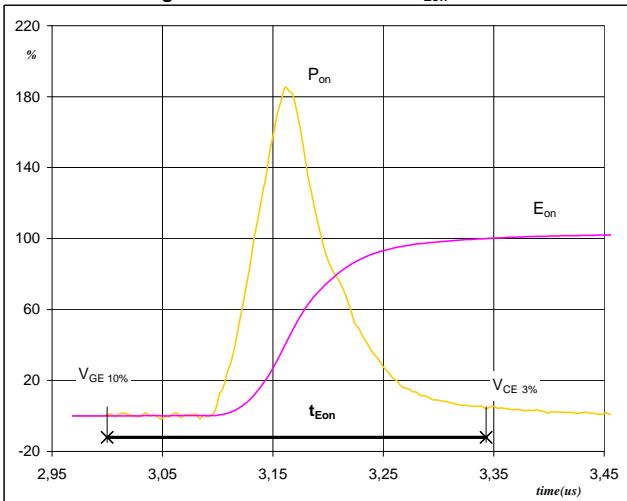
Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


$P_{off} (100\%) = 42,05 \text{ kW}$
 $E_{off} (100\%) = 6,63 \text{ mJ}$
 $t_{Eoff} = 0,69 \mu\text{s}$

Figure 6

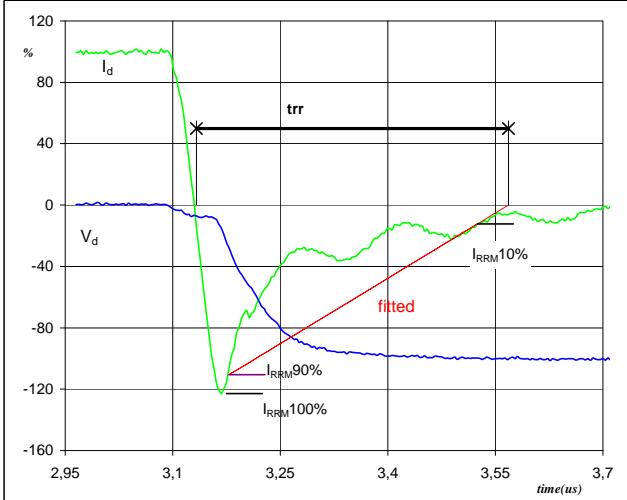
Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{Eon}


$P_{on} (100\%) = 42,05 \text{ kW}$
 $E_{on} (100\%) = 6,39 \text{ mJ}$
 $t_{Eon} = 0,34 \mu\text{s}$

Figure 7

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{trr}


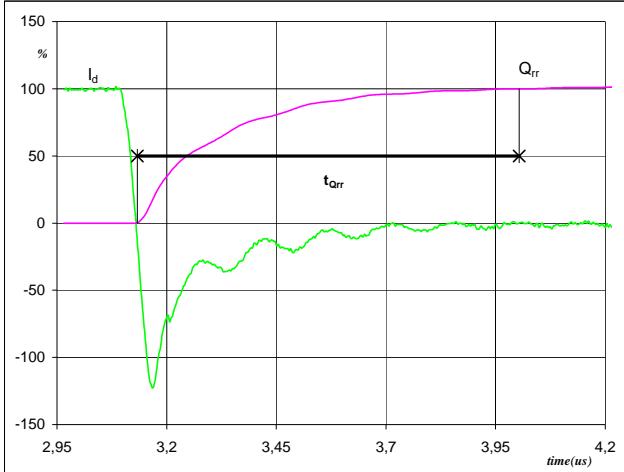
$V_d (100\%) = 600 \text{ V}$
 $I_d (100\%) = 70 \text{ A}$
 $I_{RRM} (100\%) = -85 \text{ A}$
 $t_{trr} = 0,31 \mu\text{s}$

Switching Definitions Output Inverter

Figure 8

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

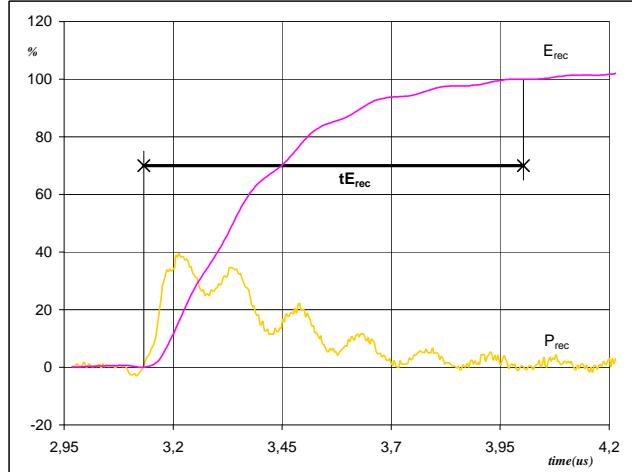


$I_d(100\%) = 70 \text{ A}$
 $Q_{rr}(100\%) = 11,55 \mu\text{C}$
 $t_{Qrr} = 0,87 \mu\text{s}$

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$

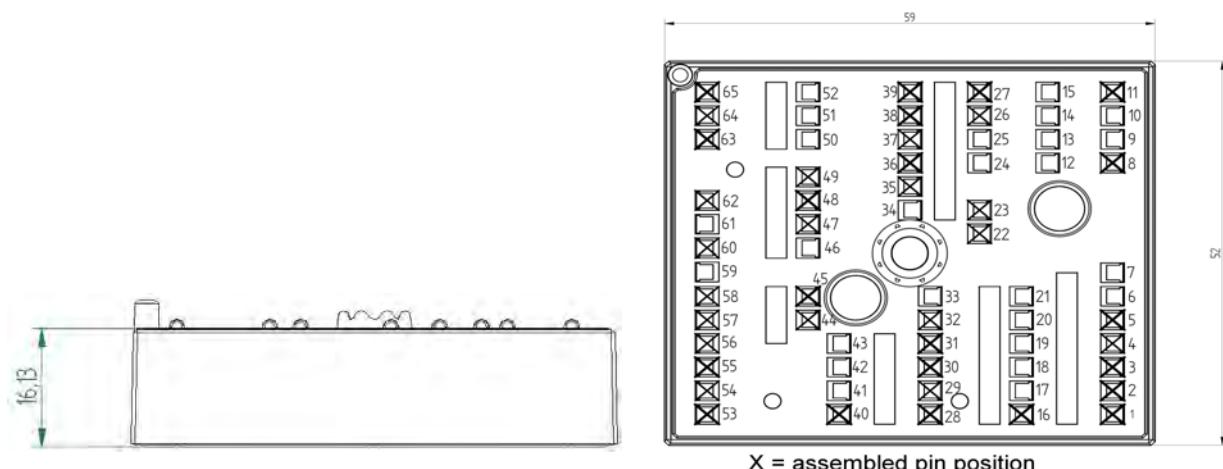


$E_{rec}(100\%) = 4,43 \text{ mJ}$
 $P_{rec}(100\%) = 42,05 \text{ kW}$
 $t_{Erec} = 0,87 \mu\text{s}$

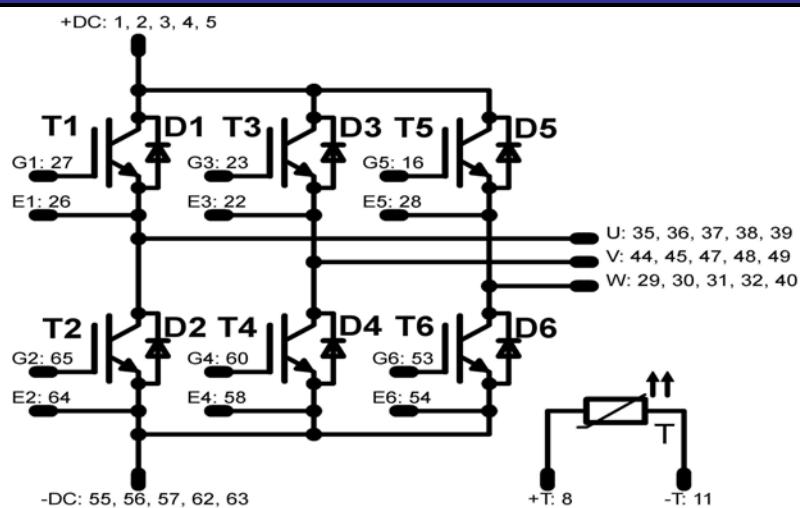
Ordering Code and Marking - Outline - Pinout

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K22-T-PM)	V23990-K230-F40/-0A/-PM	K230F40	K230F40/-0A/
with std lid (black V23990-K22-T-PM) and P12	V23990-K230-F40/-1A/-PM	K230F40	K230F40/-1A/
with thin lid (white V23990-K23-T-PM)	V23990-K230-F40/-0B/-PM	K230F40	K230F40/-0B/
with thin lid (white V23990-K23-T-PM) and P12	V23990-K230-F40/-1B/-PM	K230F40	K230F40/-1B/

Outline



Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.