



# Vincotech

MiniSKiiP PIM 0		600 V / 10 A
<b>Topology features</b>		<b>MiniSKiiP® 0 16 mm housing</b>
• Open Emitter configuration • Temperature sensor • Converter+Inverter		
<b>Component features</b>		
• Easy paralleling • Low turn-off losses • Low collector-emitter saturation voltage • Positive temperature coefficient • Short tail current		
<b>Housing features</b>		
• Base isolation: Al <sub>2</sub> O <sub>3</sub> • Easy assembly in one mounting step • Flexible PCB design w/o pin holes • Rugged solderless spring contacts		
<b>Extra features</b>		
• Equivalent: SKiiP 02NAC066V3		
<b>Target applications</b>		
• Industrial Drives • Embedded Drives		
<b>Types</b>		
• 80-M006PNB010SA-K615C		<b>Schematic</b> 



80-M006PNB010SA-K615C

datasheet

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## Maximum Ratings

$T_j = 25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
<b>Inverter Switch</b>				
Collector-emitter voltage	$V_{CES}$		600	V
Collector current (DC current)	$I_C$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	17	A
Repetitive peak collector current	$I_{CRM}$	$t_p$ limited by $T_{jmax}$	30	A
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	44	W
Gate-emitter voltage	$V_{GES}$		$\pm 20$	V
Short circuit ratings	$t_{SC}$	$V_{GE} = 15\text{ V}$ , $V_{CC} = 360\text{ V}$ $T_j = 150^\circ\text{C}$	6	$\mu\text{s}$
Maximum junction temperature	$T_{jmax}$		175	$^\circ\text{C}$
<b>Inverter Diode</b>				
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Forward current (DC current)	$I_F$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	23	A
Surge (non-repetitive) forward current	$I_{FSM}$	Single Half Sine Wave, $T_j = 150^\circ\text{C}$	95	A
Surge current capability	$I^t$	$t_p = 10\text{ ms}$	45	$\text{A}^2\text{s}$
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	45	W
Maximum junction temperature	$T_{jmax}$		175	$^\circ\text{C}$
<b>Rectifier Diode</b>				
Peak repetitive reverse voltage	$V_{RRM}$		1600	V
Forward current (DC current)	$I_F$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	32	A
Surge (non-repetitive) forward current	$I_{FSM}$	Single Half Sine Wave, $T_j = 150^\circ\text{C}$	150	A
Surge current capability	$I^t$	$t_p = 10\text{ ms}$	112	$\text{A}^2\text{s}$
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	51	W
Maximum junction temperature	$T_{jmax}$		150	$^\circ\text{C}$



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## Maximum Ratings

$T_j = 25 \text{ }^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
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### Module Properties

#### Thermal Properties

Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{jop}$		-40...+( $T_{jmax} - 25$ )	°C

#### Isolation Properties

Isolation voltage	$V_{isol}$	DC Test Voltage* $t_p = 2 \text{ s}$	5500	V
Isolation voltage	$V_{isol}$	AC Voltage $t_p = 1 \text{ min}$	2500	V
Creepage distance		With std lid For more informations see handling instructions	6,3	mm
Clearance		With std lid For more informations see handling instructions	6,3	mm
Comparative Tracking Index	CTI		$\geq 200$	

\*100 % tested in production



80-M006PNB010SA-K615C

datasheet

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## Characteristic Values

Parameter	Symbol	Conditions						Values			Unit
		$V_{GE}$ [V]	$V_{GS}$ [V]	$V_{CE}$ [V]	$V_{DS}$ [V]	$I_C$ [A]	$I_D$ [A]	$T_j$ [°C]	Min	Typ	Max

### Inverter Switch

#### Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,00015	25	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CEsat}$		15		10	25 150	1,1	1,64 1,89	1,9 <sup>(1)</sup>	V
Collector-emitter cut-off current	$I_{CES}$		0	600		25			0,6	µA
Gate-emitter leakage current	$I_{GES}$		20	0		25			300	nA
Internal gate resistance	$r_g$							None		Ω
Input capacitance	$C_{ies}$	$f = 1 \text{ MHz}$	0	25	25	25	551		pF	
Output capacitance	$C_{oes}$									
Reverse transfer capacitance	$C_{res}$									

#### Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 2,5 \text{ W/mK}$ (HPTP)						2,14		K/W
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#### Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 32 \Omega$ $R_{goft} = 32 \Omega$	$\pm 15$	300	10	25 150		89,8 90,6		ns
Rise time	$t_r$					25 150		22 24,8		ns
Turn-off delay time	$t_{d(off)}$					25 150		133,4 155,6		ns
Fall time	$t_f$					25 150		119,52 144,28		ns
Turn-on energy (per pulse)	$E_{on}$					25 150		0,261 0,382		mWs
Turn-off energy (per pulse)	$E_{off}$					25 150		0,255 0,343		mWs



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## Characteristic Values

Parameter	Symbol	Conditions						Values			Unit
		$V_{GE}$ [V]	$V_{GS}$ [V]	$V_{CE}$ [V]	$V_{DS}$ [V]	$I_C$ [A]	$I_D$ [A]	$T_j$ [°C]	Min	Typ	Max

## Inverter Diode

## Static

Forward voltage	$V_F$				20	25 150		1,78 1,8	2,3 <sup>(1)</sup>	V
Reverse leakage current	$I_R$	$V_i = 600$ V				25			100	µA

## Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 2,5$ W/mK (HPTP)						2,13		K/W
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## Dynamic

Peak recovery current	$I_{RM}$	$di/dt=266$ A/µs $di/dt=305$ A/µs	$\pm 15$	300	10	25 150		6,77 9,87		A
Reverse recovery time	$t_{rr}$					25 150		233,11 351,48		ns
Recovered charge	$Q_r$					25 150		0,655 1,46		µC
Reverse recovered energy	$E_{rec}$					25 150		0,128 0,305		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 150		104,52 109,07		A/µs

## Rectifier Diode

## Static

Forward voltage	$V_F$				8	25 125		1,05 0,976	1,21 <sup>(1)</sup> 1,1 <sup>(1)</sup>	V
Reverse leakage current	$I_R$	$V_i = 1600$ V				25			100	µA

## Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 2,5$ W/mK (HPTP)						1,37		K/W
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## Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		$V_{GE}$ [V]	$V_{GS}$ [V]	$V_{CE}$ [V]	$V_{DS}$ [V]	$I_C$ [A]	$T_j$ [°C]	Min	Typ	Max

## Thermistor

## Static

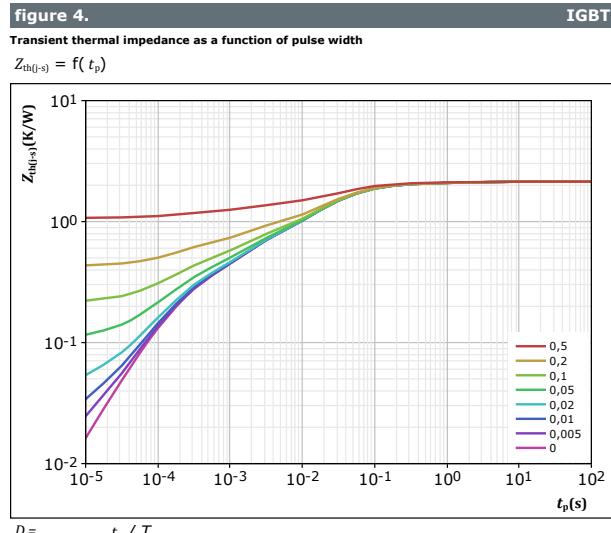
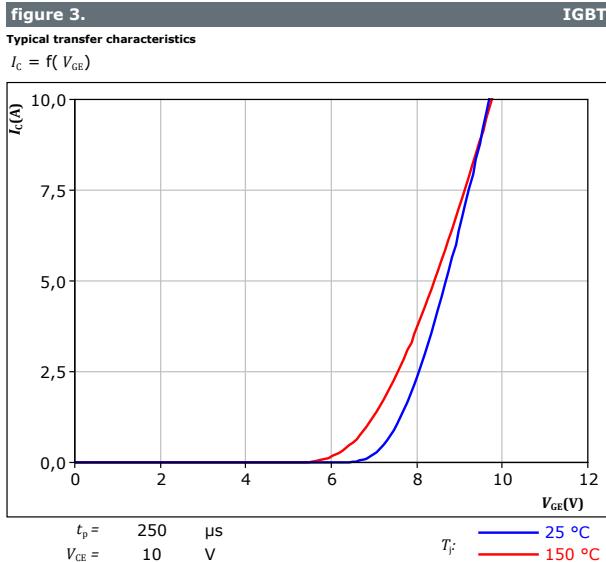
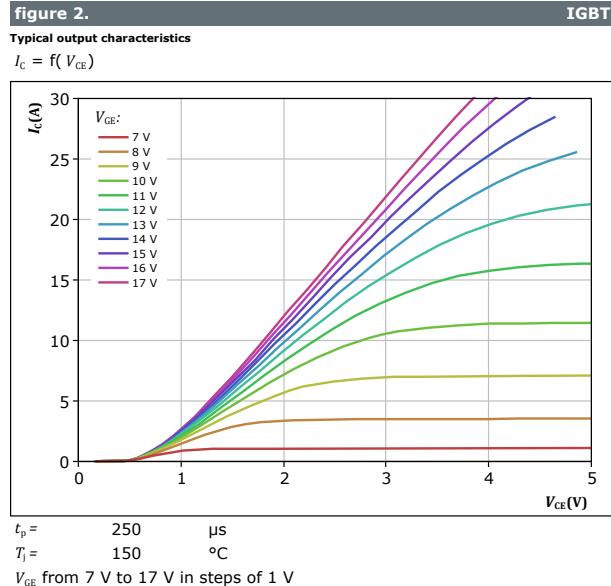
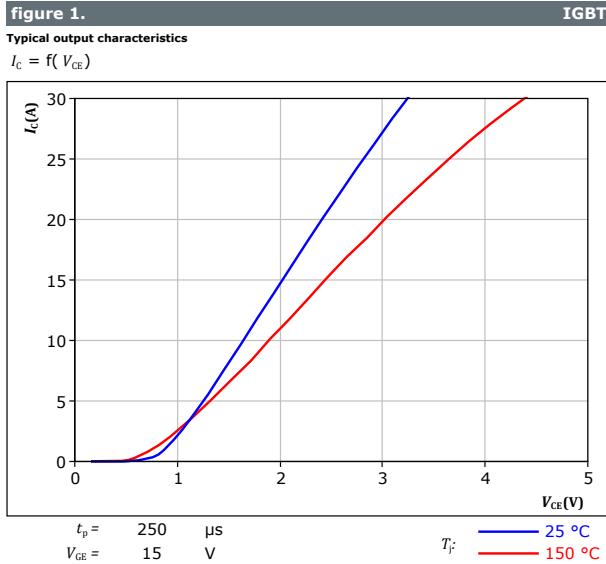
Rated resistance	$R$					25		1		kΩ
Deviation of R100	$A_{R/R}$	$R_{100} = 1670 \Omega$				100	-2		2	%
Maximum Current	$I_{max}$							3		mA
Power dissipation constant	$d$					25		0,76		mW/K
A-value	$A$							$7,635 \times 10^{-3}$		1/K
B-value	$B$							$1,73 \times 10^{-5}$		$1/K^2$
Vincotech Thermistor Reference								E		

<sup>(1)</sup> Value at chip level<sup>(2)</sup> Only valid with pre-applied Vincotech thermal interface material.



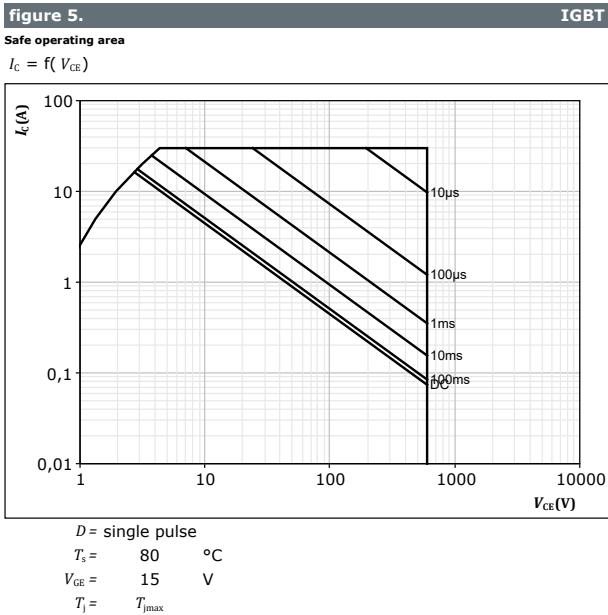
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## Inverter Switch Characteristics





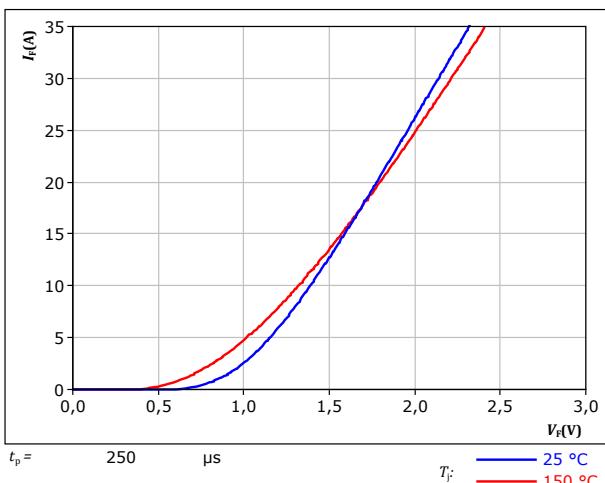
## Inverter Switch Characteristics





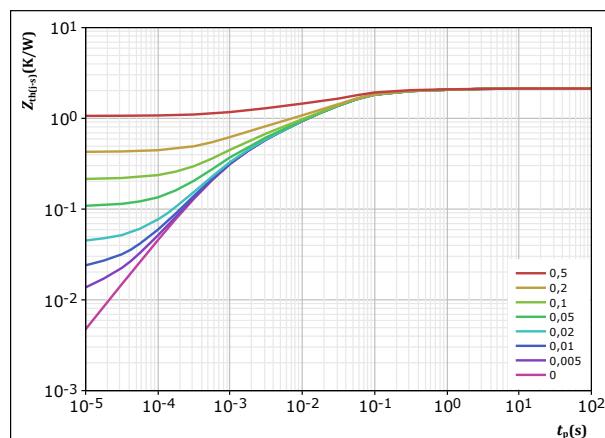
## Inverter Diode Characteristics

**figure 6.**  
Typical forward characteristics  
 $I_F = f(V_F)$



FWD

**figure 7.**  
Transient thermal impedance as a function of pulse width  
 $Z_{th(j-s)} = f(t_p)$



FWD

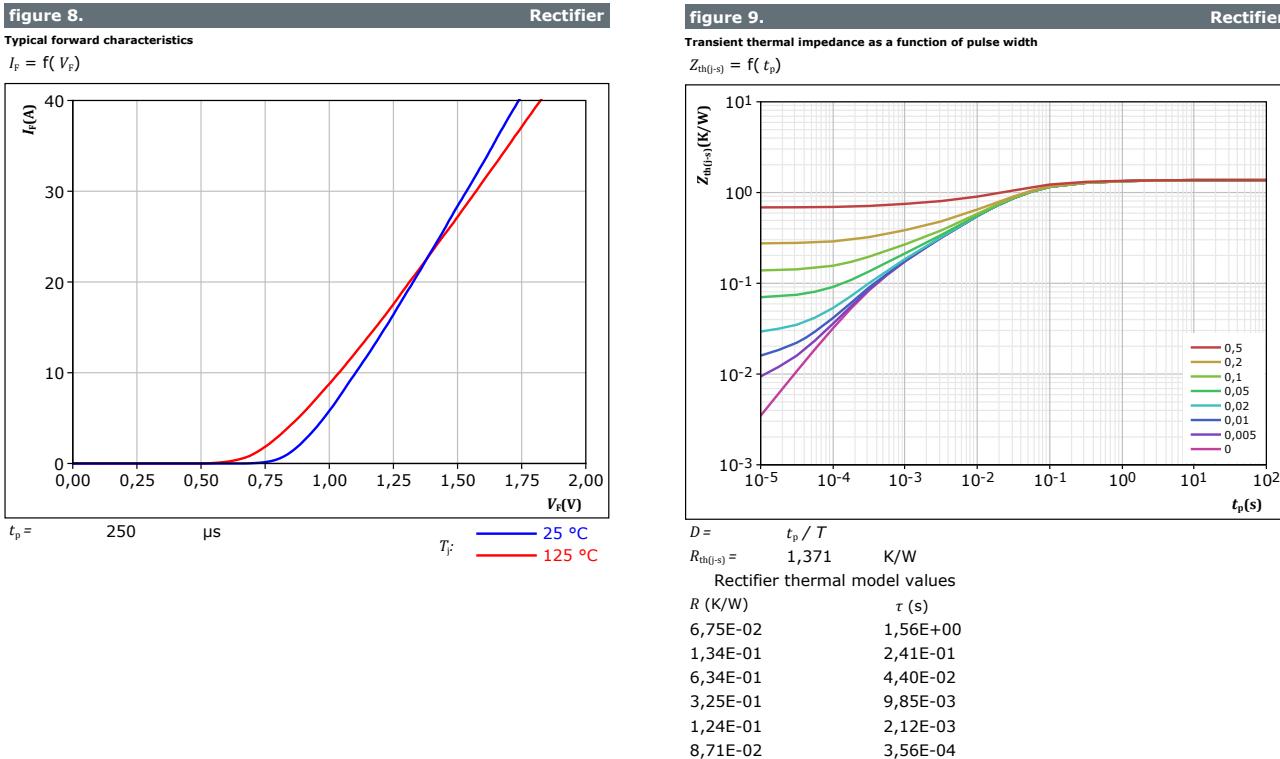
$$D = \frac{t_p / T}{2,129} \text{ K/W}$$

FWD thermal model values

$R$ (K/W)	$\tau$ (s)
1,15E-01	1,68E+00
2,75E-01	1,56E-01
1,07E+00	3,32E-02
4,14E-01	3,99E-03
2,60E-01	7,65E-04

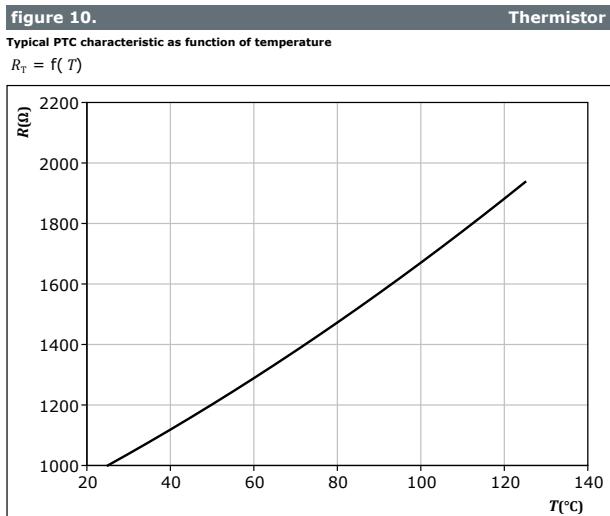


## Rectifier Diode Characteristics





## Thermistor Characteristics





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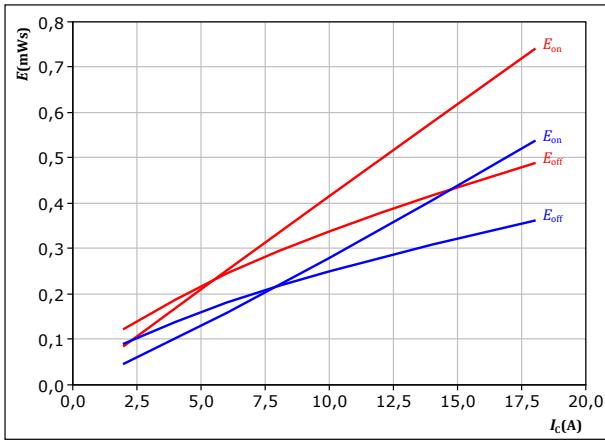
## Inverter Switching Characteristics

figure 11.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_c)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \text{ V} \\ V_{GE} &= \pm 15 \text{ V} \\ R_{gon} &= 32 \Omega \\ R_{goff} &= 32 \Omega \end{aligned}$$

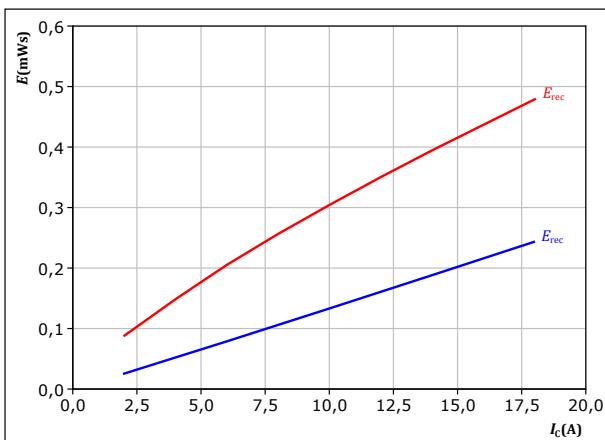
T<sub>f</sub>: — 25 °C — 150 °C

figure 13.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_c)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \text{ V} \\ V_{GE} &= \pm 15 \text{ V} \\ R_{gon} &= 32 \Omega \end{aligned}$$

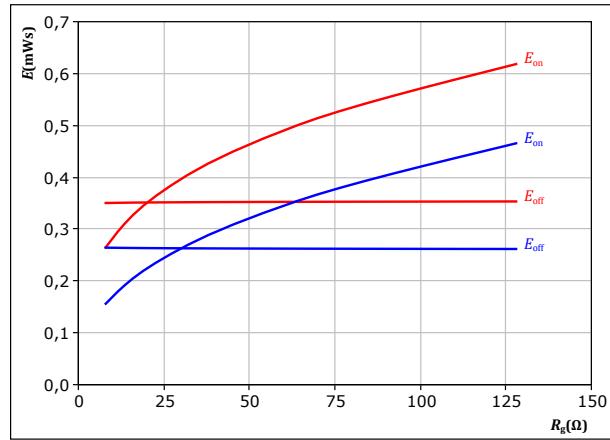
T<sub>f</sub>: — 25 °C — 150 °C

figure 12.

IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor

$$E = f(R_g)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \text{ V} \\ V_{GE} &= \pm 15 \text{ V} \\ I_c &= 10 \text{ A} \end{aligned}$$

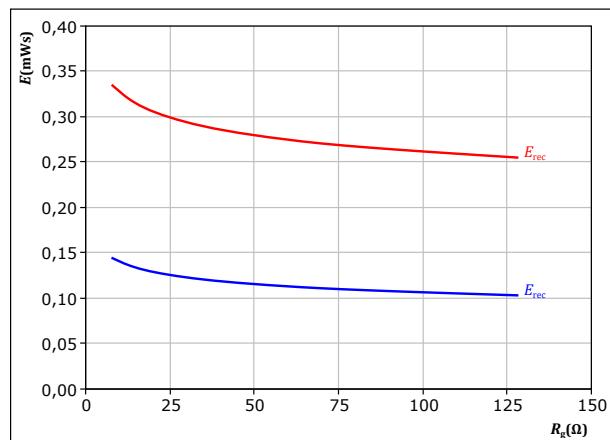
T<sub>f</sub>: — 25 °C — 150 °C

figure 14.

FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \text{ V} \\ V_{GE} &= \pm 15 \text{ V} \\ I_c &= 10 \text{ A} \end{aligned}$$

T<sub>f</sub>: — 25 °C — 150 °C

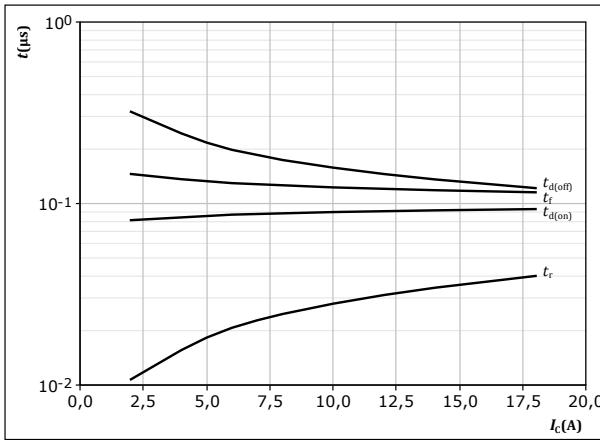


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## Inverter Switching Characteristics

figure 15. IGBT

Typical switching times as a function of collector current  
 $t = f(I_C)$

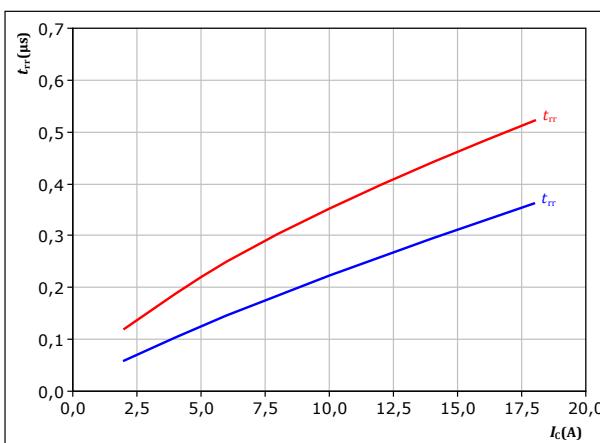


With an inductive load at

$T_j = 150^\circ\text{C}$   
 $V_{CE} = 300 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 32 \Omega$   
 $R_{goff} = 32 \Omega$

figure 17. FWD

Typical reverse recovery time as a function of collector current  
 $t_{rr} = f(I_C)$

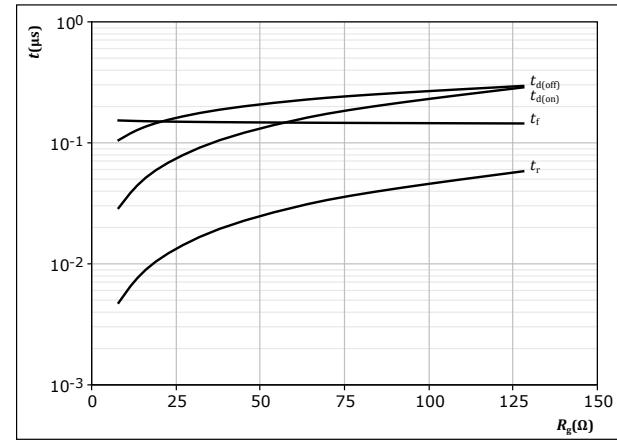


With an inductive load at

$V_{CE} = 300 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 32 \Omega$

figure 16. IGBT

Typical switching times as a function of IGBT turn on gate resistor  
 $t = f(R_g)$

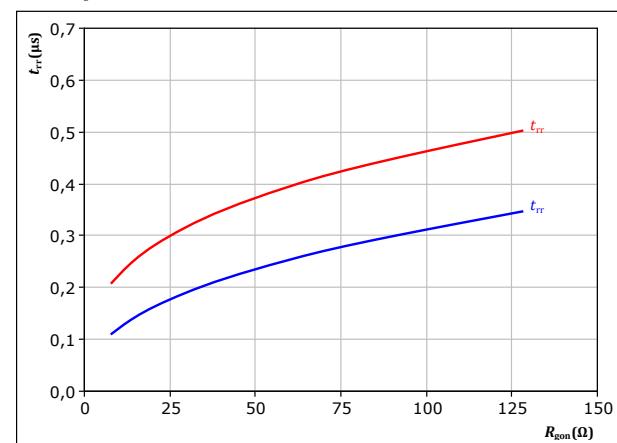


With an inductive load at

$T_j = 150^\circ\text{C}$   
 $V_{CE} = 300 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 10 \text{ A}$

figure 18. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor  
 $t_{rr} = f(R_{gon})$



With an inductive load at

$V_{CE} = 300 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 10 \text{ A}$



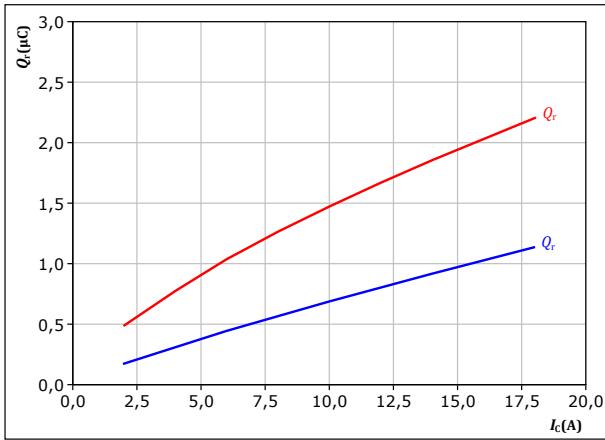
## Inverter Switching Characteristics

figure 19.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

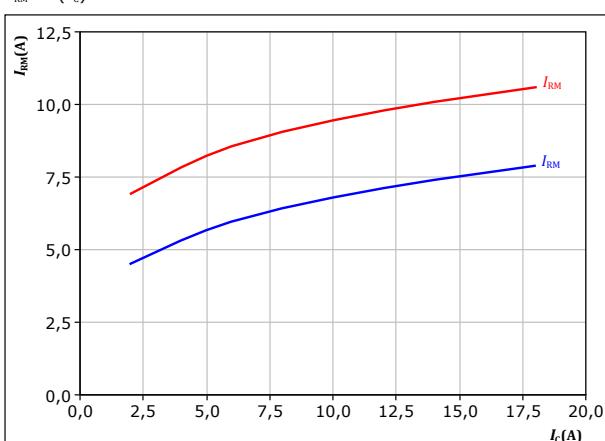
$T_f:$  25^\circ\text{C} 150^\circ\text{C}

figure 21.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 32 \quad \Omega \end{aligned}$$

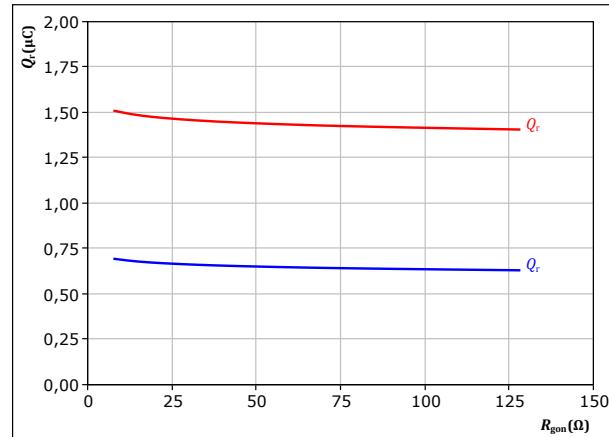
$T_f:$  25^\circ\text{C} 150^\circ\text{C}

figure 20.

FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_c &= 10 \quad \text{A} \end{aligned}$$

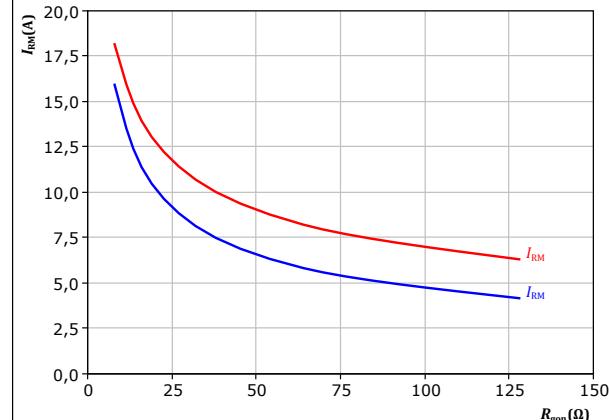
$T_f:$  25^\circ\text{C} 150^\circ\text{C}

figure 22.

FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$$\begin{aligned} V_{CE} &= 300 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_c &= 10 \quad \text{A} \end{aligned}$$

$T_f:$  25^\circ\text{C} 150^\circ\text{C}

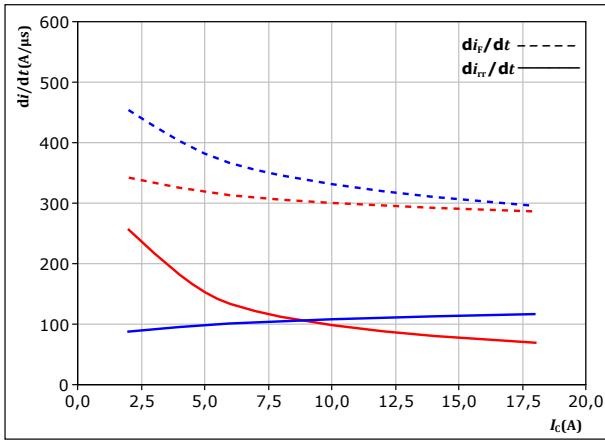


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## Inverter Switching Characteristics

figure 23. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current  
 $di_f/dt, di_{rr}/dt = f(I_c)$

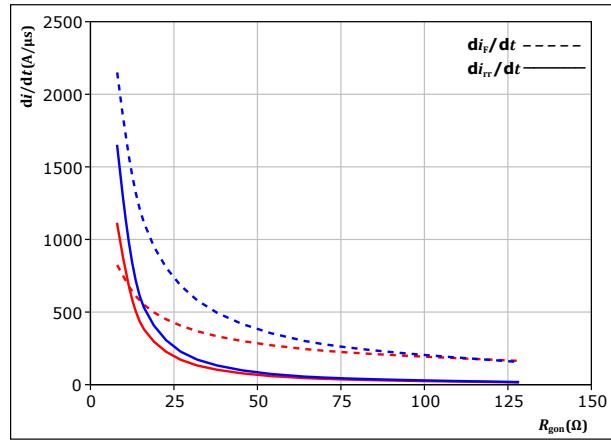


With an inductive load at

$V_{CE} = 300 \text{ V}$        $T_j = 25 \text{ }^\circ\text{C}$   
 $V_{GE} = \pm 15 \text{ V}$        $T_j = 150 \text{ }^\circ\text{C}$   
 $R_{gon} = 32 \Omega$

figure 24. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor  
 $di_f/dt, di_{rr}/dt = f(R_{gon})$

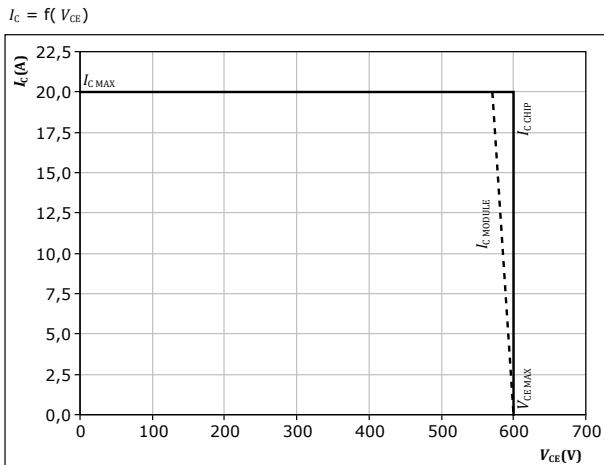


With an inductive load at

$V_{CE} = 300 \text{ V}$        $T_j = 25 \text{ }^\circ\text{C}$   
 $V_{GE} = \pm 15 \text{ V}$        $T_j = 150 \text{ }^\circ\text{C}$   
 $I_c = 10 \text{ A}$

figure 25. IGBT

Reverse bias safe operating area



At       $T_j = 150 \text{ }^\circ\text{C}$   
 $R_{gon} = 32 \Omega$   
 $R_{goff} = 32 \Omega$

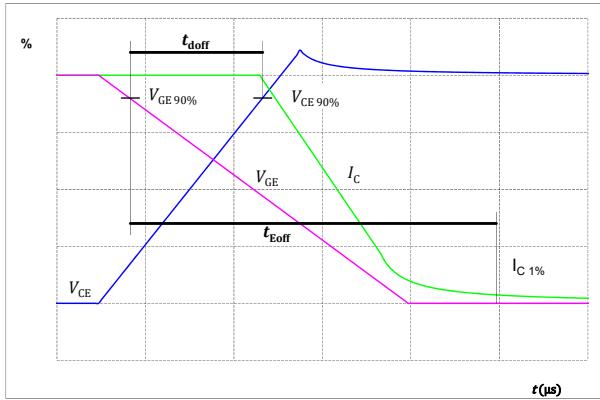


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## Inverter Switching Definitions

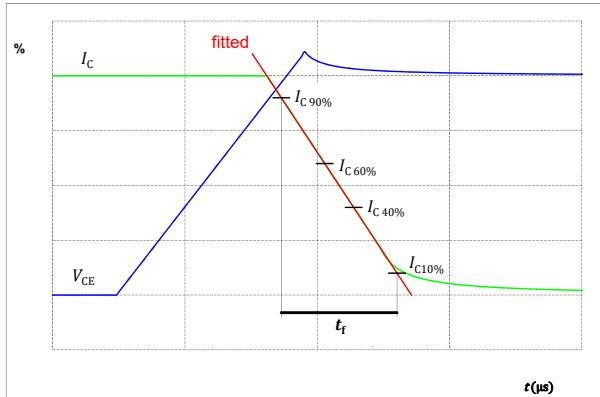
**figure 26.** IGBT

Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$  ( $t_{Eoff}$  = integrating time for  $E_{off}$ )



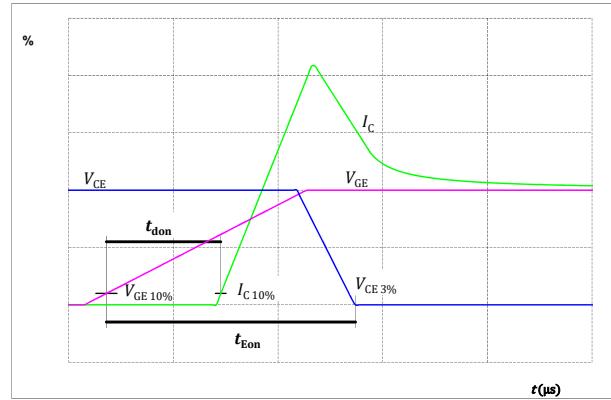
**figure 28.** IGBT

Turn-off Switching Waveforms & definition of  $t_f$



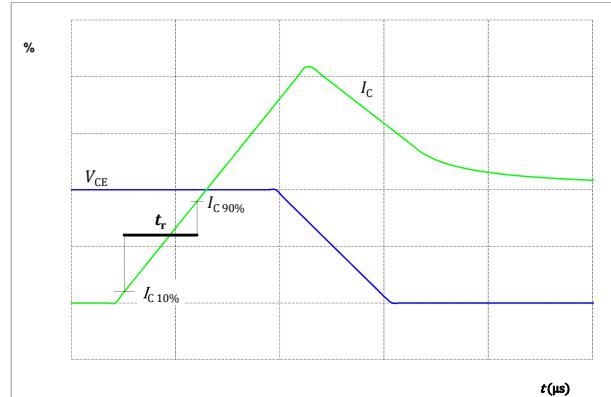
**figure 27.** IGBT

Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$  ( $t_{Eon}$  = integrating time for  $E_{on}$ )



**figure 29.** IGBT

Turn-on Switching Waveforms & definition of  $t_r$





## Inverter Switching Definitions

figure 30.

Turn-off Switching Waveforms & definition of  $t_{tr}$

FWD

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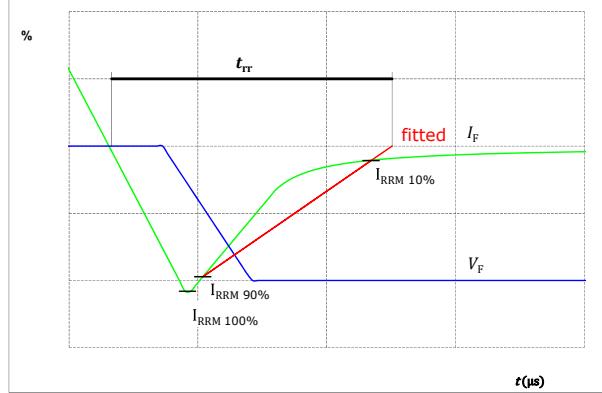
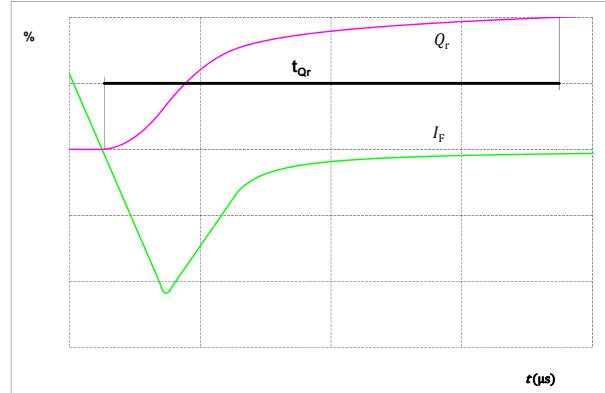


figure 31.

Turn-on Switching Waveforms & definition of  $t_{Qr}$  ( $t_{Qr}$  = integrating time for  $Q_r$ )

FWD

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# **80-M006PNB010SA-K615C**

## datasheet

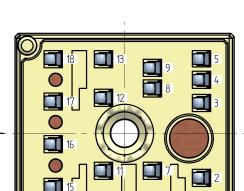
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Ordering Code	
Version	Ordering Code
With std lid (6.5mm height) + no thermal grease	80-M006PNB010SA-K615C-/0A/
With thin lid (2.8mm height) + no thermal grease	80-M006PNB010SA-K615C-/0B/
With std lid (6.5mm height) + thermal grease (0.8 W/mK, P12, silicone-based)	80-M006PNB010SA-K615C-/1A/
With thin lid (2.8mm height) + thermal grease (0.8 W/mK, P12, silicone-based)	80-M006PNB010SA-K615C-/1B/
With std lid (6.5mm height) + thermal grease (2.5 W/mK, TG20032, silicone-free)	80-M006PNB010SA-K615C-/4A/
With thin lid (2.8mm height) + thermal grease (2.5 W/mK, TG20032, silicone-free)	80-M006PNB010SA-K615C-/4B/
With std lid (6.5mm height) + thermal grease (2.5 W/mK, HPTP, silicone-based)	80-M006PNB010SA-K615C-/5A/
With thin lid (2.8mm height) + thermal grease (2.5 W/mK, HPTP, silicone-based)	80-M006PNB010SA-K615C-/5B/

Marking						
Text	Name		Type&Ver	Date code	VIN & Lot	Serial&UL
	NN-NNNNNNNNNNNNNN		TTTTTTVV	WWYY	VIN LLLL	SSSS UL
	Type&Ver	Lot number	Serial	Date code		
Datamatrix	TTTTTTVV		SSSS	WWYY		
NNNNNNNNNNNNNNNN NNNN-YYYYVV VIN LLLL WWYY SSSS UL						

**Outline**

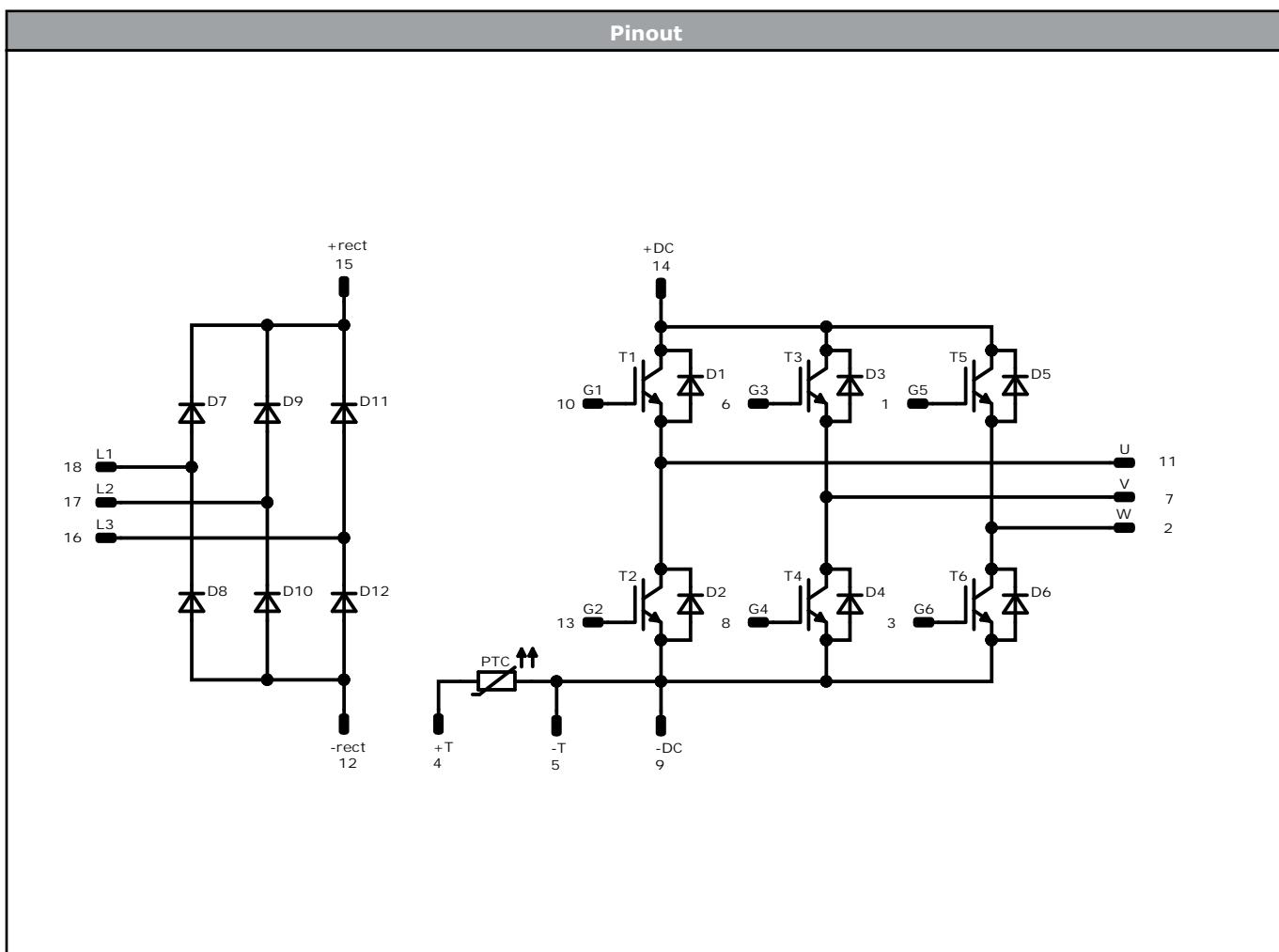
Pin table [mm]			
Pin	X	Y	Function
1	11,93	-11,5	G5
2	11,93	-6,9	W
3	11,93	4,71	G6
4	11,93	8,3	+T
5	11,93	11,5	-T
6	4,33	-11,5	G3
7	4,33	-5,8	V
8	4,33	6,95	G4
9	4,33	10,15	-DC
10	-3,27	-11,5	G1
11	-3,27	-5,8	U
12	-3,27	5,5	-RECT
13	-3,27	11,5	G2
14	-11,07	-11,5	+DC
15	-11,07	-8,3	+RECT
16	-11,07	-1,68	L3
17	-11,07	4,93	L2
18	-11,07	11,5	L1



Pad positions refers to center point. For more informations on pad design please see package data



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**Identification**

ID	Component	Voltage	Current	Function	Comment
T2, T1, T4, T3, T6, T5	IGBT	600 V	10 A	Inverter Switch	
D1, D2, D3, D4, D5, D6	FWD	600 V	20 A	Inverter Diode	
D8, D7, D10, D9, D12, D11	Rectifier	1600 V	14 A	Rectifier Diode	
PTC	Thermistor			Thermistor	

**80-M006PNB010SA-K615C**

datasheet

**Vincotech****Packaging instruction**

Standard packaging quantity (SPQ) 198	>SPQ	Standard	<SPQ	Sample
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**Handling instruction**

Handling instructions for MiniSKiiP® 0 packages see vincotech.com website.

**Package data**

Package data for MiniSKiiP® 0 packages see vincotech.com website.

**Vincotech thermistor reference**

See Vincotech thermistor reference table at vincotech.com website.

**UL recognition and file number**

This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.



Document No.:	Date:	Modification:	Pages
80-M006PNB010SA-K615C-D4-14	31 Aug. 2023	Rectifier diode, surge (non-repetitive) forward current Introduce Rth values with HPTP New Datasheet format, module is unchanged Separate datasheet	

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.