Vincotech

30-F2126PA075SC-L288F09

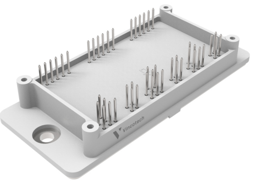
datasheet

***flow*PACK 2** **1200 V / 75 A**

Features

- Trench Fieldstop IGBTs for low saturation losses
- Open emitter configuration
- Compact and low inductive design
- Integrated NTC

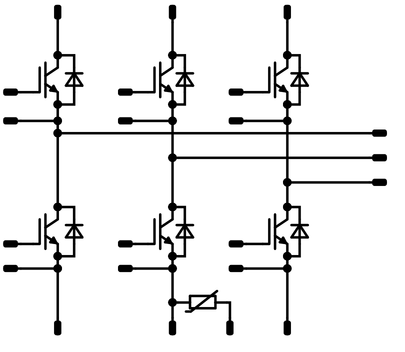
flow 2 17 mm housing



Target applications

- Elevator Drives
- Embedded Drives
- Industrial Drives

Schematic



Types

- 30-F2126PA075SC-L288F09

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Inverter Switch					
Collector-emitter voltage	V_{CES}			1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	90	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		225	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	232	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 800 V	T_j = 150 °C	10	µs
Maximum junction temperature	T_{jmax}			175	°C
Inverter Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	84	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		150	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	154	W
Maximum junction temperature	T_{jmax}			175	°C
Module Properties					
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	4000	V
Isolation voltage	V_{isol}	AC Voltage	t_p = 1 min	2500	V
Creepage distance				min. 12,7	mm
Clearance				min. 12,7	mm
Comparative Tracking Index	CTI			≥ 200	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0026	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	V_{CEsat}		15		75	25 125 150	1,58	1,83 2,12 2,19	2,07(1)	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			1	µA
Gate-emitter leakage current	I_{GES}		20	0		25			120	nA
Internal gate resistance	r_g							10		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		4300		pF
Reverse transfer capacitance	C_{res}							160		pF
Gate charge	Q_g		15		0	25		570		nC
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,41		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 4 Ω R_{goff} = 4 Ω	±15	600	75	25 125 150		149,4 160 164,4		ns
Rise time	t_r					25 125 150		38,8 44,2 43,2		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		250,2 311,8 328,6		ns
Fall time	t_f					25 125 150		75,97 121,53 135,61		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=7,18 µC Q_{rFWD}=12,83 µC Q_{rFWD}=14,76 µC				25 125 150		7,68 10,72 11,42		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		4,22 6,49 7,22		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Diode										
Static										
Forward voltage	V_F				75	25 125 150	1,35	1,74 1,75 1,74	2,05(1)	V
Reverse leakage current	I_R	V_r = 1200 V				25			14	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,62		K/W
Dynamic										
Peak recovery current	I_{RRM}	di/dt=2324 A/µs di/dt=1749 A/µs di/dt=1360 A/µs	±15	600	75	25 125 150		40,35 49,16 51,85		A
Reverse recovery time	t_{rr}					25 125 150		351,15 496,84 546,36		ns
Recovered charge	Q_r					25 125 150		7,18 12,83 14,76		µC
Reverse recovered energy	E_{rec}					25 125 150		2,5 4,59 5,37		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		116,4 102,53 98,97		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Thermistor										
Static										
Rated resistance	R					25		22		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	R_{100} = 1484 Ω				100	-5		5	%
Power dissipation	P							5		mW
Power dissipation constant	d					25		1,5		mW/K
B-value	$B_{(25/50)}$	Tol. ±1 %						3962		K
B-value	$B_{(25/100)}$	Tol. ±1 %						4000		K
Vincotech Thermistor Reference									I	

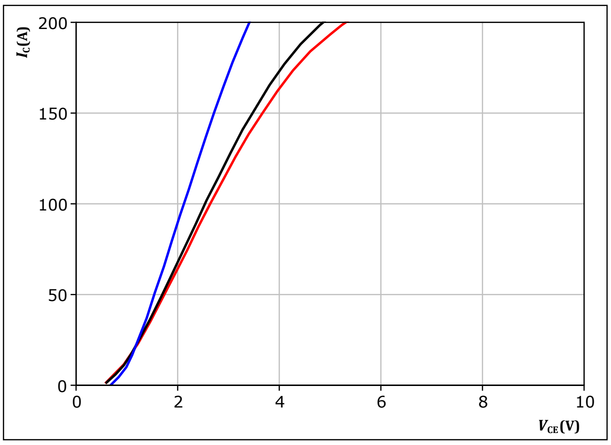
(1) Value at chip level
(2) Only valid with pre-applied Vincotech thermal interface material.

Inverter Switch Characteristics

figure 1. **IGBT**

Typical output characteristics

$I_C = f(V_{CE})$

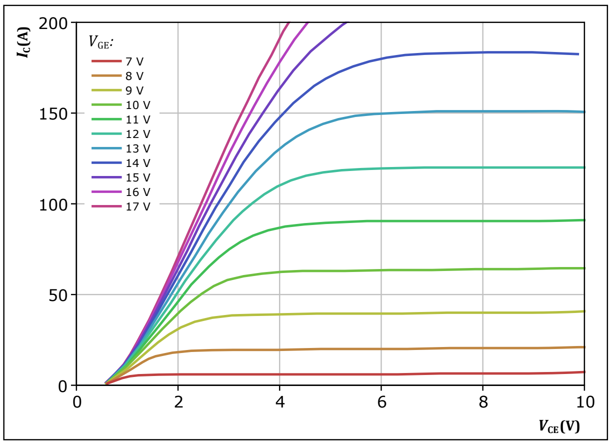


t_p = 250 μs
V_{GE} = 15 V
T_j: 25 °C, 125 °C, 150 °C

figure 2. **IGBT**

Typical output characteristics

$I_C = f(V_{CE})$

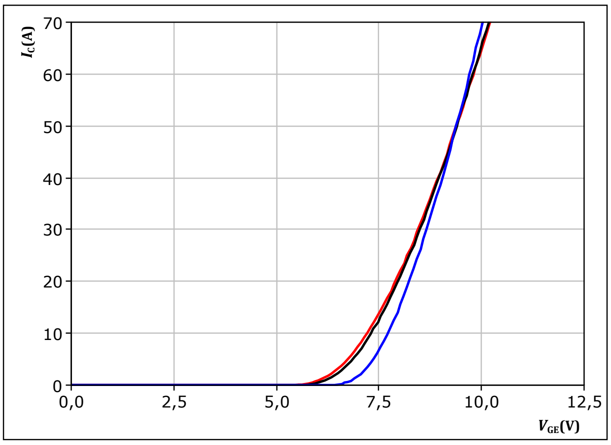


t_p = 250 μs
T_j = 150 °C
V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. **IGBT**

Typical transfer characteristics

$I_C = f(V_{GE})$

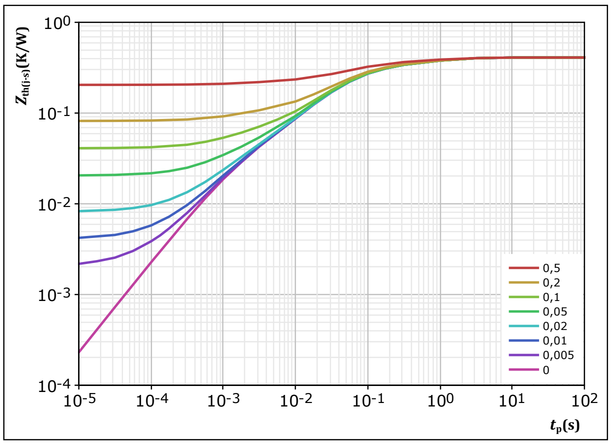


t_p = 250 μs
V_{CE} = 10 V
T_j: 25 °C, 125 °C, 150 °C

figure 4. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T
$R_{th(j-s)}$ = 0,409 K/W

IGBT thermal model values

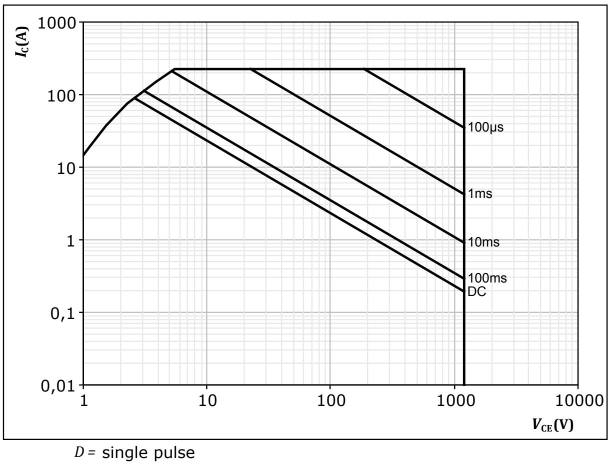
R (K/W)	τ (s)
6,08E-02	1,41E+00
9,91E-02	1,99E-01
1,78E-01	4,61E-02
5,03E-02	1,41E-02
2,16E-02	1,42E-03

Inverter Switch Characteristics

figure 5. **IGBT**

Safe operating area

I_C = f(V_{CE})



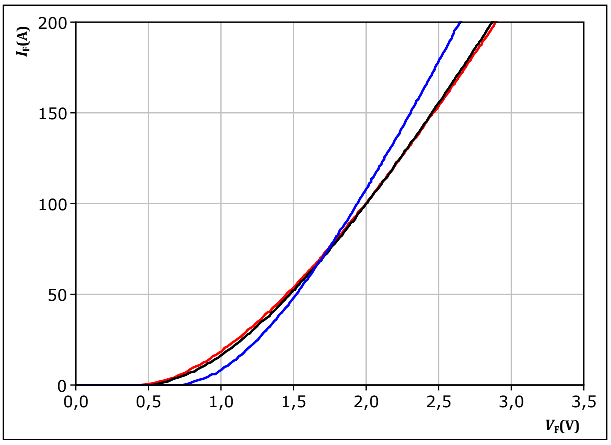
D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Inverter Diode Characteristics

figure 6. **FWD**

Typical forward characteristics

I_F = f(V_F)



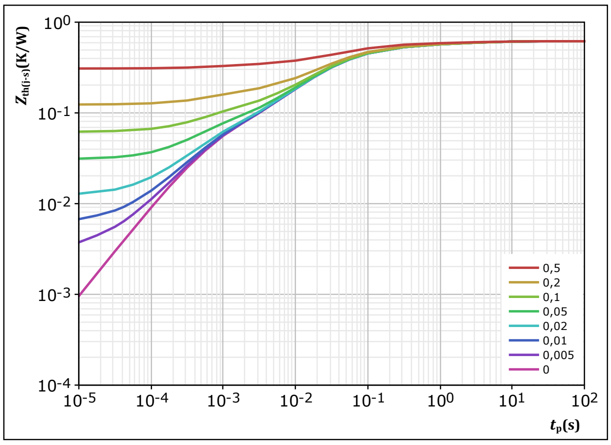
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 7. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 0,617 K/W

FWD thermal model values

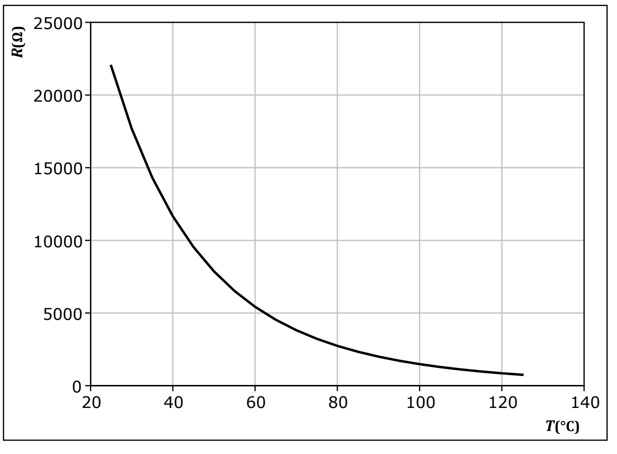
R (K/W)	τ (s)
4,35E-02	4,66E+00
7,48E-02	5,44E-01
1,95E-01	8,13E-02
2,13E-01	2,26E-02
4,51E-02	5,48E-03
4,51E-02	5,92E-04

Thermistor Characteristics

figure 8.	Thermistor

Typical NTC characteristic as function of temperature

R_T = f(T)

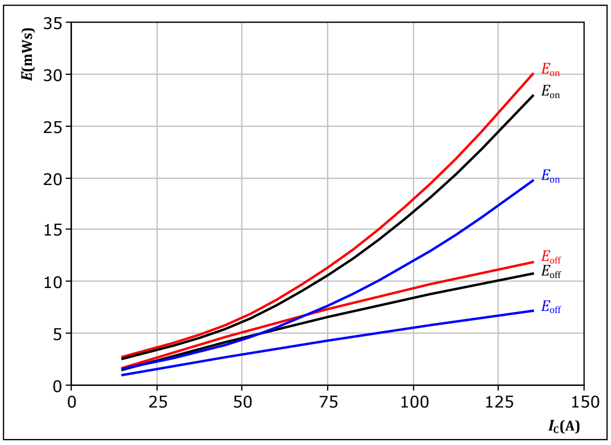


Inverter Switching Characteristics

figure 9. **IGBT**

Typical switching energy losses as a function of collector current

$E = f(I_C)$



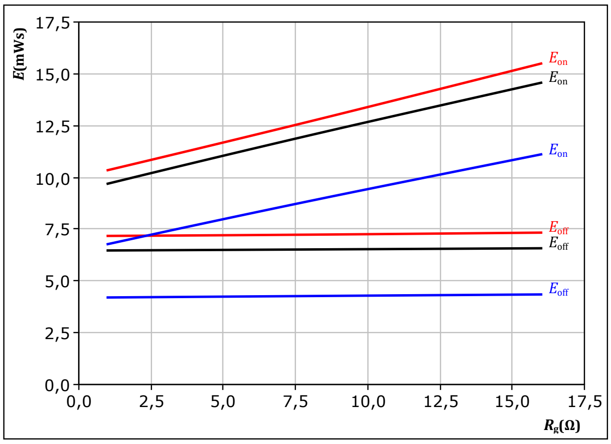
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	4	Ω		150 °C
R_{goff} =	4	Ω		

figure 10. **IGBT**

Typical switching energy losses as a function of gate resistor

$E = f(R_g)$



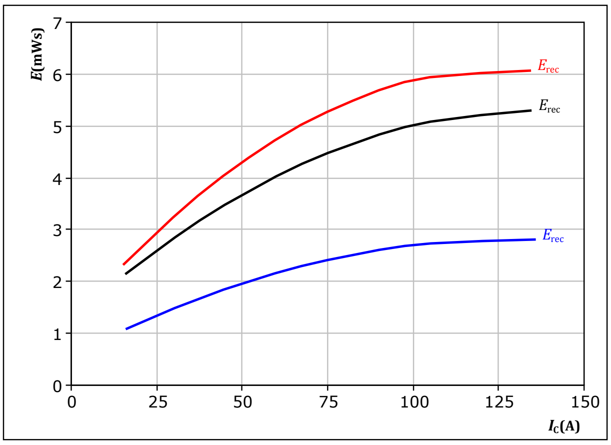
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	75	A		150 °C

figure 11. **FWD**

Typical reverse recovered energy loss as a function of collector current

$E_{rec} = f(I_C)$



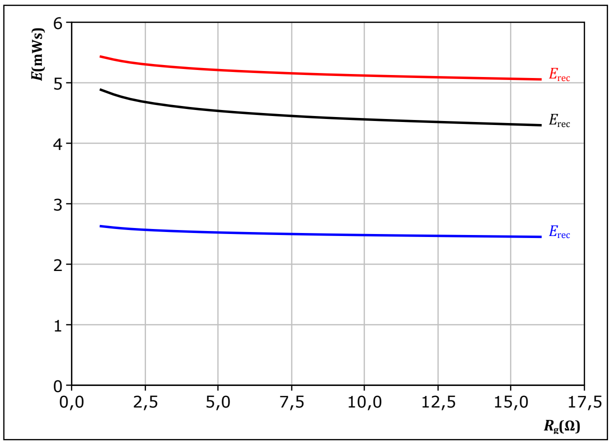
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	4	Ω		150 °C

figure 12. **FWD**

Typical reverse recovered energy loss as a function of gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

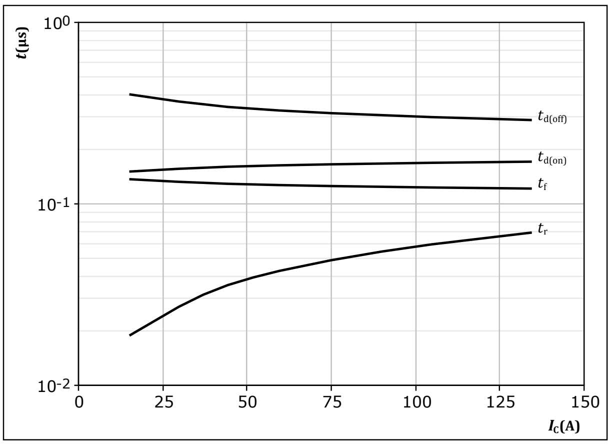
V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	75	A		150 °C

Inverter Switching Characteristics

figure 13. **IGBT**

Typical switching times as a function of collector current

$t = f(I_C)$



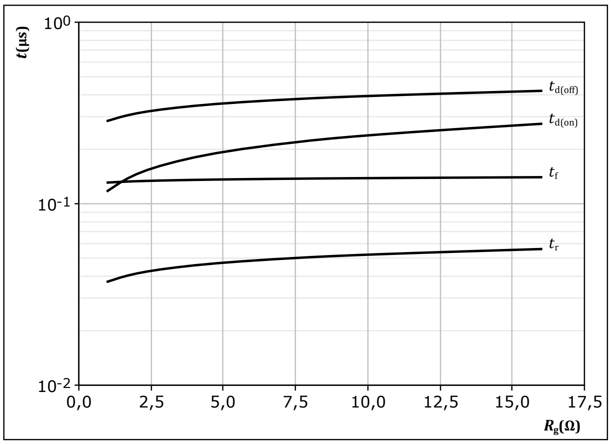
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	4	Ω
R_{goff} =	4	Ω

figure 14. **IGBT**

Typical switching times as a function of gate resistor

$t = f(R_g)$



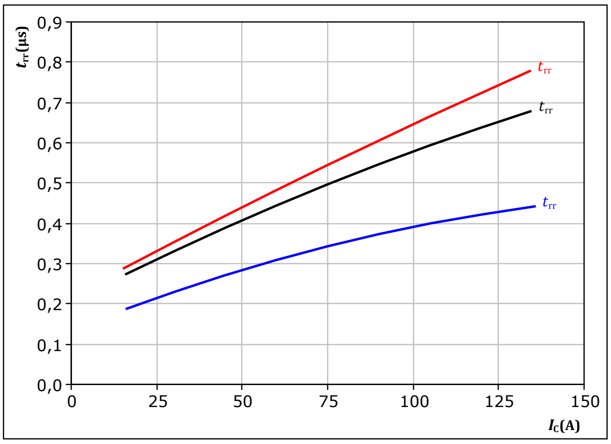
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	75	A

figure 15. **FWD**

Typical reverse recovery time as a function of collector current

$t_{rr} = f(I_C)$



With an inductive load at

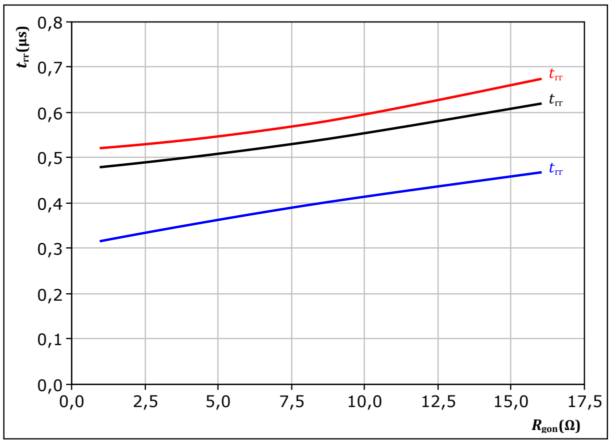
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	4	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 16. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

$t_{rr} = f(R_{gon})$



With an inductive load at

V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	75	A

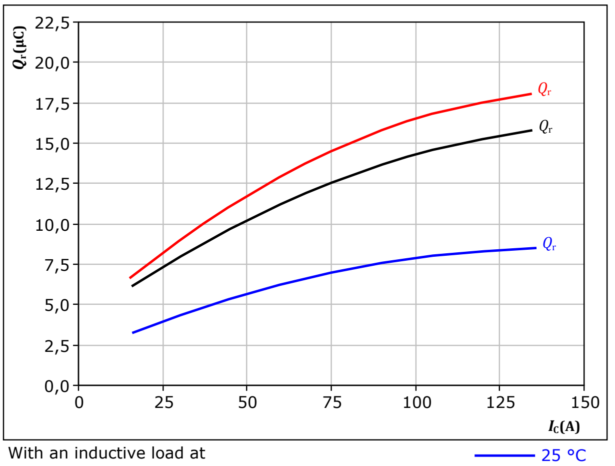
T_j: 25 °C, 125 °C, 150 °C

Inverter Switching Characteristics

figure 17. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



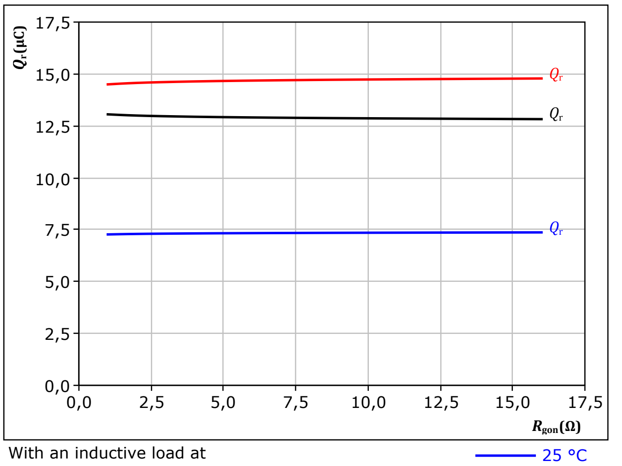
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	4	Ω		150 °C

figure 18. **FWD**

Typical recovered charge as a function of turn on gate resistor

Q_r = f(R_{gon})



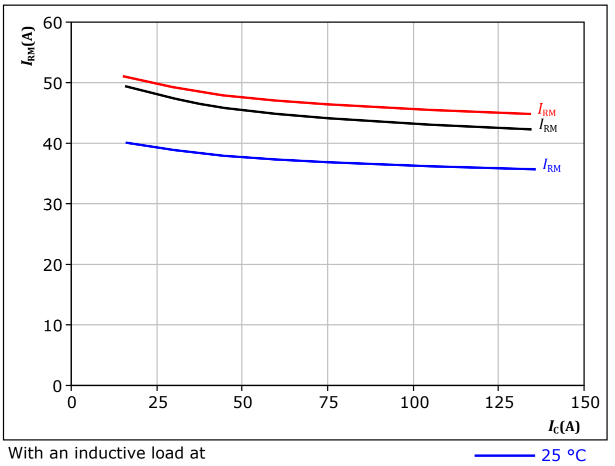
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	75	A		150 °C

figure 19. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



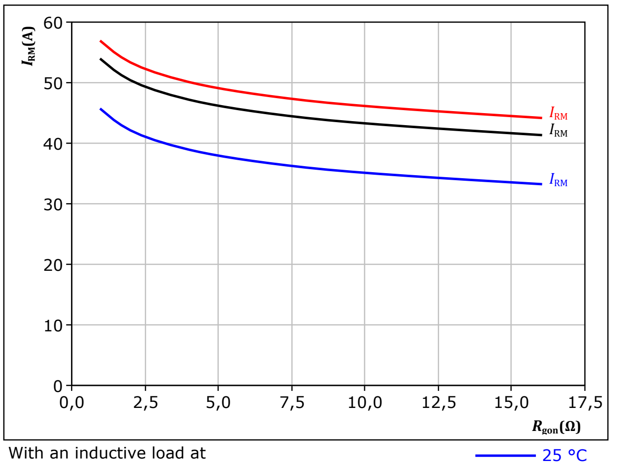
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	4	Ω		150 °C

figure 20. **FWD**

Typical peak reverse recovery current as a function of turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

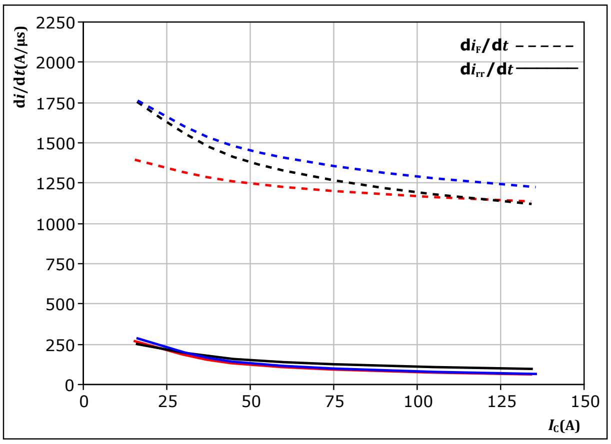
V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	75	A		150 °C

Inverter Switching Characteristics

figure 21. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

$di_F/dt,\ di_{rr}/dt = f(I_C)$



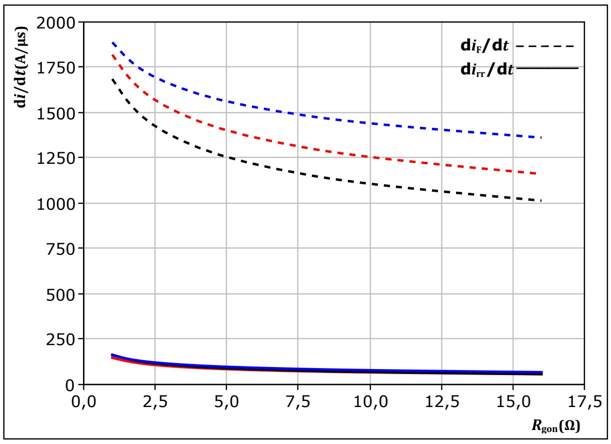
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	4	Ω		150 °C

figure 22. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

$di_F/dt,\ di_{rr}/dt = f(R_{gon})$



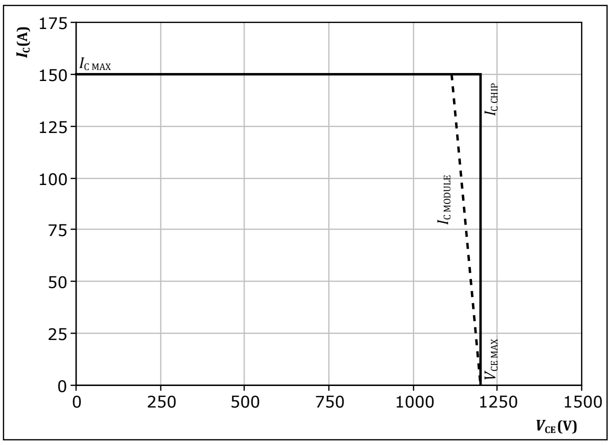
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	75	A		150 °C

figure 23. **IGBT**

Reverse bias safe operating area

$I_C = f(V_{CE})$



At	T_j =	150	°C
	R_{gon} =	4	Ω
	R_{goff} =	4	Ω

Inverter Switching Definitions

figure 24. IGBT

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{tEoff} = integrating time for E_{off})

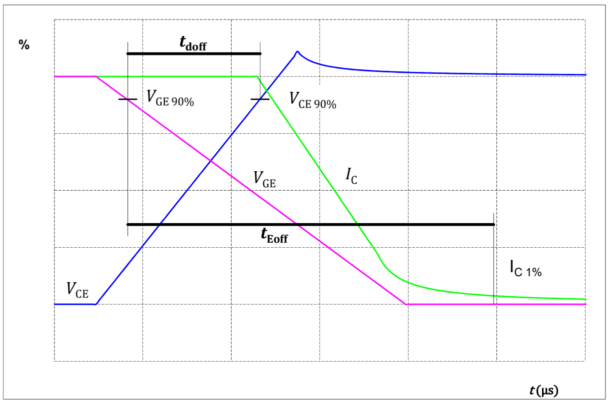


figure 25. IGBT

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} = integrating time for E_{on})

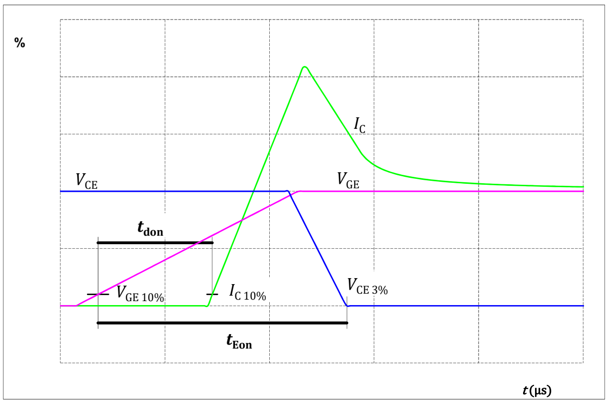


figure 26. IGBT

Turn-off Switching Waveforms & definition of t_f

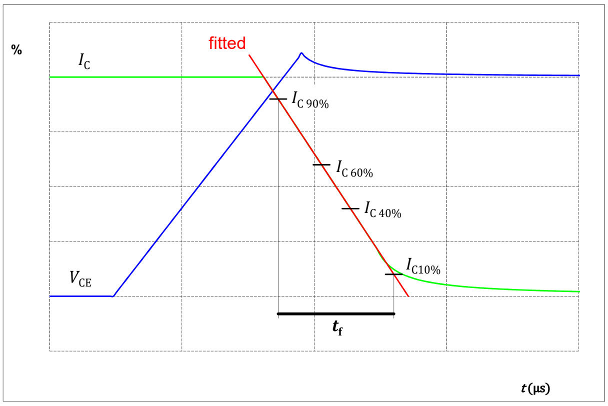
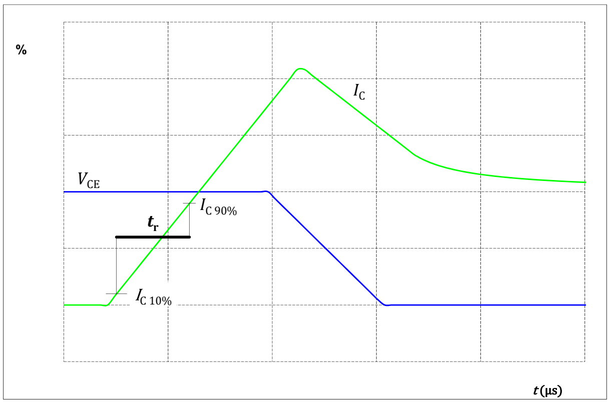


figure 27. IGBT

Turn-on Switching Waveforms & definition of t_r



Inverter Switching Definitions

figure 28. **FWD**

Turn-off Switching Waveforms & definition of t_{rr}

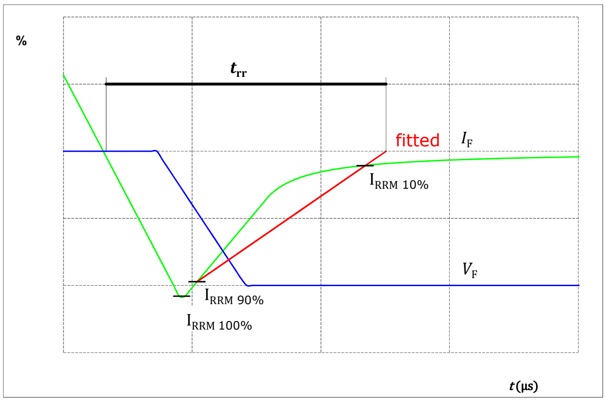
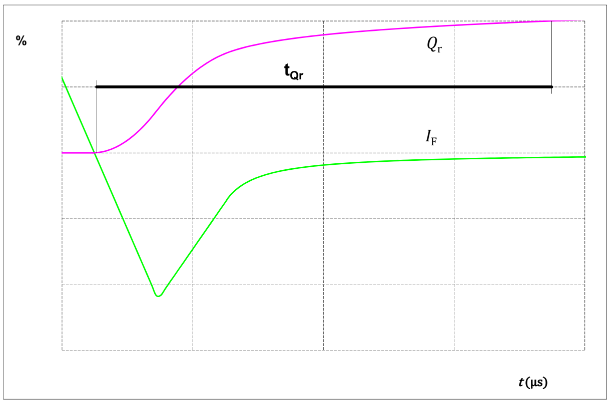


figure 29. **FWD**

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



Ordering Code

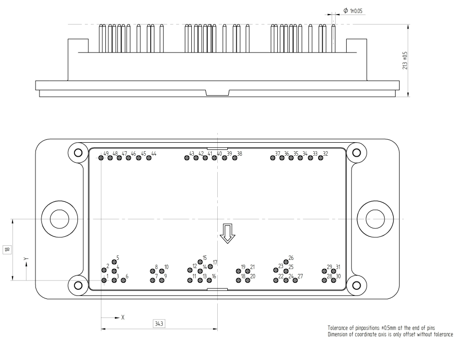
Version	Ordering Code
Without thermal paste	30-F2126PA075SC-L288F09
With thermal paste	30-F2126PA075SC-L288F09-/3/

Marking

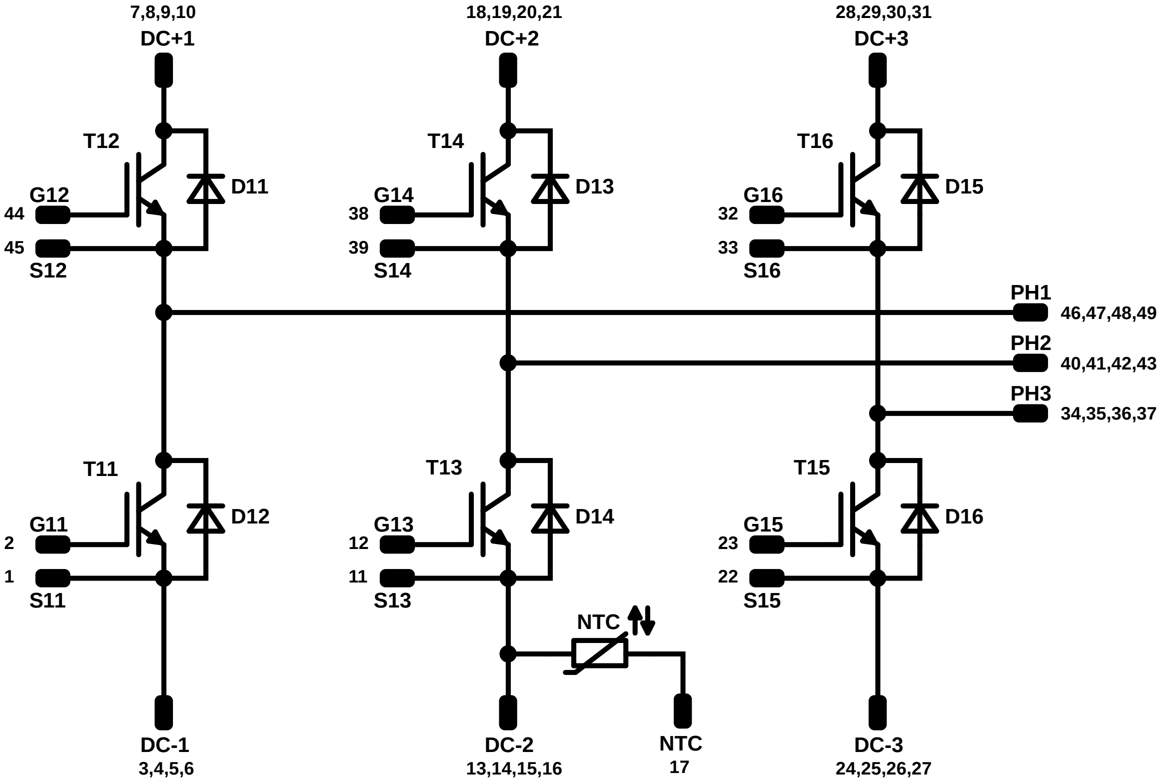
Text	Name		Date code	UL & VIN	Lot	Serial
	NN-NNNNNNNNNNNNNN-TTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	Type&Ver	Lot number	Serial	Date code		
	TTTTTTTVV	LLLLL	SSSS	WWYY		

Outline

Pin table [mm]							
Pin	X	Y	Function	26	54,5	5,4	DC-3
1	0,899999999999999	0	S11	27	57,2	0	DC-3
2	0,899999999999999	3	G11	28	65,8	0	DC+3
3	3,9	0	DC-1	29	65,8	2,7	DC+3
4	3,9	2,7	DC-1	30	68,5	0	DC+3
5	3,9	5,4	DC-1	31	68,5	2,7	DC+3
6	6,6	0	DC-1	32	64,7	36	G16
7	15,2	0	DC+1	33	61,7	36	S16
8	15,2	2,7	DC+1	34	58,7	36	PH3
9	17,9	0	DC+1	35	56	36	PH3
10	17,9	2,7	DC+1	36	53,3	36	PH3
11	26,2	0	S13	37	50,6	36	PH3
12	26,2	3	G13	38	39,4	36	G14
13	29,2	0	DC-2	39	36,4	36	S14
14	29,2	2,7	DC-2	40	33,4	36	PH2
15	29,2	5,4	DC-2	41	30,7	36	PH2
16	31,9	0	DC-2	42	28	36	PH2
17	32,2	4,05	NTC	43	25,3	36	PH2
18	40,5	0	DC+2	44	14,1	36	G12
19	40,5	2,7	DC+2	45	11,1	36	S12
20	43,2	0	DC+2	46	8,1	36	PH1
21	43,2	2,7	DC+2	47	5,4	36	PH1
22	51,5	0	S15	48	2,7	36	PH1
23	51,5	3	G15	49	0	36	PH1
24	54,5	0	DC-3				
25	54,5	2,7	DC-3				



Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	75 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	75 A	Inverter Diode	
Rt	Thermistor			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 36	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow* 2 packages see vincotech.com website.

Package data
Package data for *flow* 2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
30-F2126PA075SC-L288F09-D1-14	2 Oct. 2020	Initial Release	