



flowMNPC 0

1200 V / 80 A

**Features**

- mixed voltage component topology
- neutral point clamped inverter
- reactive power capability
- low inductance layout
- improved LVRT

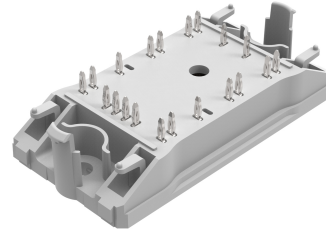
**Target applications**

- Solar inverter
- UPS

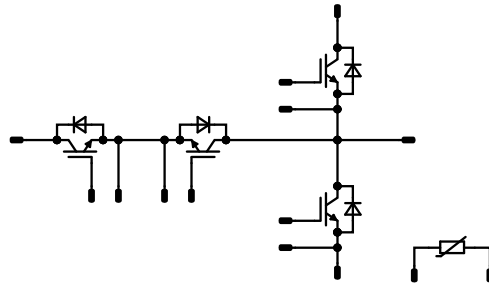
**Types**

- 10-PZ12NMA080SH23-M260F03Y

**flow 0 12 mm housing**



**Schematic**





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## Maximum Ratings

$T_j = 25\text{ °C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
<b>Buck Switch</b>				
Collector-emitter voltage	$V_{CES}$		1200	V
Collector current (DC current)	$I_C$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	76	A
Repetitive peak collector current	$I_{CRM}$	$t_p$ limited by $T_{jmax}$	240	A
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	186	W
Gate-emitter voltage	$V_{GES}$		$\pm 20$	V
Short circuit ratings	$t_{SC}$	$V_{GE} = 15\text{ V}$ , $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	10	$\mu\text{s}$
Maximum junction temperature	$T_{jmax}$		175	$^{\circ}\text{C}$

## Buck Diode

Peak repetitive reverse voltage	$V_{RRM}$		650	V
Forward current (DC current)	$I_F$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	55	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_{jmax}$	150	A
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	71	W
Maximum junction temperature	$T_{jmax}$		175	$^{\circ}\text{C}$

## Boost Switch

Collector-emitter voltage	$V_{CES}$		600	V
Collector current (DC current)	$I_C$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	65	A
Repetitive peak collector current	$I_{CRM}$	$t_p$ limited by $T_{jmax}$	225	A
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	101	W
Gate-emitter voltage	$V_{GES}$		$\pm 20$	V
Short circuit ratings	$t_{SC}$	$V_{GE} = 15\text{ V}$ , $V_{CC} = 360\text{ V}$ $T_j = 150\text{ °C}$	6	$\mu\text{s}$
Maximum junction temperature	$T_{jmax}$		175	$^{\circ}\text{C}$



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**10-PZ12NMA080SH23-M260F03Y**  
datasheet

## Maximum Ratings

$T_j = 25\text{ °C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
<b>Boost Diode</b>				
Peak repetitive reverse voltage	$V_{RRM}$		1200	V
Forward current (DC current)	$I_F$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	53	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_{jmax}$	100	A
Total power dissipation	$P_{tot}$	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	90	W
Maximum junction temperature	$T_{jmax}$		175	°C

## Module Properties

### Thermal Properties

Storage temperature	$T_{stg}$		-40...+125	°C
Operation temperature under switching condition	$T_{jop}$		-40...+( $T_{jmax} - 25$ )	°C

### Isolation Properties

Isolation voltage	$V_{isol}$	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Isolation voltage	$V_{isol}$	AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			>12,7	mm
Clearance			9	mm
Comparative Tracking Index	CTI		≥ 200	

\*100 % tested in production



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10-PZ12NMA080SH23-M260F03Y  
datasheet

### Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		$V_{GE}$ [V] $V_{GS}$ [V]	$V_{CE}$ [V] $V_{DS}$ [V] $V_F$ [V]	$I_C$ [A] $I_D$ [A] $I_F$ [A]	$T_j$ [°C]	Min	Typ	Max		

#### Buck Switch

##### Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,003	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		80	25 125 150	1,78	1,99 2,33 2,41	2,42 <sup>(1)</sup>	V
Collector-emitter cut-off current	$I_{CES}$		0	1200		25			10	μA
Gate-emitter leakage current	$I_{GES}$		20	0		25			240	nA
Internal gate resistance	$r_g$							None		Ω
Input capacitance	$C_{ies}$							4660		pF
Output capacitance	$C_{oes}$	$f = 1$ Mhz	0	25		25		300		pF
Reverse transfer capacitance	$C_{res}$							260		pF
Gate charge	$Q_g$	$V_{CC} = 960$ V	15		80	25		370		nC

##### Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,51		K/W
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##### Dynamic

Turn-on delay time	$t_{d(on)}$					25 125		77,4 78,6		ns
Rise time	$t_r$	$R_{gon} = 4$ Ω $R_{goff} = 4$ Ω				25 125		11,4 13,8		ns
Turn-off delay time	$t_{d(off)}$		±15	350	50	25 125		179,8 242		ns
Fall time	$t_f$					25 125		47,76 75,59		ns
Turn-on energy (per pulse)	$E_{on}$	$Q_{tFWD} = 2,06$ μC $Q_{tFWD} = 3,8$ μC				25 125		0,524 0,98		mWs
Turn-off energy (per pulse)	$E_{off}$					25 125		1,31 2,28		mWs





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### Characteristic Values

Parameter	Symbol	Conditions						Values			Unit
		$V_{GE}$ [V] $V_{GS}$ [V]	$V_{CE}$ [V] $V_{DS}$ [V] $V_F$ [V]	$I_C$ [A] $I_D$ [A] $I_F$ [A]	$T_j$ [°C]	Min	Typ	Max			

#### Buck Diode

##### Static

Forward voltage	$V_F$				75	25 125 150		1,53 1,49 1,46	1,92 <sup>(1)</sup>	V
Reverse leakage current	$I_R$	$V_r = 650$ V				25			3,8	μA

##### Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,34		K/W
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##### Dynamic

Peak recovery current	$I_{RRM}$	$di/dt=5245$ A/μs $di/dt=3680$ A/μs	±15	350	50	25		63,32		A
Reverse recovery time	$t_{rr}$					125		72,81		ns
						25		52,08		
Recovered charge	$Q_r$					125		2,06		μC
						25		3,8		
Reverse recovered energy	$E_{rec}$	125		0,473		mWs				
		25		0,845						
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$	25		1198		A/μs				
		125		852,09						



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### Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		$V_{GE}$ [V] $V_{GS}$ [V]	$V_{CE}$ [V] $V_{DS}$ [V] $V_F$ [V]	$I_C$ [A] $I_D$ [A] $I_F$ [A]	$T_j$ [°C]	Min	Typ	Max		

#### Boost Switch

##### Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0012	25	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		75	25 125 150	1,05	1,45 1,59 1,64	1,85 <sup>(1)</sup>	V
Collector-emitter cut-off current	$I_{CES}$		0	600		25			3,8	μA
Gate-emitter leakage current	$I_{GES}$		20	0		25			600	nA
Internal gate resistance	$r_g$							None		Ω
Input capacitance	$C_{ies}$							4620		pF
Output capacitance	$C_{oes}$	$f = 1$ Mhz	0	25		25		288		pF
Reverse transfer capacitance	$C_{res}$							137		pF
Gate charge	$Q_g$	$V_{CC} = 480$ V	15		75	25		470		nC

##### Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,94		K/W
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##### Dynamic

Turn-on delay time	$t_{d(on)}$					25 125		83,6 84,8		ns
Rise time	$t_r$	$R_{gon} = 4$ Ω $R_{goff} = 4$ Ω				25 125		11 12,2		ns
Turn-off delay time	$t_{d(off)}$		±15	350	56	25 125		177 204,6		ns
Fall time	$t_f$					25 125		86,45 105,19		ns
Turn-on energy (per pulse)	$E_{on}$	$Q_{tFWD} = 5,32$ μC $Q_{tFWD} = 8,22$ μC				25 125		0,528 0,747		mWs
Turn-off energy (per pulse)	$E_{off}$					25 125		1,86 2,5		mWs



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### Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		$V_{GS}$ [V]	$V_{GE}$ [V]	$V_{DS}$ [V]	$V_{CE}$ [V]	$T_j$ [°C]	Min	Typ	Max	

#### Boost Diode

##### Static

Forward voltage	$V_F$				50	25 125 150	1,35	1,73 1,7 1,68	2,05 <sup>(1)</sup>	V
Reverse leakage current	$I_R$	$V_T = 1200$ V				25			10	μA

##### Thermal

Thermal resistance junction to sink <sup>(2)</sup>	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,06		K/W
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##### Dynamic

Peak recovery current	$I_{RRM}$	$di/dt=6090$ A/μs $di/dt=5325$ A/μs	±15	350	56	25		106,49		A
						125		117,83		
Reverse recovery time	$t_{rr}$					25		102,06		ns
						125		148,06		
Recovered charge	$Q_r$					25		5,32		μC
						125		8,22		
Reverse recovered energy	$E_{rec}$	25		1,55		mWs				
		125		2,42						
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$	25		6904		A/μs				
		125		4951						



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### Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		$V_{GS}$ [V]	$V_{GE}$ [V]	$V_{DS}$ [V]	$V_{CE}$ [V]	$T_j$ [°C]	Min	Typ	Max	

### Thermistor

#### Static

Rated resistance	$R$					25		22		kΩ
Deviation of $R_{100}$	$A_{R/R}$	$R_{100} = 1486 \Omega$				100	-12		14	%
Power dissipation	$P$							200		mW
Power dissipation constant	$d$					25		2		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 3 \%$						3950		K
B-value	$B_{(25/100)}$	Tol. $\pm 3 \%$						3998		K
Vincotech Thermistor Reference									B	

<sup>(1)</sup> Value at chip level

<sup>(2)</sup> Only valid with pre-applied Vincotech thermal interface material.

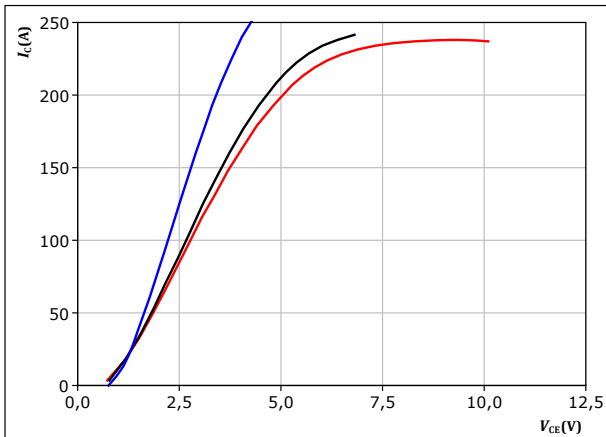


## Buck Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$



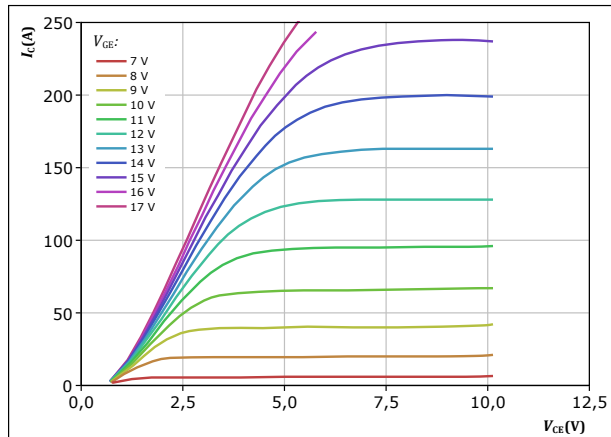
$t_p = 250 \mu s$   
 $V_{GE} = 15 V$

$T_j:$  — 25 °C  
— 125 °C  
— 150 °C

figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

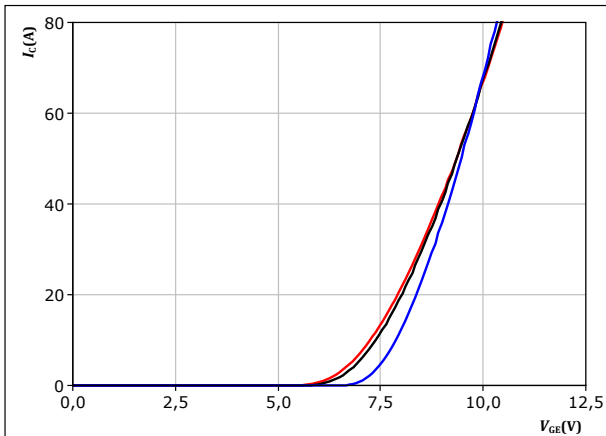


$t_p = 250 \mu s$   
 $T_j = 150 \text{ °C}$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$



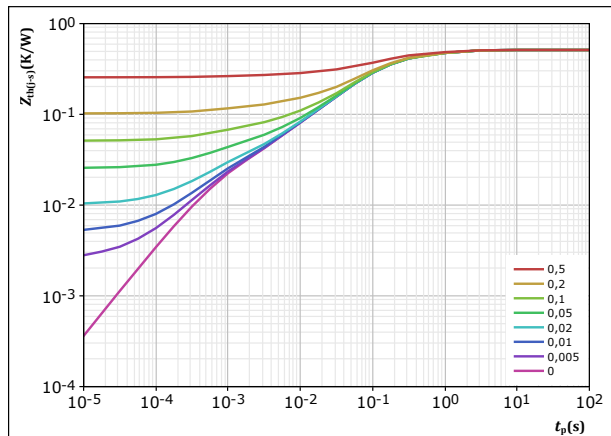
$t_p = 250 \mu s$   
 $V_{CE} = 10 V$

$T_j:$  — 25 °C  
— 125 °C  
— 150 °C

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$   
 $R_{th(j-s)} = 0,512 \text{ K/W}$

IGBT thermal model values

$R$ (K/W)	$\tau$ (s)
9,50E-02	1,05E+00
1,84E-01	1,66E-01
1,81E-01	6,37E-02
3,37E-02	7,18E-03
1,79E-02	6,47E-04



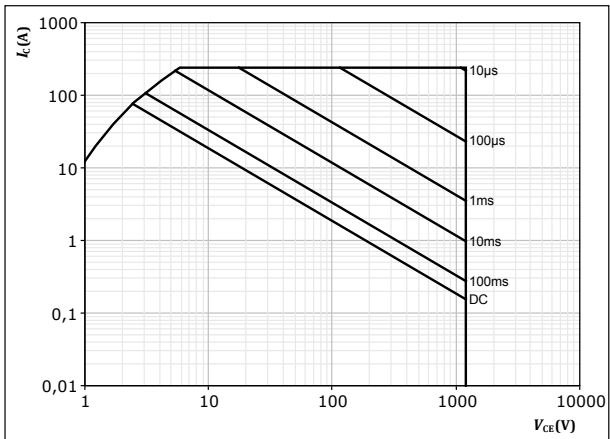
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## Buck Switch Characteristics

figure 5. IGBT

Safe operating area

$$I_C = f(V_{CE})$$



$D =$  single pulse

$T_s = 80$  °C

$V_{GE} = 15$  V

$T_j = T_{jmax}$



### Buck Diode Characteristics

figure 6. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

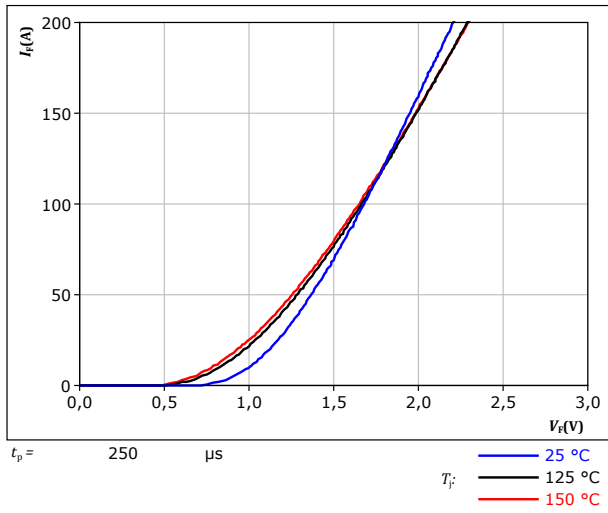
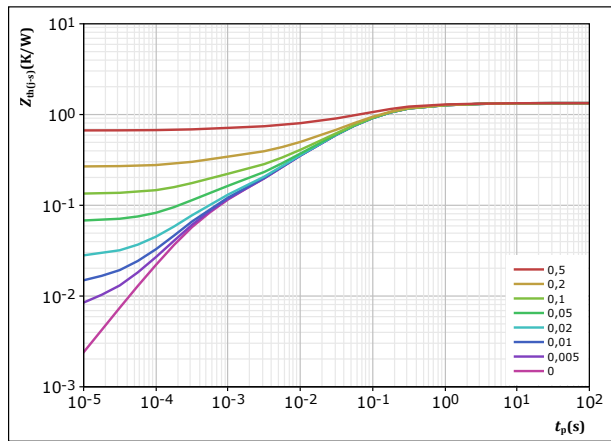


figure 7. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$   
 $R_{th(j-s)} = 1,336 \text{ K/W}$   
 FWD thermal model values

R (K/W)	$\tau$ (s)
5,84E-02	3,64E+00
1,57E-01	5,25E-01
5,86E-01	1,06E-01
3,27E-01	2,57E-02
1,27E-01	4,84E-03
8,12E-02	4,11E-04

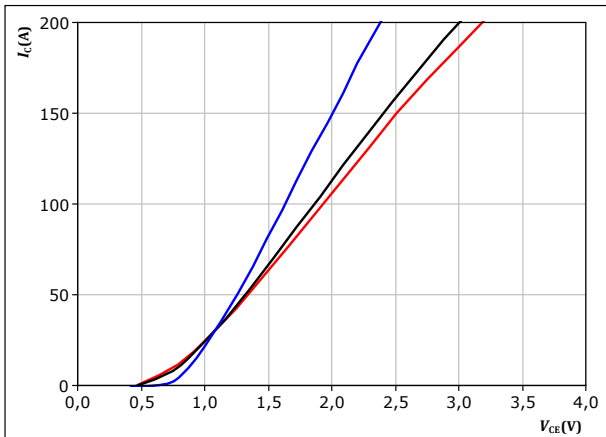


### Boost Switch Characteristics

**figure 8.** IGBT

Typical output characteristics

$I_C = f(V_{CE})$

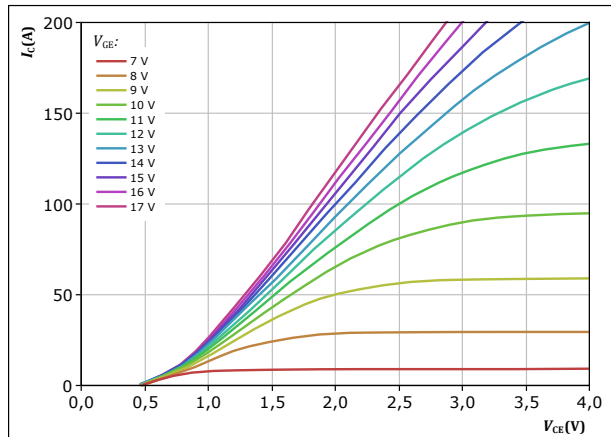


$t_p = 250 \mu s$   
 $V_{GE} = 15 V$   
 $T_j:$  25 °C, 125 °C, 150 °C

**figure 9.** IGBT

Typical output characteristics

$I_C = f(V_{CE})$

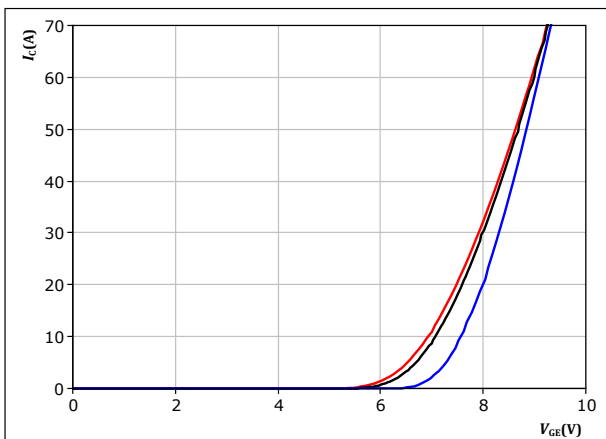


$t_p = 250 \mu s$   
 $T_j = 150 \text{ °C}$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**figure 10.** IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$

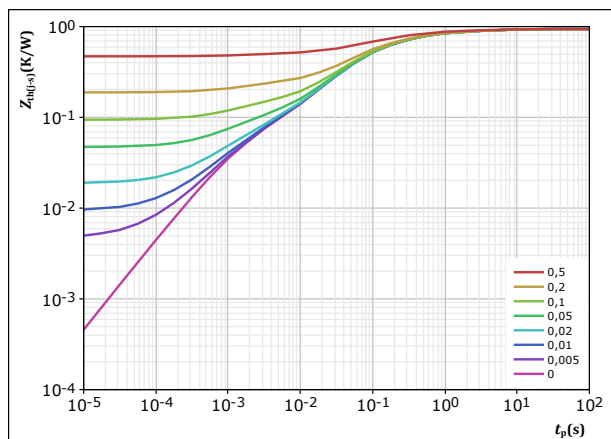


$t_p = 250 \mu s$   
 $V_{CE} = 10 V$   
 $T_j:$  25 °C, 125 °C, 150 °C

**figure 11.** IGBT

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



$D = t_p / T$   
 $R_{th(j-s)} = 0,94 \text{ K/W}$   
IGBT thermal model values  

R (K/W)	$\tau$ (s)
8,42E-02	4,56E+00
2,89E-01	4,19E-01
4,35E-01	7,20E-02
8,50E-02	2,19E-02
4,67E-02	1,33E-03



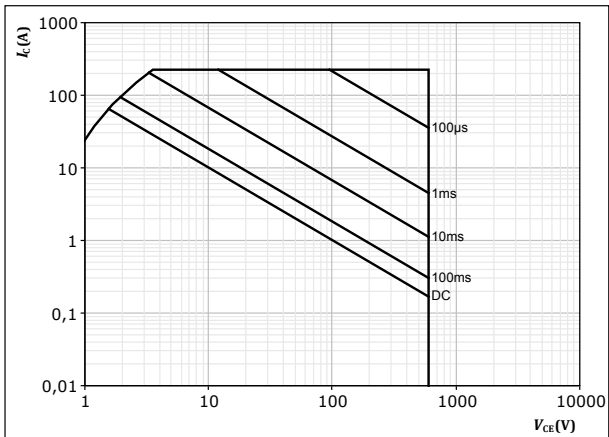


### Boost Switch Characteristics

figure 12. IGBT

Safe operating area

$I_C = f(V_{CE})$



$D =$  single pulse  
 $T_s = 80 \text{ } ^\circ\text{C}$   
 $V_{GE} = 15 \text{ V}$   
 $T_j = T_{jmax}$



### Boost Diode Characteristics

figure 13. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

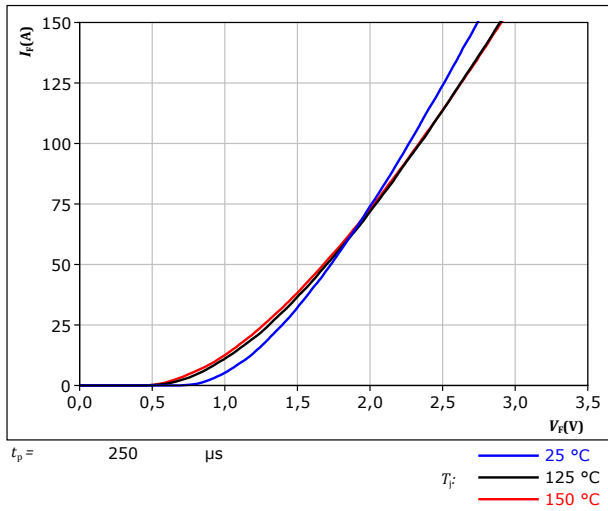
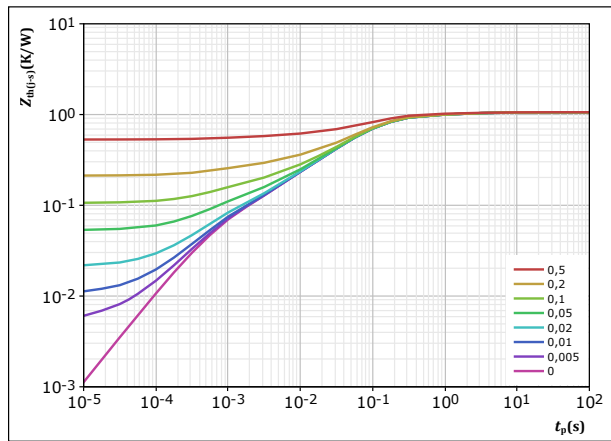


figure 14. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$   
 $R_{th(j-s)} = 1,061 \text{ K/W}$   
 FWD thermal model values

$R$ (K/W)	$\tau$ (s)
4,19E-02	4,68E+00
8,50E-02	8,80E-01
4,99E-01	1,21E-01
2,83E-01	4,12E-02
9,28E-02	6,53E-03
5,92E-02	6,76E-04

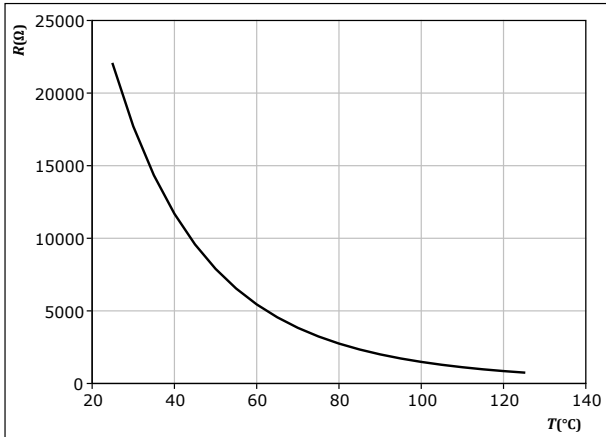


### Thermistor Characteristics

figure 15. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$

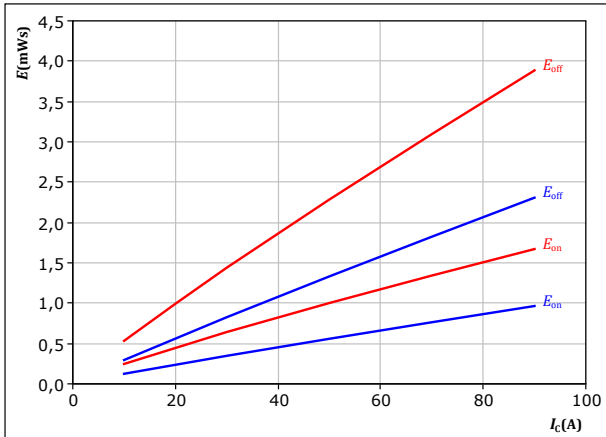




## Buck Switching Characteristics

**figure 16.** IGBT

Typical switching energy losses as a function of collector current  
 $E = f(I_c)$



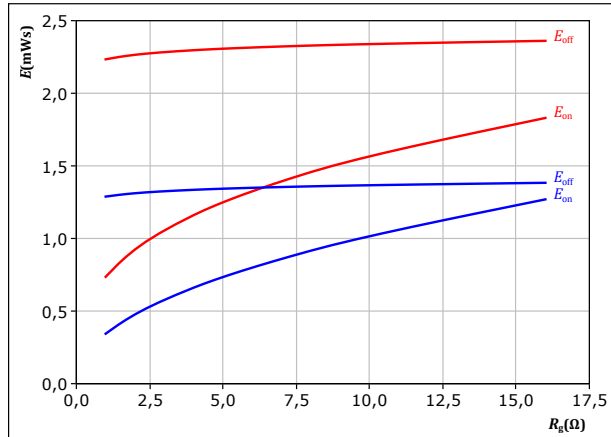
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{g(on)} = 4$  Ω  
 $R_{g(off)} = 4$  Ω

$T_j$ : — 25 °C  
 — 125 °C

**figure 17.** IGBT

Typical switching energy losses as a function of gate resistor  
 $E = f(R_g)$



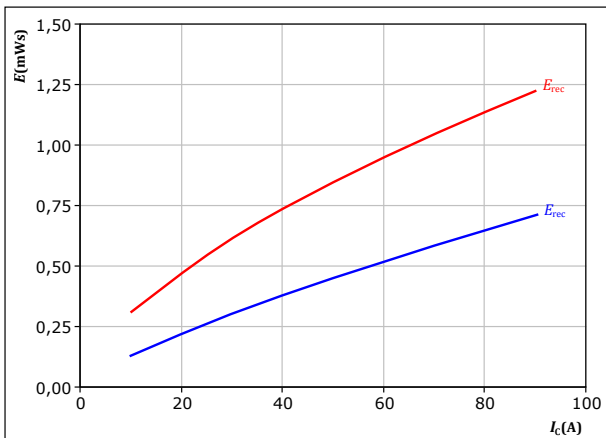
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 50$  A

$T_j$ : — 25 °C  
 — 125 °C

**figure 18.** FWD

Typical reverse recovered energy loss as a function of collector current  
 $E_{rec} = f(I_c)$



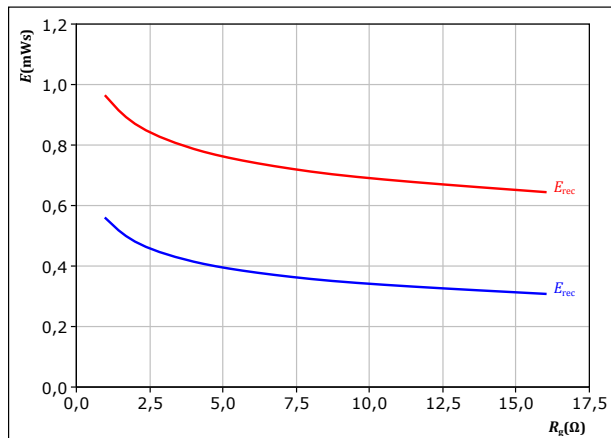
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{g(on)} = 4$  Ω

$T_j$ : — 25 °C  
 — 125 °C

**figure 19.** FWD

Typical reverse recovered energy loss as a function of gate resistor  
 $E_{rec} = f(R_g)$



With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 50$  A

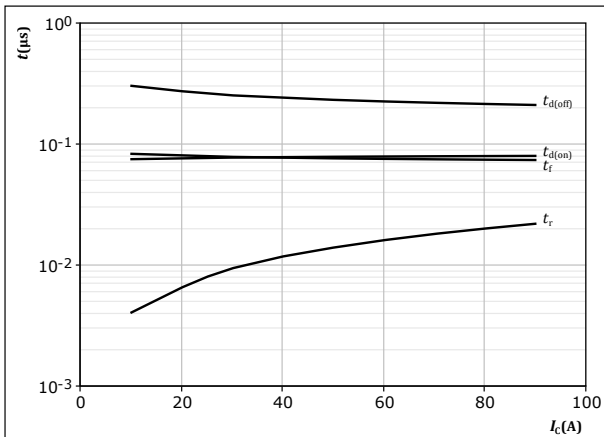
$T_j$ : — 25 °C  
 — 125 °C



## Buck Switching Characteristics

**figure 20.** IGBT

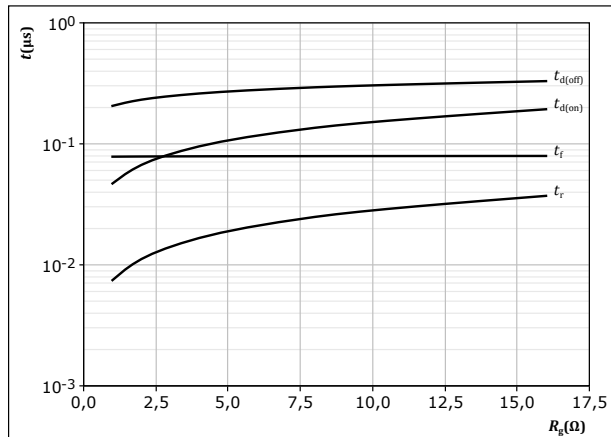
Typical switching times as a function of collector current  
 $t = f(I_c)$



With an inductive load at  
 $T_j = 125 \text{ }^\circ\text{C}$   
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{g(on)} = 4 \text{ } \Omega$   
 $R_{g(off)} = 4 \text{ } \Omega$

**figure 21.** IGBT

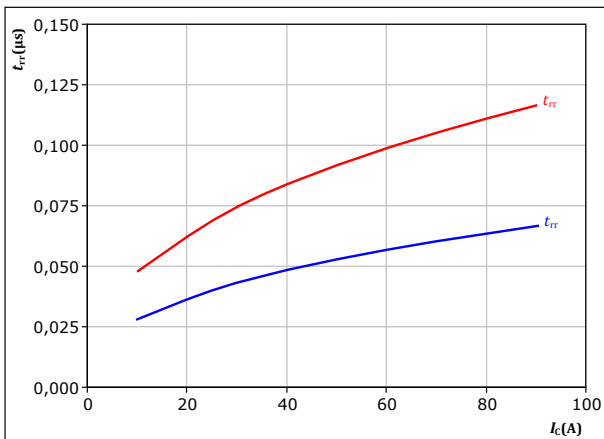
Typical switching times as a function of gate resistor  
 $t = f(R_g)$



With an inductive load at  
 $T_j = 125 \text{ }^\circ\text{C}$   
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_c = 50 \text{ A}$

**figure 22.** FWD

Typical reverse recovery time as a function of collector current  
 $t_{rr} = f(I_c)$

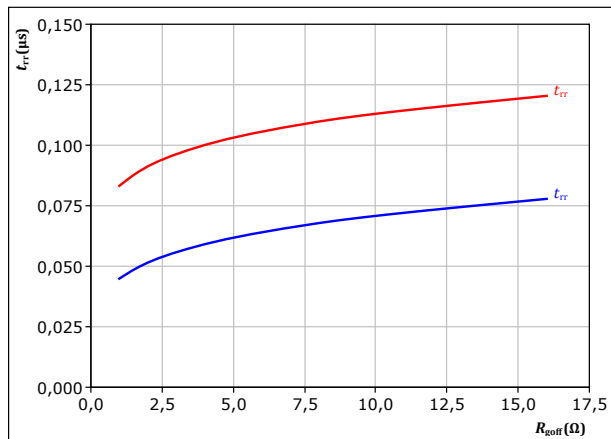


With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{g(on)} = 4 \text{ } \Omega$

$T_j$ : — 25  $^\circ\text{C}$   
 — 125  $^\circ\text{C}$

**figure 23.** FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor  
 $t_{rr} = f(R_{g(off)})$



With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_c = 50 \text{ A}$

$T_j$ : — 25  $^\circ\text{C}$   
 — 125  $^\circ\text{C}$

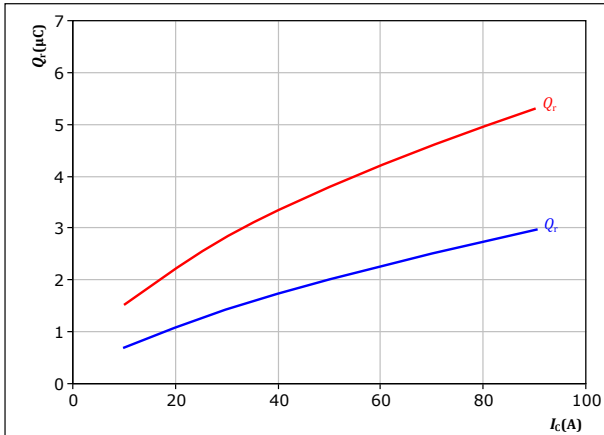


## Buck Switching Characteristics

**figure 24.** FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

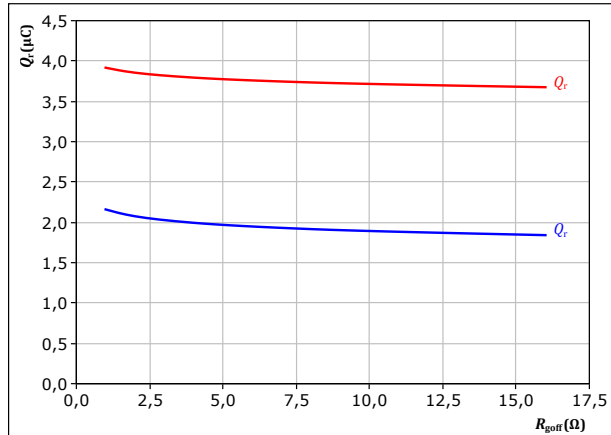
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{goff} = 4$  Ω

$T_j$ : — 25 °C  
— 125 °C

**figure 25.** FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

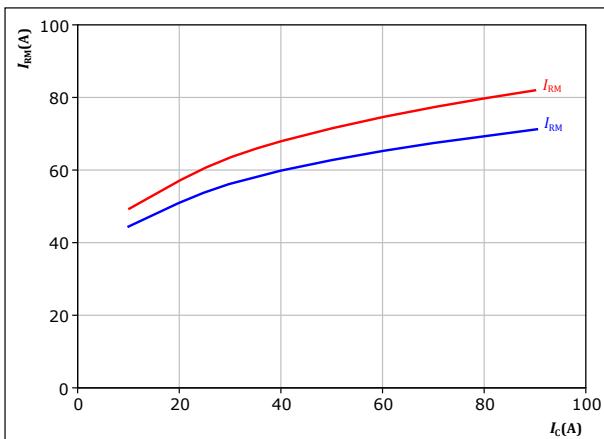
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 50$  A

$T_j$ : — 25 °C  
— 125 °C

**figure 26.** FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

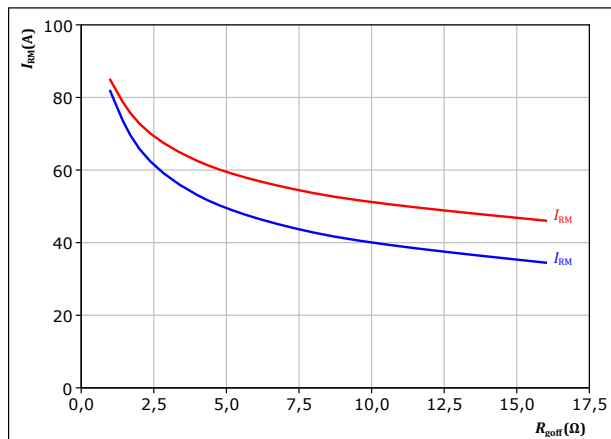
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{goff} = 4$  Ω

$T_j$ : — 25 °C  
— 125 °C

**figure 27.** FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 50$  A

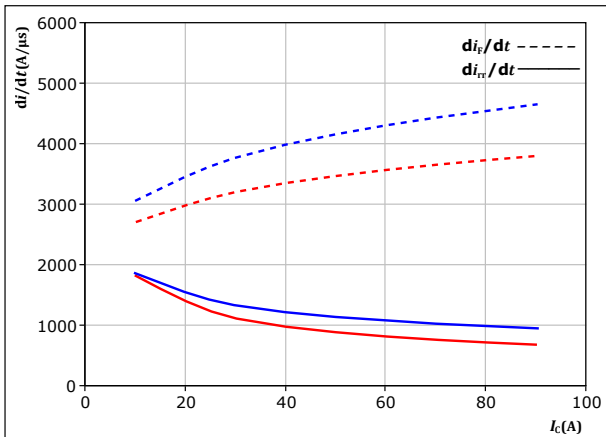
$T_j$ : — 25 °C  
— 125 °C



### Buck Switching Characteristics

**figure 28.** FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current  
 $di_f/dt, di_r/dt = f(I_C)$



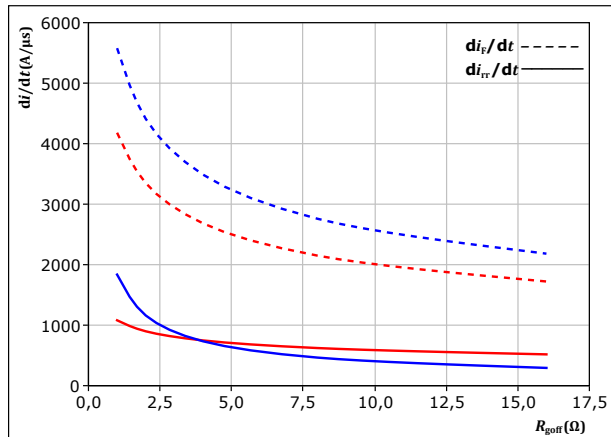
With an inductive load at

$V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{goff} = 4 \text{ } \Omega$

$T_j$ : — 25 °C  
 — 125 °C

**figure 29.** FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor  
 $di_f/dt, di_r/dt = f(R_{goff})$



With an inductive load at

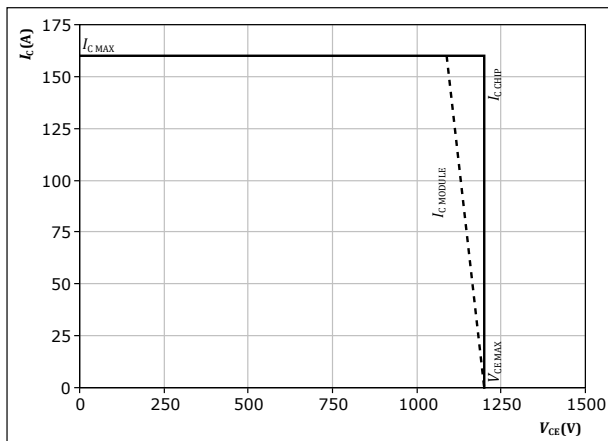
$V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 50 \text{ A}$

$T_j$ : — 25 °C  
 — 125 °C

**figure 30.** IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



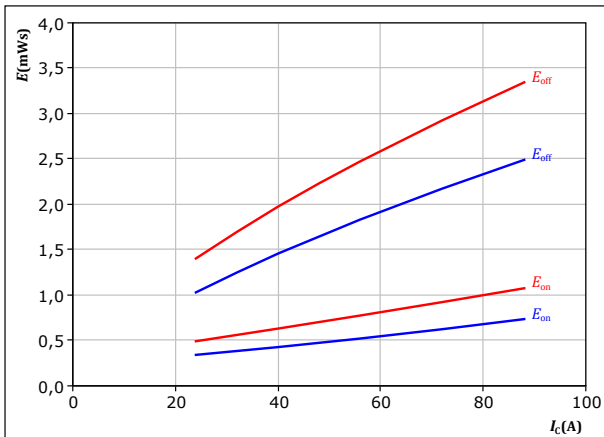
At  $T_j = 125 \text{ } ^\circ\text{C}$   
 $R_{goff} = 4 \text{ } \Omega$   
 $R_{goff} = 4 \text{ } \Omega$



## Boost Switching Characteristics

**figure 31.** IGBT

Typical switching energy losses as a function of collector current  
 $E = f(I_c)$



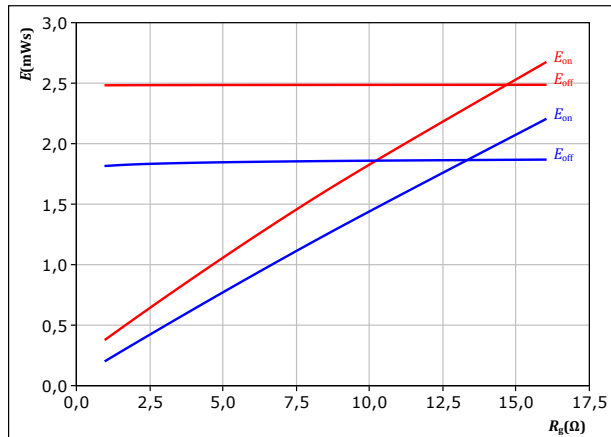
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{gon} = 4$   $\Omega$   
 $R_{goff} = 4$   $\Omega$

$T_j$ : — 25 °C  
— 125 °C

**figure 32.** IGBT

Typical switching energy losses as a function of gate resistor  
 $E = f(R_g)$



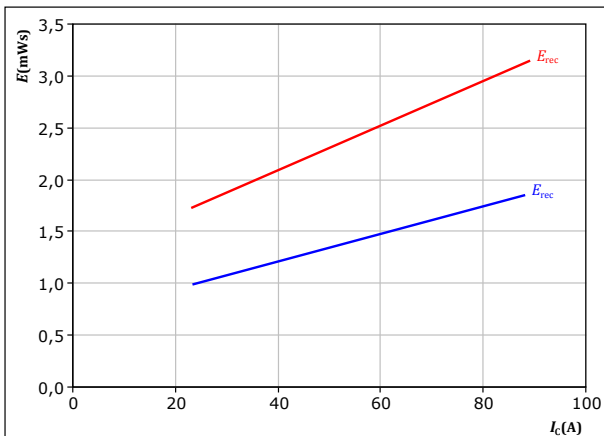
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 56$  A

$T_j$ : — 25 °C  
— 125 °C

**figure 33.** FWD

Typical reverse recovered energy loss as a function of collector current  
 $E_{rec} = f(I_c)$



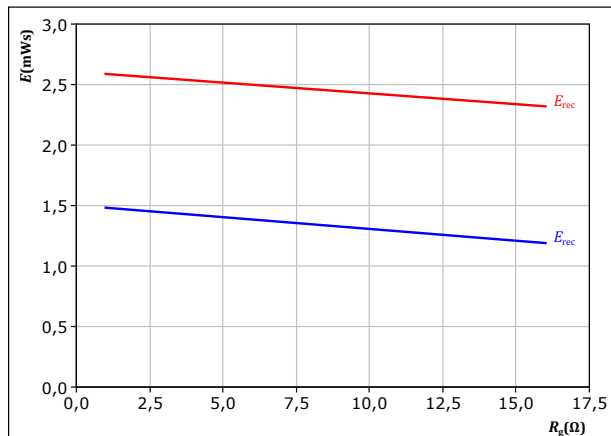
With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{gon} = 4$   $\Omega$

$T_j$ : — 25 °C  
— 125 °C

**figure 34.** FWD

Typical reverse recovered energy loss as a function of gate resistor  
 $E_{rec} = f(R_g)$



With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 56$  A

$T_j$ : — 25 °C  
— 125 °C

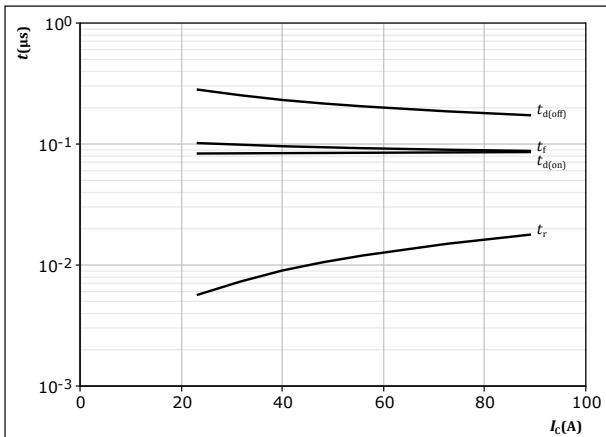




## Boost Switching Characteristics

**figure 35.** IGBT

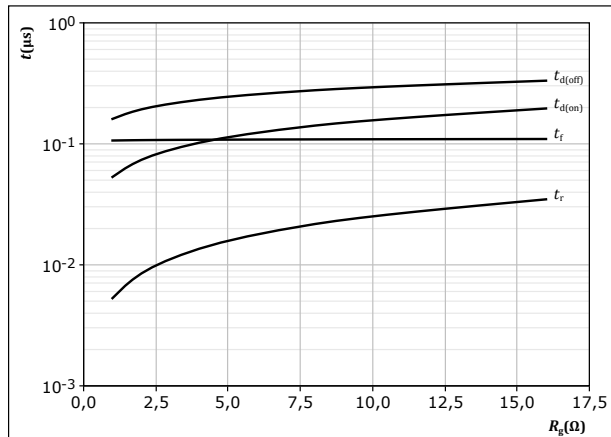
Typical switching times as a function of collector current  
 $t = f(I_c)$



With an inductive load at  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{g(on)} = 4 \text{ } \Omega$   
 $R_{g(off)} = 4 \text{ } \Omega$

**figure 36.** IGBT

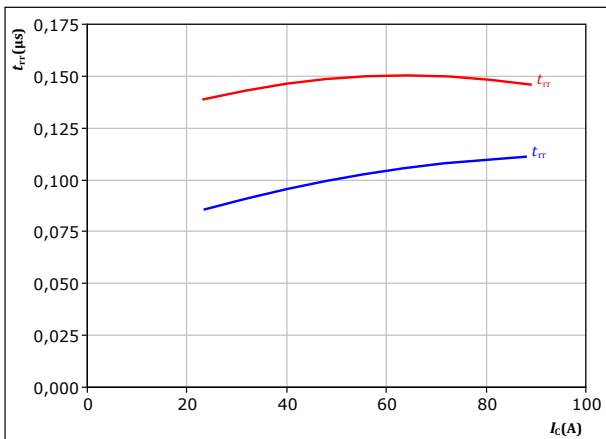
Typical switching times as a function of gate resistor  
 $t = f(R_g)$



With an inductive load at  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_c = 56 \text{ A}$

**figure 37.** FWD

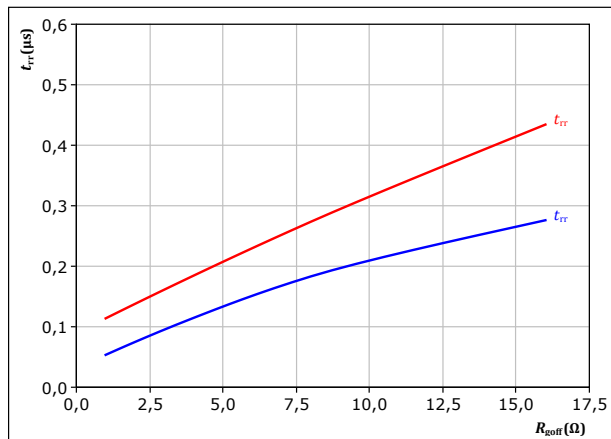
Typical reverse recovery time as a function of collector current  
 $t_{rr} = f(I_c)$



With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{g(on)} = 4 \text{ } \Omega$   
 $T_j:$  — 25 °C  
           — 125 °C

**figure 38.** FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor  
 $t_{rr} = f(R_{g(off)})$



With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_c = 56 \text{ A}$   
 $T_j:$  — 25 °C  
           — 125 °C

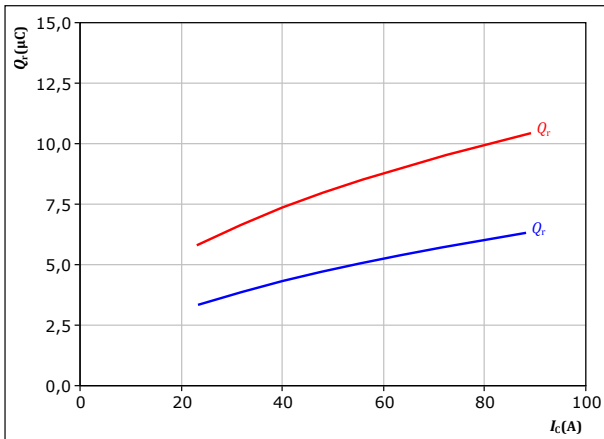


## Boost Switching Characteristics

**figure 39.** FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

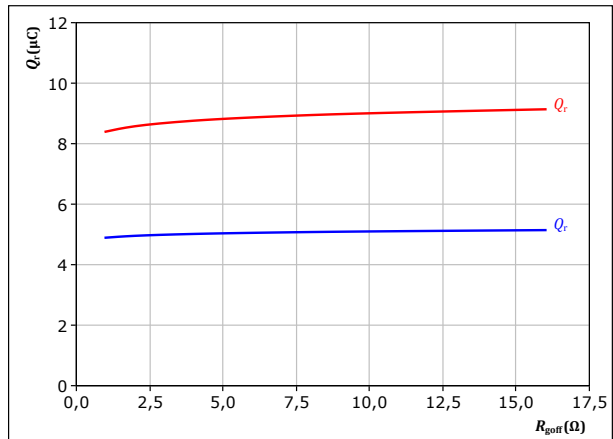
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{goff} = 4$  Ω

$T_j$ : — 25 °C  
— 125 °C

**figure 40.** FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

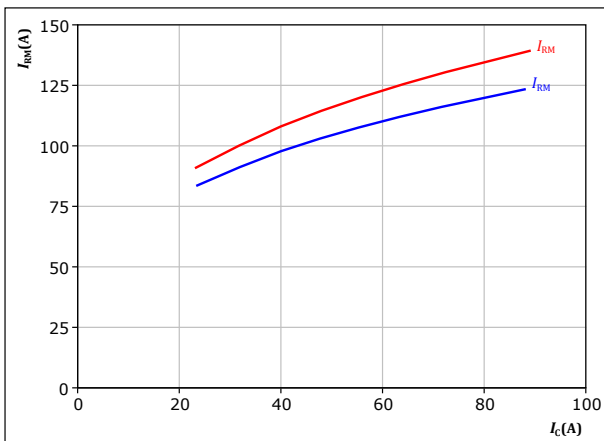
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 56$  A

$T_j$ : — 25 °C  
— 125 °C

**figure 41.** FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

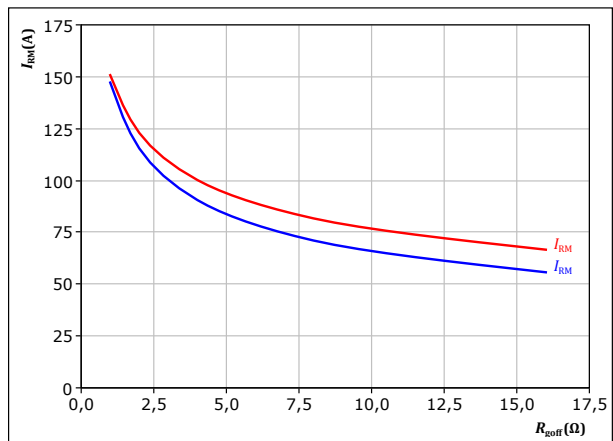
$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $R_{goff} = 4$  Ω

$T_j$ : — 25 °C  
— 125 °C

**figure 42.** FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$  V  
 $V_{GE} = \pm 15$  V  
 $I_c = 56$  A

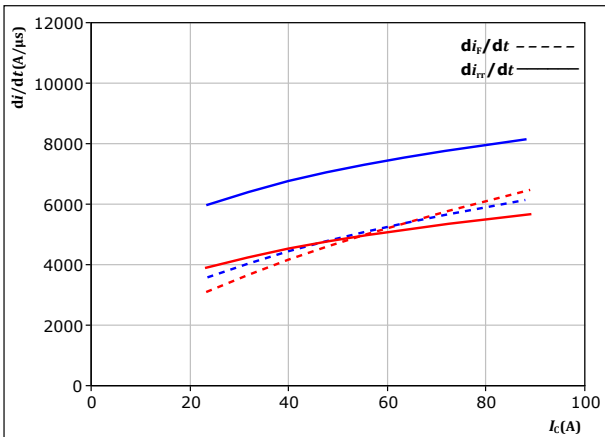
$T_j$ : — 25 °C  
— 125 °C



### Boost Switching Characteristics

**figure 43.** FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current  
 $di_i/dt, di_r/dt = f(I_c)$

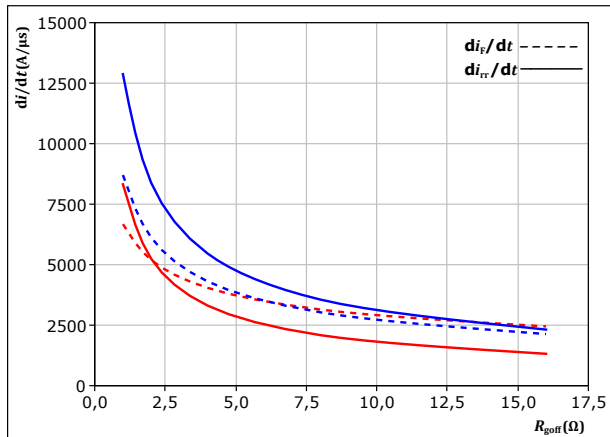


With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{goff} = 4 \text{ } \Omega$

$T_j$ : — 25 °C  
 — 125 °C

**figure 44.** FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor  
 $di_i/dt, di_r/dt = f(R_{goff})$

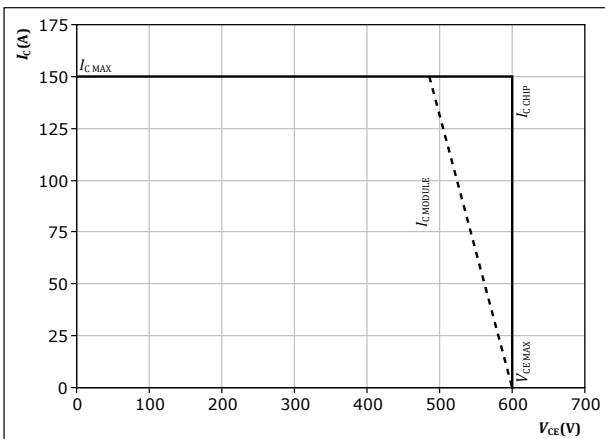


With an inductive load at  
 $V_{CE} = 350 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_c = 56 \text{ A}$

$T_j$ : — 25 °C  
 — 125 °C

**figure 45.** IGBT

Reverse bias safe operating area  
 $I_c = f(V_{CE})$



At  $T_j = 125 \text{ } ^\circ\text{C}$   
 $R_{goff} = 4 \text{ } \Omega$   
 $R_{goff} = 4 \text{ } \Omega$



## Switching Definitions

figure 46. IGBT

Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$  ( $t_{Eoff}$  = integrating time for  $E_{off}$ )

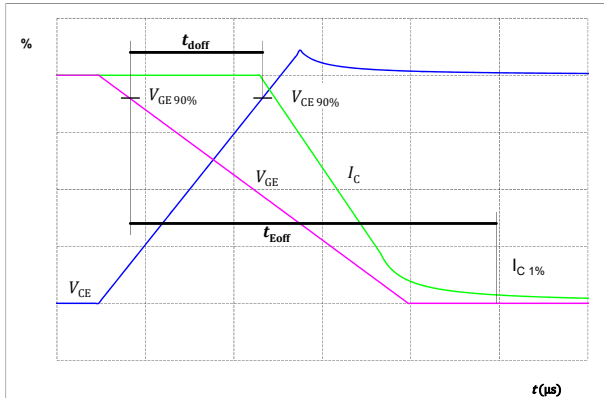


figure 47. IGBT

Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$  ( $t_{Eon}$  = integrating time for  $E_{on}$ )

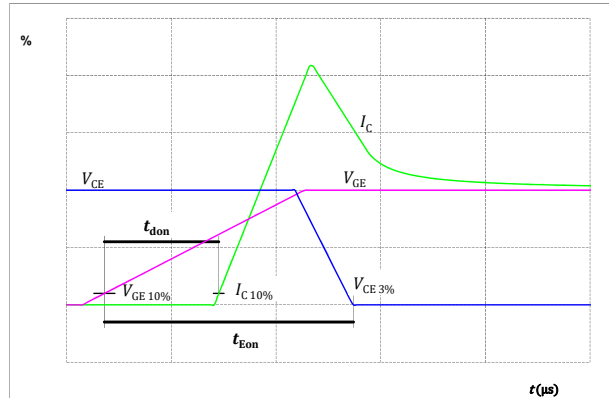


figure 48. IGBT

Turn-off Switching Waveforms & definition of  $t_f$

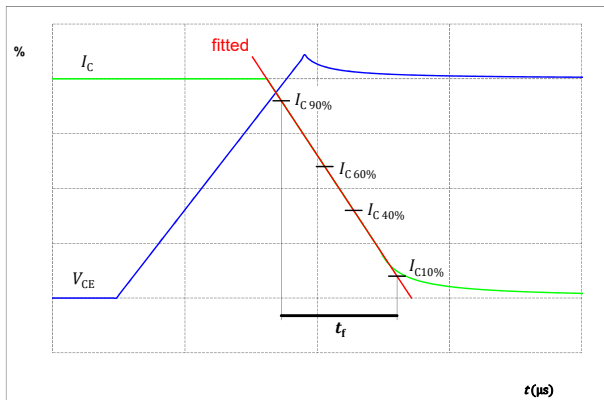
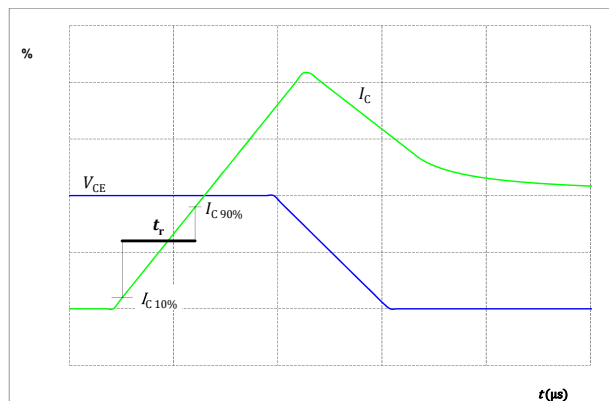


figure 49. IGBT

Turn-on Switching Waveforms & definition of  $t_r$





### Switching Definitions

figure 50. FWD

Turn-off Switching Waveforms & definition of  $t_{rr}$

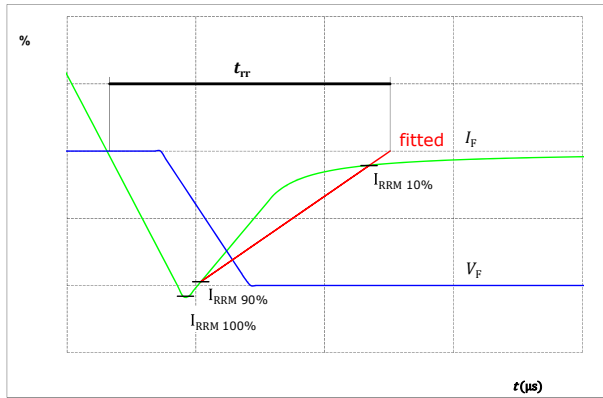
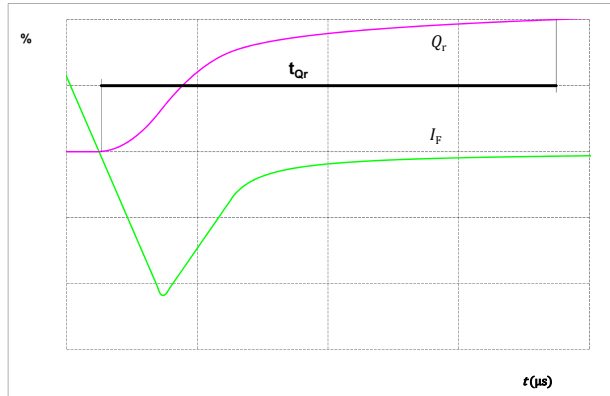


figure 51. FWD

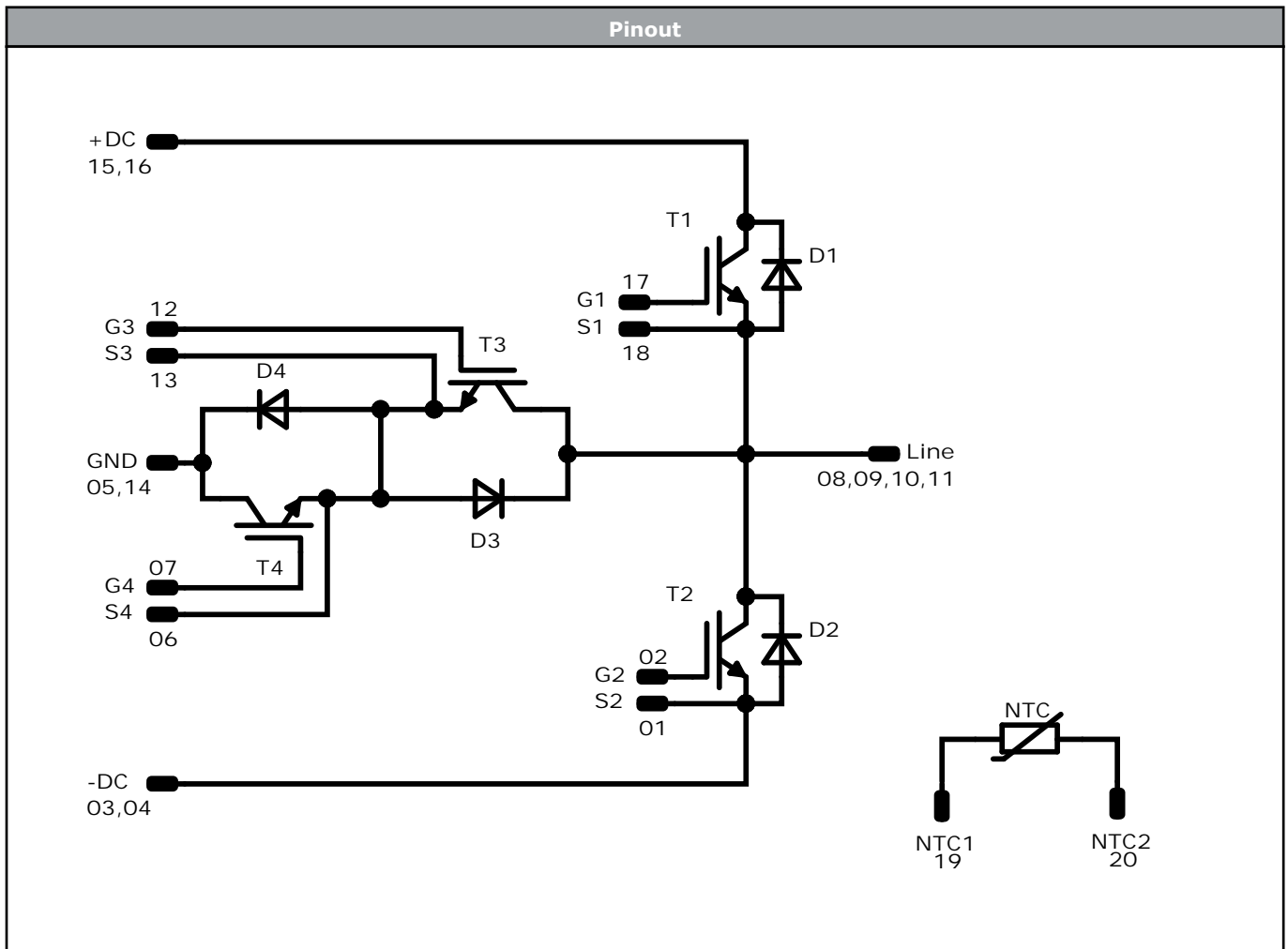
Turn-on Switching Waveforms & definition of  $t_{Qr}$  ( $t_{Qr}$  = integrating time for  $Q_r$ )







Vincotech



Identification					
ID	Component	Voltage	Current	Function	Comment
T1, T2	IGBT	1200 V	80 A	Buck Switch	
D3, D4	FWD	650 V	75 A	Buck Diode	
T3, T4	IGBT	600 V	75 A	Boost Switch	
D1, D2	FWD	1200 V	50 A	Boost Diode	
NTC	Thermistor			Thermistor	




Vincotech

Packaging instruction				
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow 0</i> packages see vincotech.com website.

Package data
Package data for <i>flow 0</i> packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website. 

Document No.:	Date:	Modification:	Pages
10-PZ12NMA080SH23-M260F03Y-D2-14	11 Sep. 2021	New Datasheet format, module is unchanged Introduce Rth values with PSX-P7 TIM	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.