



Vincotech

10-PZ12NMA080SH-M269FY

datasheet

flowMNPC 0

1200 V / 80 A

Features

- mixed voltage component topology
- neutral point clamped inverter
- reactive power capability
- low inductance layout

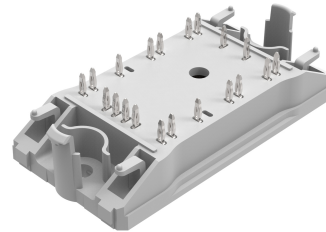
Target applications

- solar inverter
- UPS

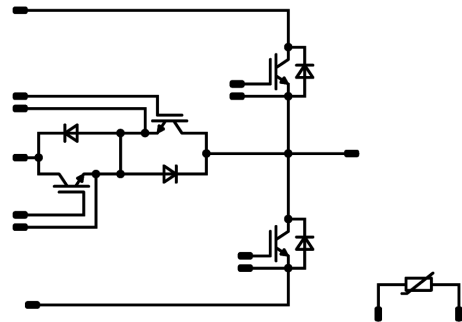
Types

- 10-PZ12NMA080SH-M269FY

flow 0 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Buck Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	74	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	240	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	186	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	10	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Buck Diode

Peak repetitive reverse voltage	V_{RRM}		600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	38	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 8,3\text{ ms}$ $T_j = 25\text{ °C}$	300	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	65	W
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Boost Switch

Collector-emitter voltage	V_{CES}		600	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	49	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	150	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	90	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 360\text{ V}$ $T_j = 150\text{ °C}$	6	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$



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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Boost Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	37	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	69	W
Maximum junction temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Isolation voltage	V_{isol}	AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			min. 12,7	mm
Clearance			9	mm
Comparative Tracking Index	CTI		> 200	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Buck Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,003	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	V_{CEsat}		15		80	25 125	1,78	2,1 2,44	2,42 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			10	µA
Gate-emitter leakage current	I_{GES}		20	0		25			240	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	$f = 1 \text{ Mhz}$	0	25	25			4660		pF
Output capacitance	C_{oes}							300		pF
Reverse transfer capacitance	C_{res}							260		pF
Gate charge	Q_g	$V_{CC} = 960 \text{ V}$	15		80	25		370		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						0,51		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 8 \Omega$ $R_{goff} = 8 \Omega$	± 15	350	40	25 125 150		125 126 126,2		ns
Rise time	t_r					25 125 150		19,6 22,6 23,8		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		219,4 281,6 298		ns
Fall time	t_f					25 125 150		42,64 72,54 77,19		ns
Turn-on energy (per pulse)	E_{on}	$Q_{rFWD}=0,304 \mu\text{C}$ $Q_{rFWD}=0,952 \mu\text{C}$ $Q_{rFWD}=1,24 \mu\text{C}$				25 125 150		0,468 0,7 0,806		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,981 1,65 1,83		mWs



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Characteristic Values

Parameter	Symbol	Conditions						Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]		Min	Typ	Max	

Buck Diode

Static

Forward voltage	V_F				30	25 125	1,88	2,45 1,86	2,78 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 600$ V				25 125			10 500	μA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,46		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt=2715$ A/μs $di/dt=2632$ A/μs $di/dt=2675$ A/μs	±15	350	40	25 125 150		30,8 43,36 47,82		A
Reverse recovery time	t_{rr}					25 125 150		17,69 38,25 45,83		ns
Recovered charge	Q_r					25 125 150		0,304 0,952 1,24		μC
Reverse recovered energy	E_{rec}					25 125 150		0,024 0,12 0,16		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		7783 4120 3812		A/μs



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Characteristic Values

Parameter	Symbol	Conditions						Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]		Min	Typ	Max	

Boost Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0008	25		5	5,8	6,5	V
Collector-emitter saturation voltage	V_{CEsat}		15		50	25 125		1,05	1,54 1,75	1,85 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	600		25				2,6	µA
Gate-emitter leakage current	I_{GES}		20	0		25				600	nA
Internal gate resistance	r_g								None		Ω
Input capacitance	C_{ies}	$f = 1 \text{ Mhz}$	0	25		25			3140		pF
Output capacitance	C_{oes}								200		pF
Reverse transfer capacitance	C_{res}								93		pF

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)							1,06		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 8 \Omega$ $R_{goff} = 8 \Omega$	± 15	350	40	25 125 150		99,4 102 102,6		ns
Rise time	t_r					25 125 150		10,4 13 14,4		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		183 205,6 212,4		ns
Fall time	t_f					25 125 150		79,77 98,82 101,99		ns
Turn-on energy (per pulse)	E_{on}					25 125 150		0,492 0,719 0,782		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		1,16 1,5 1,58		mWs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Boost Diode

Static

Forward voltage	V_F				30	25 125		2,23 1,91	3,3 ⁽¹⁾ 3,1 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1200$ V				25			100	µA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,37		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt=4028$ A/µs $di/dt=3209$ A/µs $di/dt=2957$ A/µs	± 15	350	40	25 125 150		64,49 78,61 81,36		A
Reverse recovery time	t_{rr}					25 125 150		28,74 171,48 172,26		ns
Recovered charge	Q_r					25 125 150		2,75 6,14 7,09		µC
Reverse recovered energy	E_{rec}					25 125 150		0,744 1,79 2,08		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		8246 4626 4314		A/µs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Thermistor

Static

Rated resistance	R					25		22		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	$R_{100} = 1486 \Omega$				100	-12		14	%
Power dissipation	P							200		mW
Power dissipation constant	d					25		2		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 3 \%$						3950		K
B-value	$B_{(25/100)}$	Tol. $\pm 3 \%$						3998		K
Vincotech Thermistor Reference									B	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



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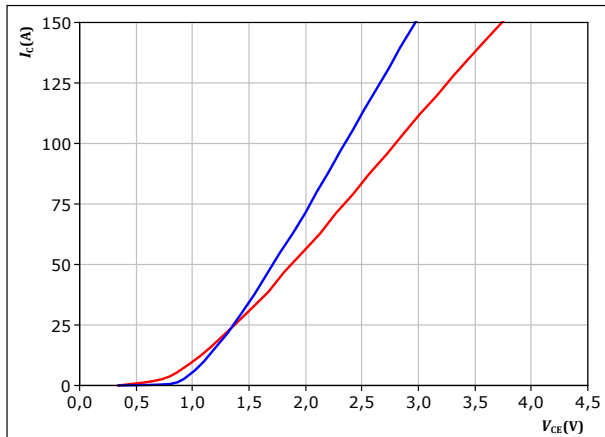
datasheet

Buck Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

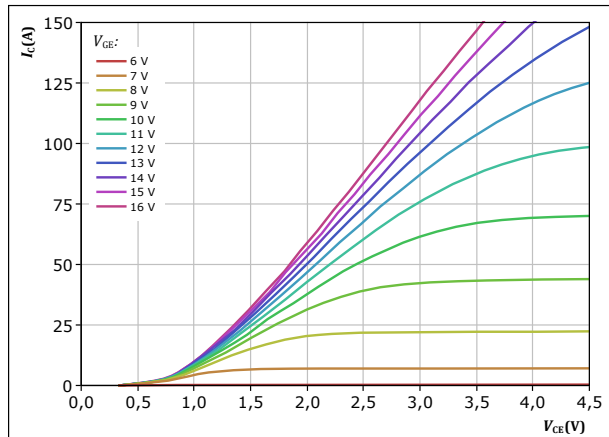


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_J: 25 ^\circ C$
 $125 ^\circ C$

figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

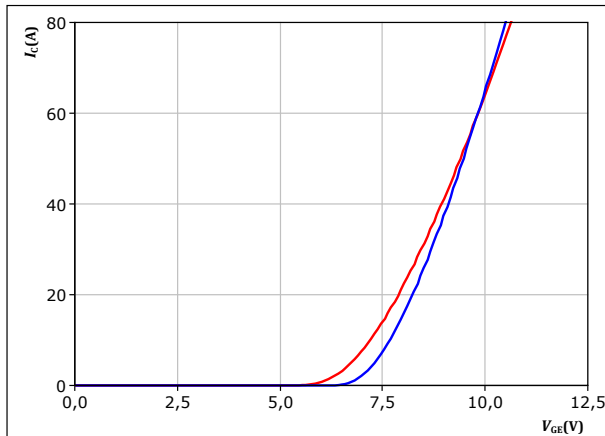


$t_p = 250 \mu s$
 $T_J = 125 ^\circ C$
 V_{GE} from 6 V to 16 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

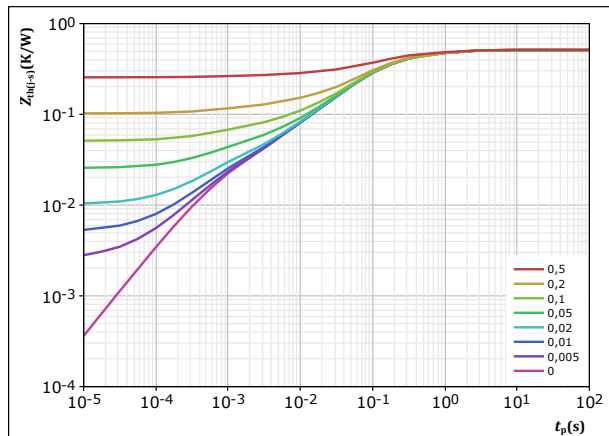


$t_p = 250 \mu s$
 $V_{CE} = 10 V$
 $T_J: 25 ^\circ C$
 $125 ^\circ C$

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 0,512 K/W$
IGBT thermal model values

$R (K/W)$	$\tau (s)$
9,50E-02	1,05E+00
1,84E-01	1,66E-01
1,81E-01	6,37E-02
3,37E-02	7,18E-03
1,79E-02	6,47E-04



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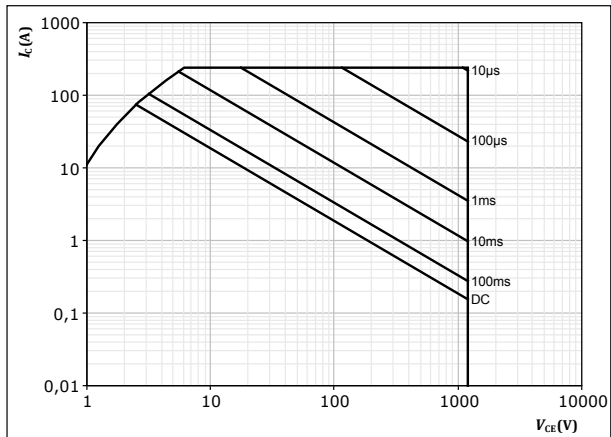
Buck Switch Characteristics

figure 5.

IGBT

Safe operating area

$$I_C = f(V_{CE})$$



D = single pulse

$T_s = 80$ °C
 $V_{GE} = 15$ V
 $T_j = T_{jmax}$



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Buck Diode Characteristics

figure 6. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

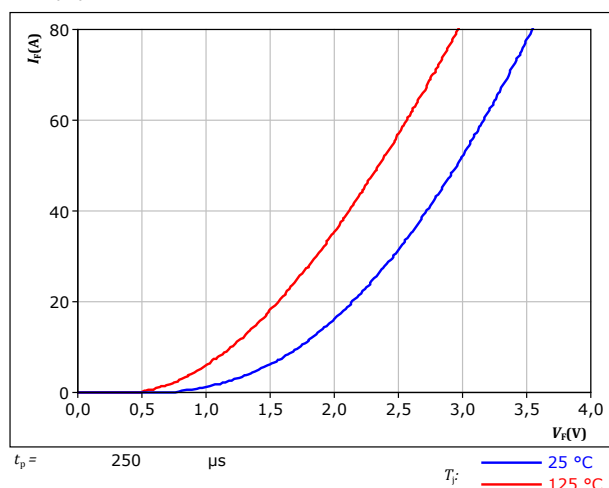
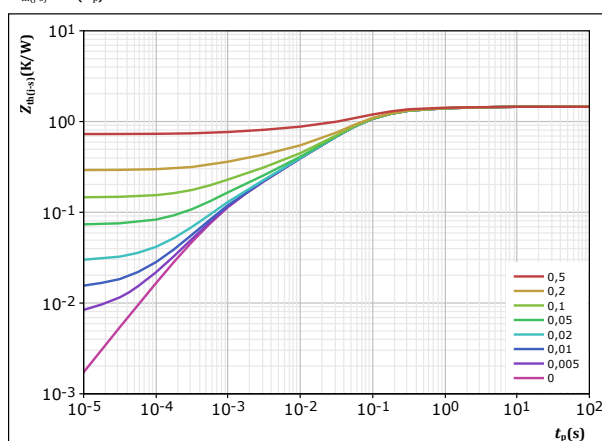


figure 7. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	
$R_{th(j-s)} =$	1,456	K/W
FWD thermal model values		
R (K/W)	τ (s)	
6,84E-02	2,71E+00	
1,85E-01	3,24E-01	
7,77E-01	6,88E-02	
2,30E-01	1,94E-02	
1,15E-01	3,46E-03	
8,19E-02	7,02E-04	



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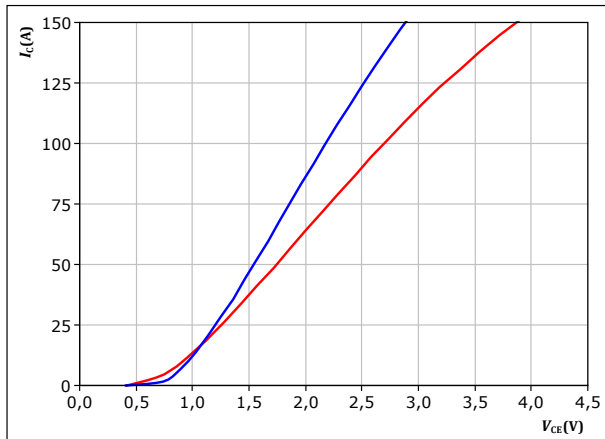
Boost Switch Characteristics

figure 8.

IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$



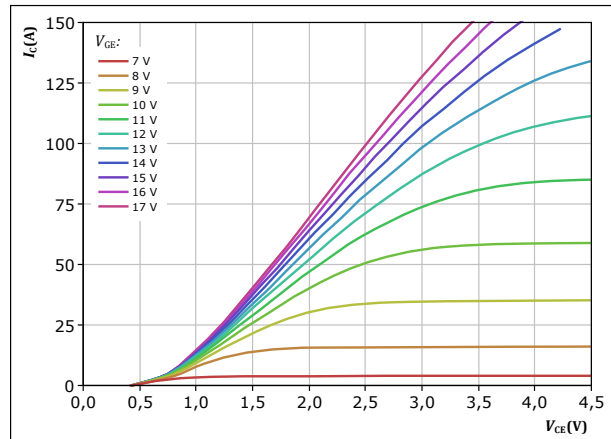
$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 T_j : — 25 °C
— 125 °C

figure 9.

IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$



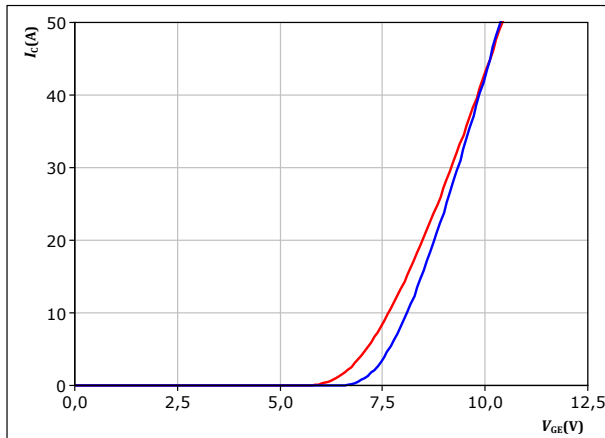
$t_p = 250 \mu s$
 $T_j = 125 ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 10.

IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$



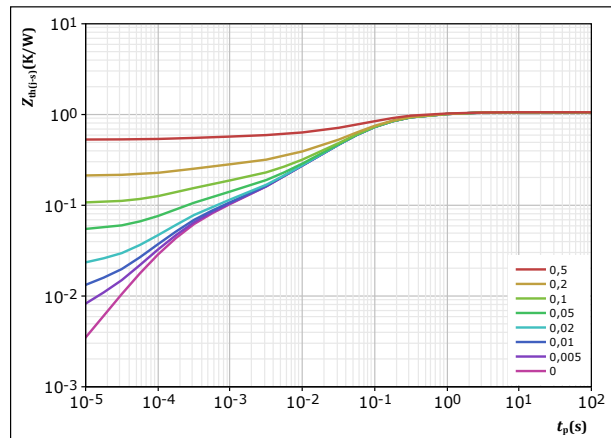
$t_p = 250 \mu s$
 $V_{CE} = 10 V$
 T_j : — 25 °C
— 125 °C

figure 11.

IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,059 K/W$
IGBT thermal model values

$R (K/W)$	$\tau (s)$
1,11E-01	1,12E+00
3,60E-01	1,48E-01
3,77E-01	4,74E-02
1,24E-01	7,68E-03
4,58E-02	6,49E-04
4,19E-02	1,61E-04



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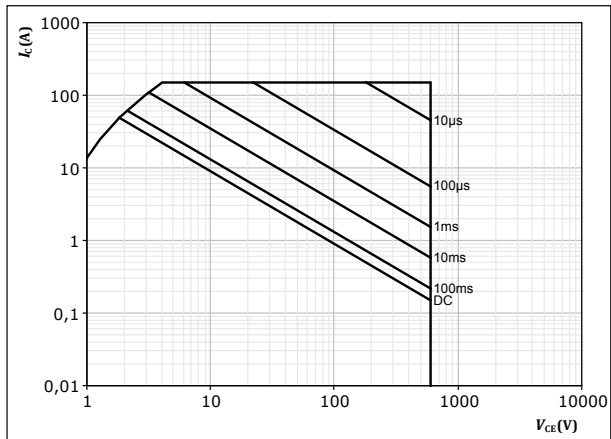
Boost Switch Characteristics

figure 12.

IGBT

Safe operating area

$$I_C = f(V_{CE})$$



D = single pulse

$T_s = 80$ °C
 $V_{GE} = 15$ V
 $T_j = T_{jmax}$



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Boost Diode Characteristics

figure 13. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

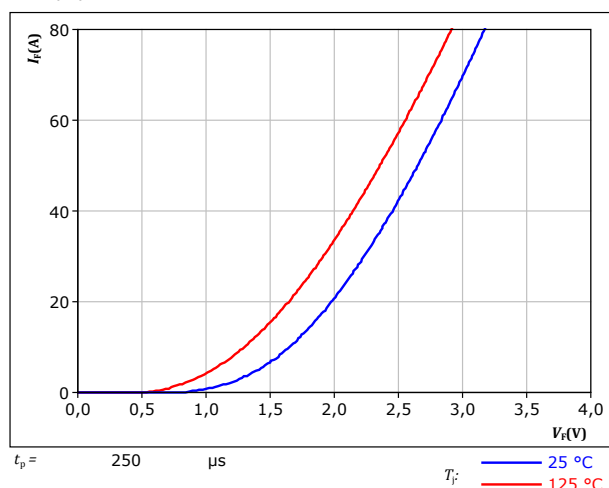
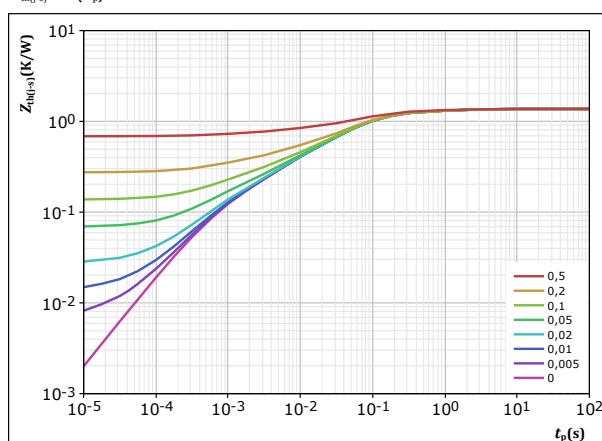


figure 14. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	
$R_{th(j-s)} =$	1,37	K/W
FWD thermal model values		
R (K/W)	τ (s)	
1,09E-01	1,57E+00	
4,26E-01	1,42E-01	
5,60E-01	3,94E-02	
1,89E-01	4,71E-03	
8,68E-02	6,04E-04	



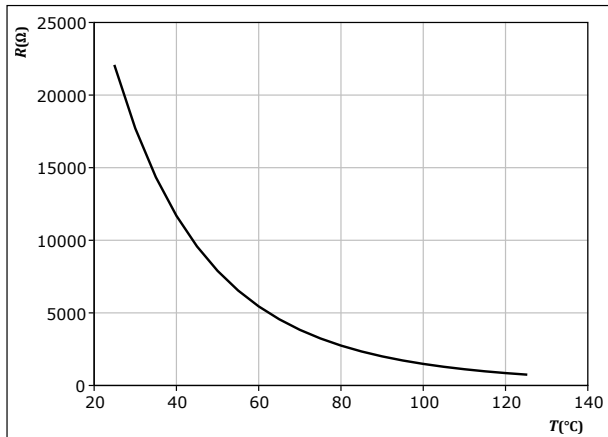
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Thermistor Characteristics

figure 15. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$





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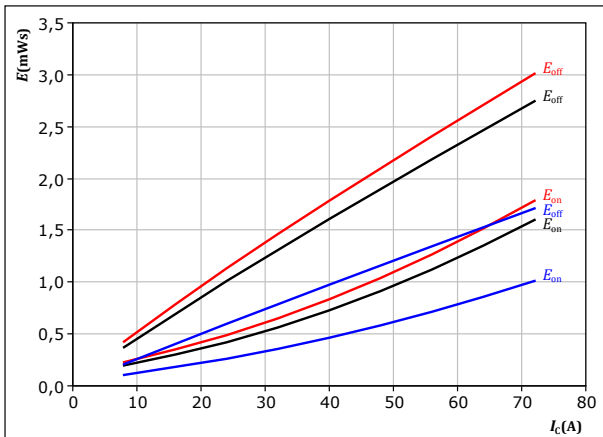
Buck Switching Characteristics

figure 16.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_C)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω

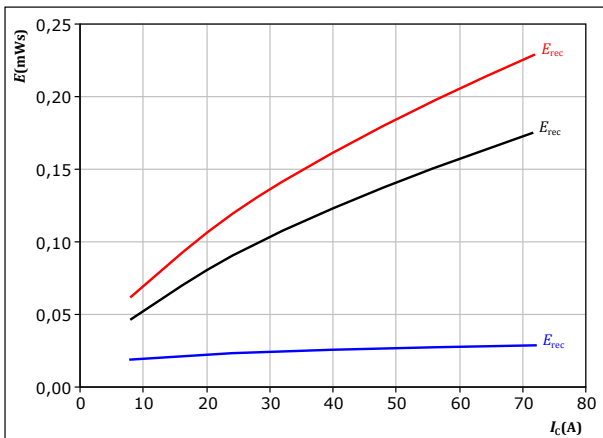
T_j : 25 °C
125 °C
150 °C

figure 18.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω

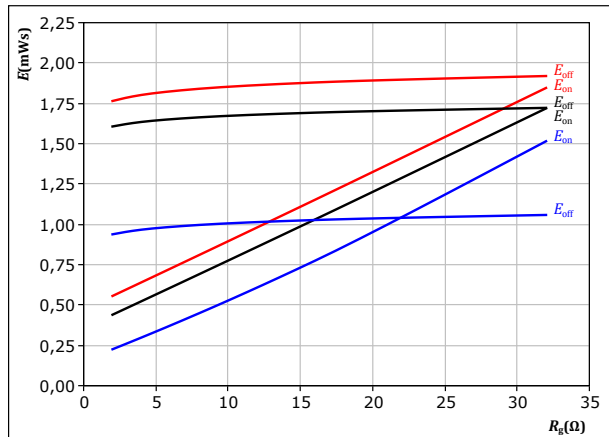
T_j : 25 °C
125 °C
150 °C

figure 17.

IGBT

Typical switching energy losses as a function of gate resistor

$$E = f(R_g)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

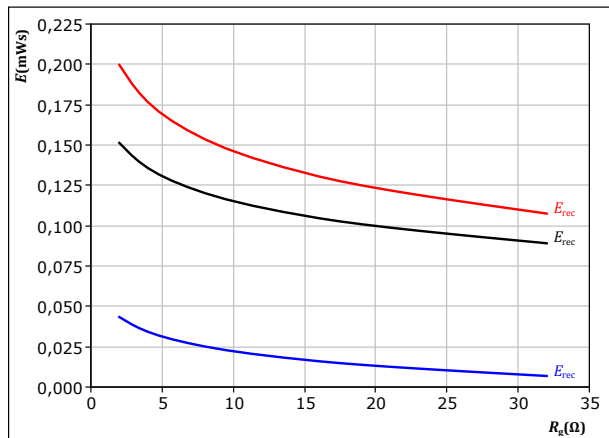
T_j : 25 °C
125 °C
150 °C

figure 19.

FWD

Typical reverse recovered energy loss as a function of gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

T_j : 25 °C
125 °C
150 °C



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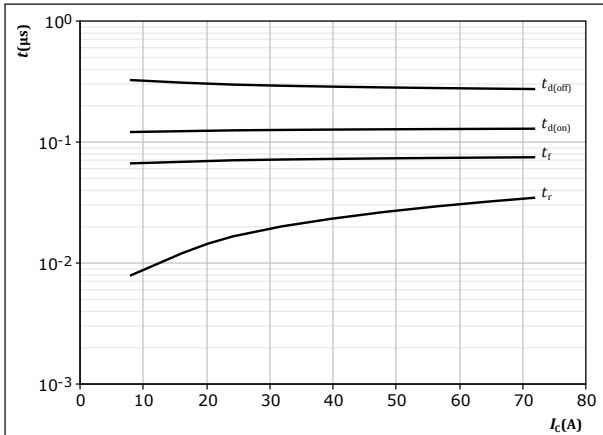
Buck Switching Characteristics

figure 20.

IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



With an inductive load at

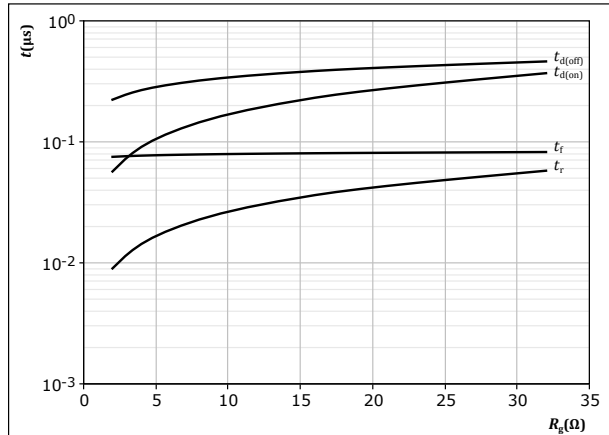
$T_j = 150$ °C
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω

figure 21.

IGBT

Typical switching times as a function of gate resistor

$$t = f(R_g)$$



With an inductive load at

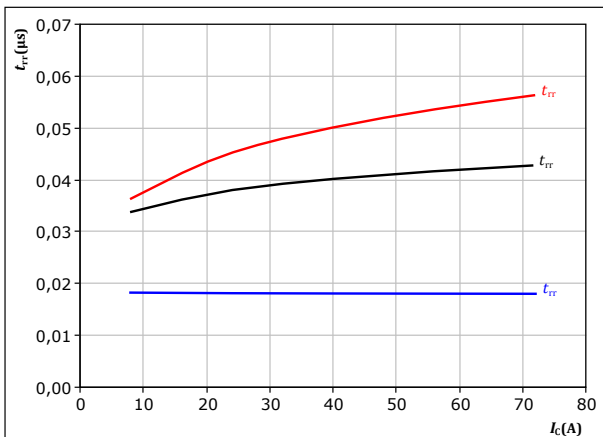
$T_j = 150$ °C
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

figure 22.

FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



With an inductive load at

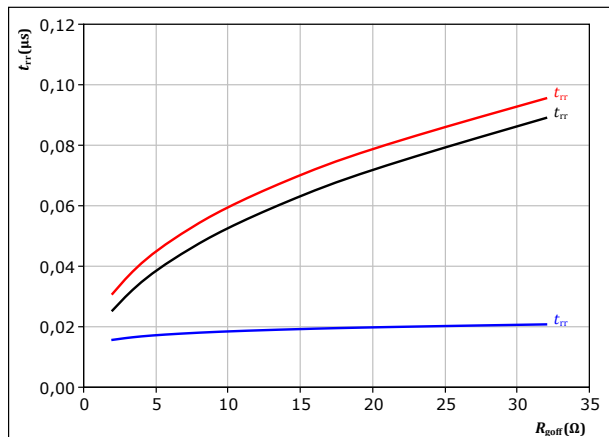
$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $T_j: 25$ °C (blue), 125 °C (black), 150 °C (red)

figure 23.

FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor

$$t_{rr} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A
 $T_j: 25$ °C (blue), 125 °C (black), 150 °C (red)



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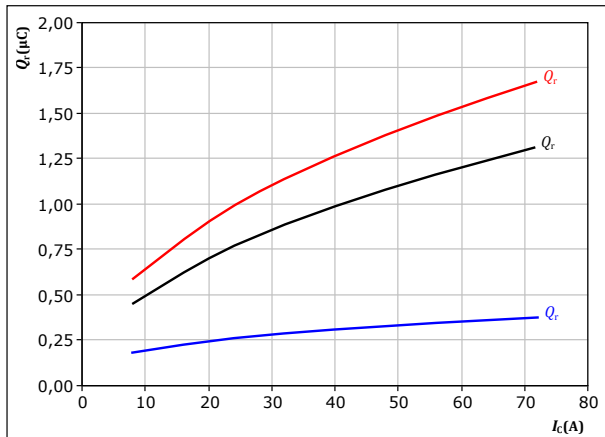
Buck Switching Characteristics

figure 24.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω

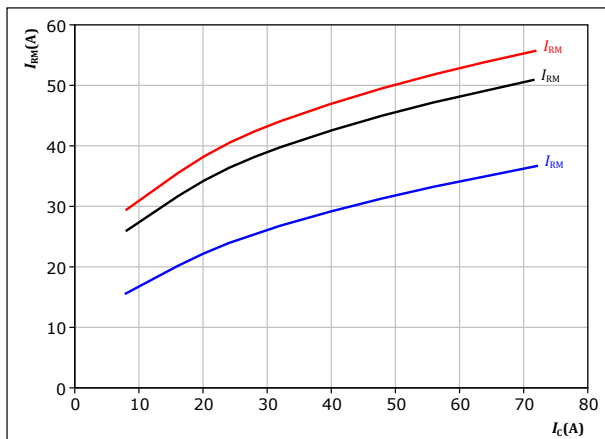
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 26.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω

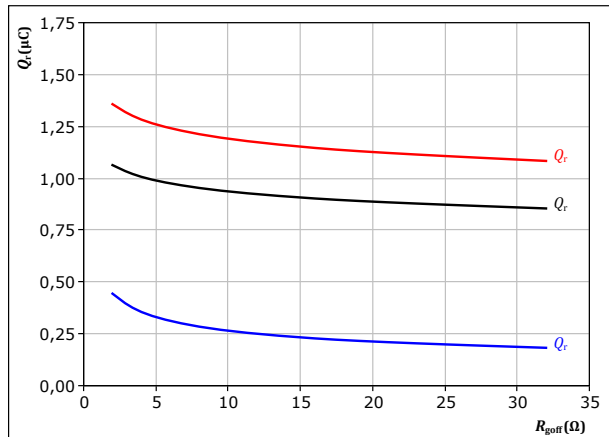
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 25.

FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 40$ A

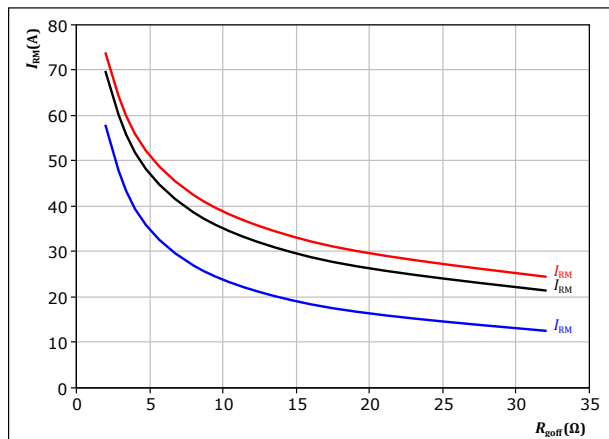
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 27.

FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 40$ A

T_j :
— 25 °C
— 125 °C
— 150 °C



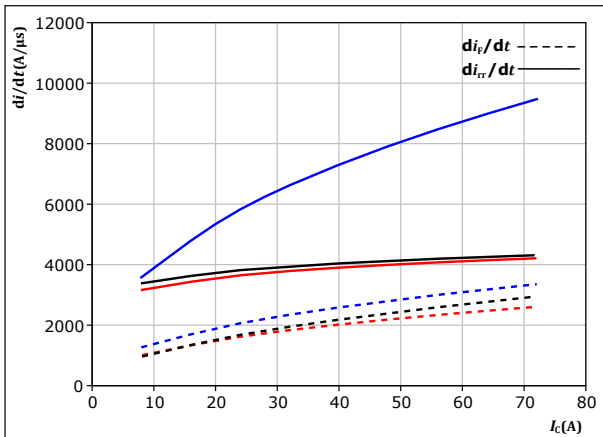
Vincotech

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datasheet

Buck Switching Characteristics

figure 28. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



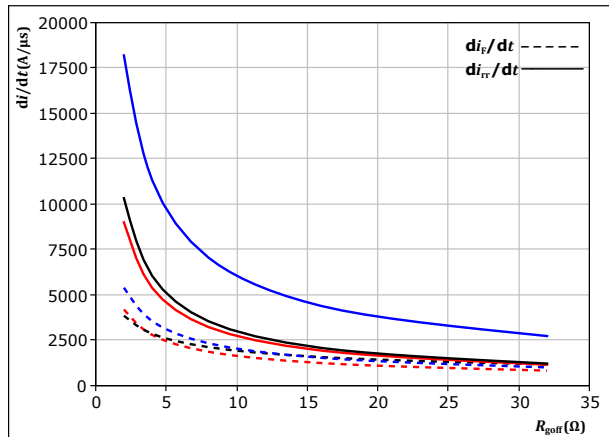
With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω

T_j : 25 °C
125 °C
150 °C

figure 29. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_r/dt = f(R_{goff})$



With an inductive load at

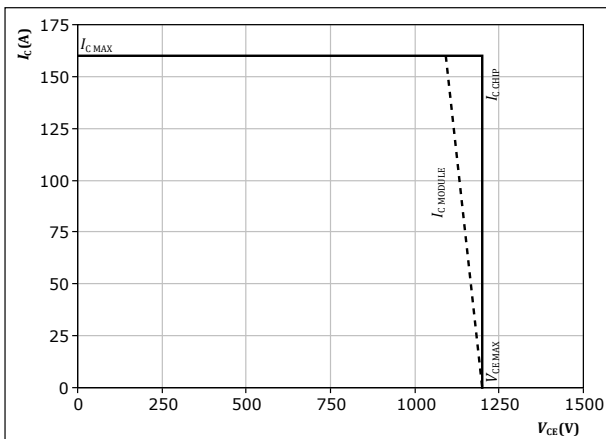
$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

T_j : 25 °C
125 °C
150 °C

figure 30. IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At $T_j = 150$ °C
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω



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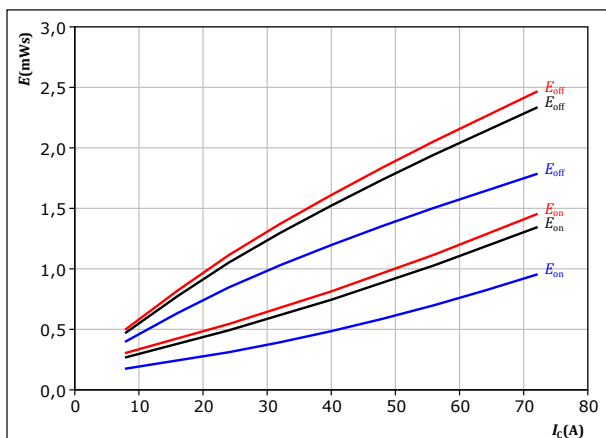
Boost Switching Characteristics

figure 31.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_C)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω
 $R_{goff} = 8$ Ω

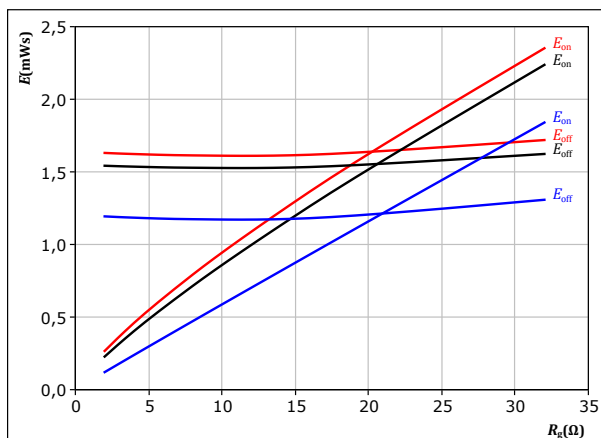
T_j : 25 °C
125 °C
150 °C

figure 32.

IGBT

Typical switching energy losses as a function of gate resistor

$$E = f(R_g)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

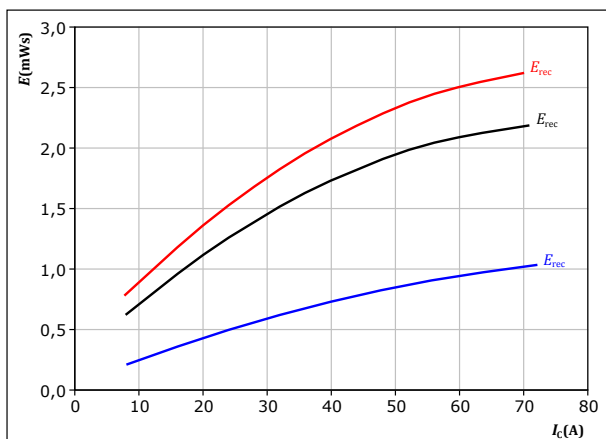
T_j : 25 °C
125 °C
150 °C

figure 33.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 8$ Ω

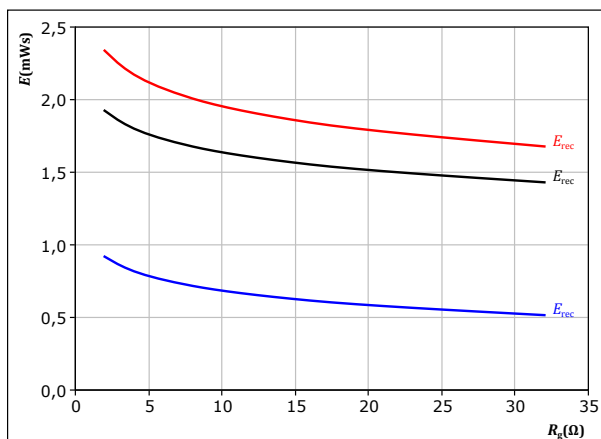
T_j : 25 °C
125 °C
150 °C

figure 34.

FWD

Typical reverse recovered energy loss as a function of gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 40$ A

T_j : 25 °C
125 °C
150 °C



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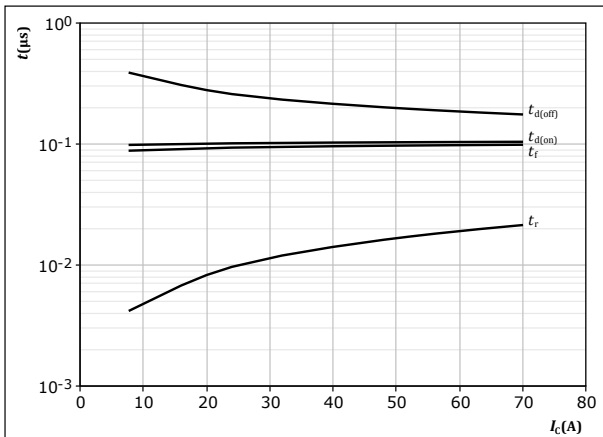
Boost Switching Characteristics

figure 35.

IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



With an inductive load at

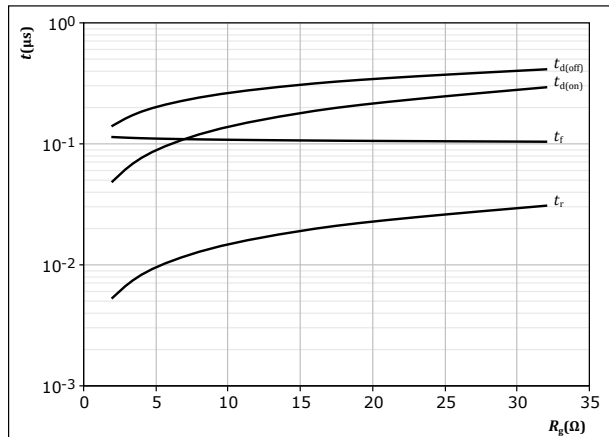
$T_j = 150 \text{ } ^\circ\text{C}$
 $V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \text{ } \Omega$
 $R_{goff} = 8 \text{ } \Omega$

figure 36.

IGBT

Typical switching times as a function of gate resistor

$$t = f(R_g)$$



With an inductive load at

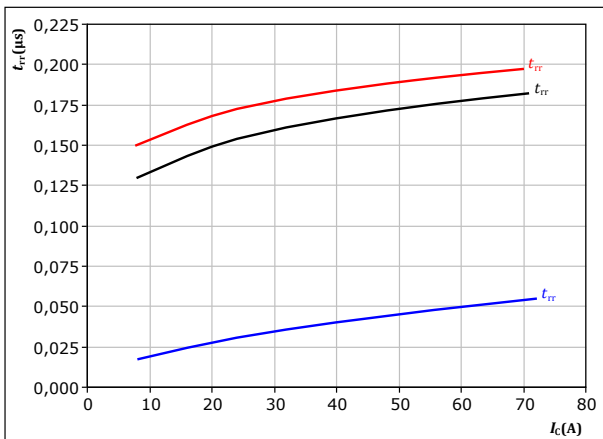
$T_j = 150 \text{ } ^\circ\text{C}$
 $V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 40 \text{ A}$

figure 37.

FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



With an inductive load at

$V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \text{ } \Omega$

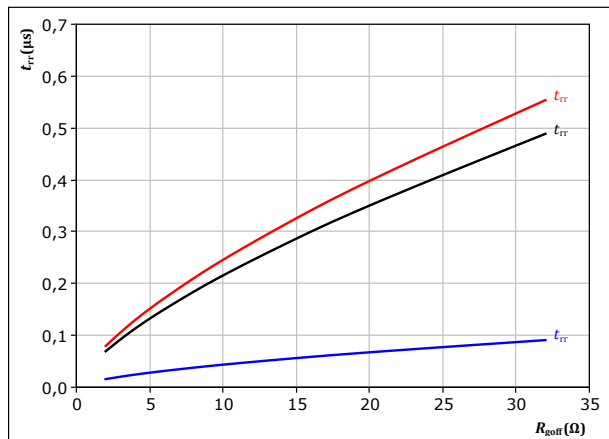
T_j : — 25 °C
 — 125 °C
 — 150 °C

figure 38.

FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor

$$t_{rr} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 40 \text{ A}$

T_j : — 25 °C
 — 125 °C
 — 150 °C



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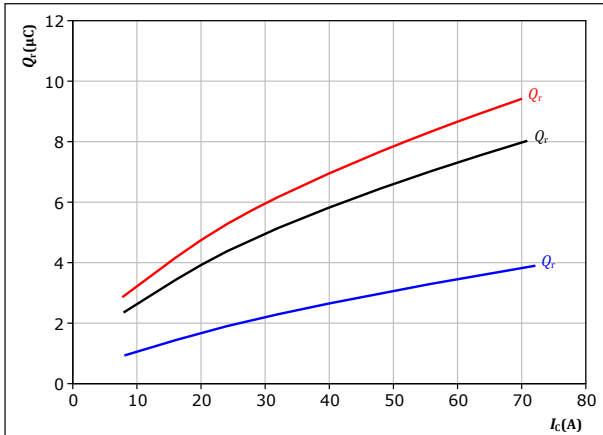
Boost Switching Characteristics

figure 39.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_{goff} = 8$ Ω

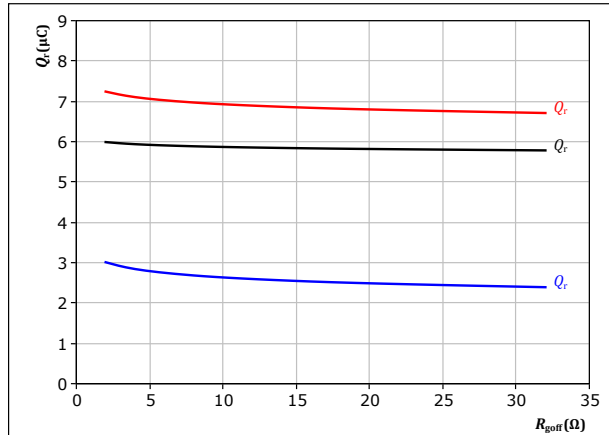
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 40.

FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 40$ A

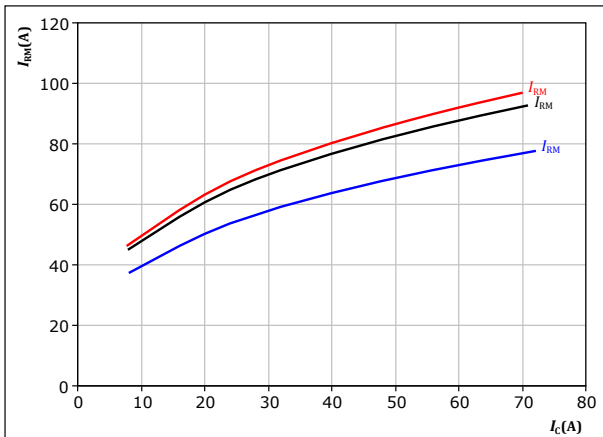
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 41.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_{goff} = 8$ Ω

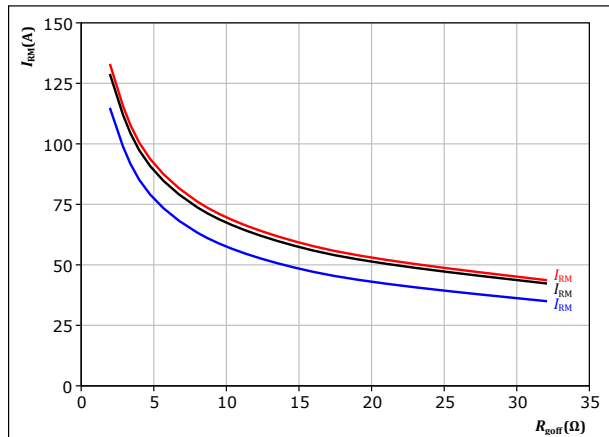
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 42.

FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 40$ A

T_j :
— 25 °C
— 125 °C
— 150 °C



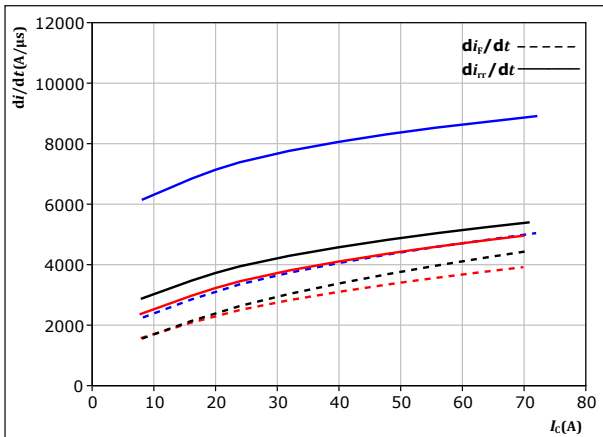
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datasheet

Boost Switching Characteristics

figure 43. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



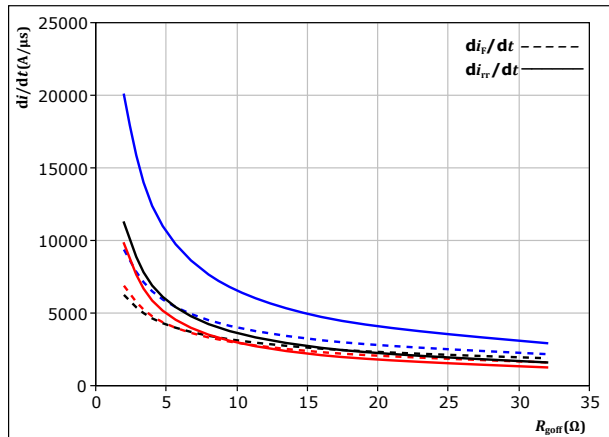
With an inductive load at

$V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 8 \text{ } \Omega$

$T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 44. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_r/dt = f(R_{goff})$



With an inductive load at

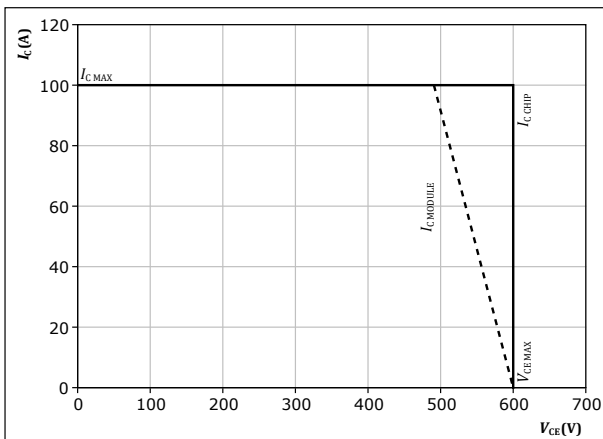
$V_{CE} = 350 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 40 \text{ A}$

$T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 45. IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At $T_j = 150 \text{ } ^\circ\text{C}$
 $R_{gon} = 8 \text{ } \Omega$
 $R_{goff} = 8 \text{ } \Omega$



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Switching Definitions

figure 46. IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

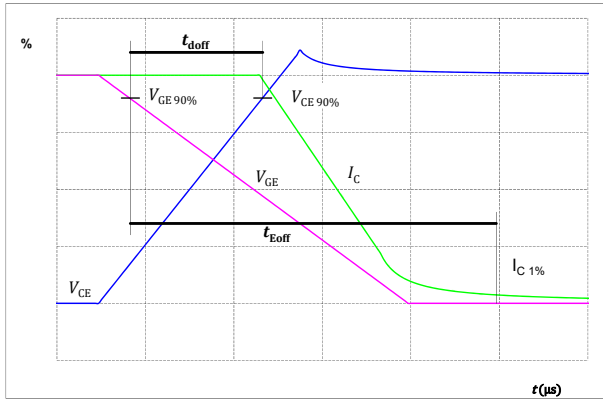


figure 47. IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

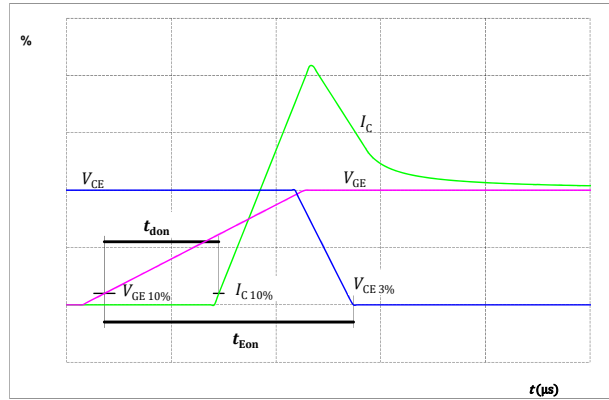


figure 48. IGBT

Turn-off Switching Waveforms & definition of t_f

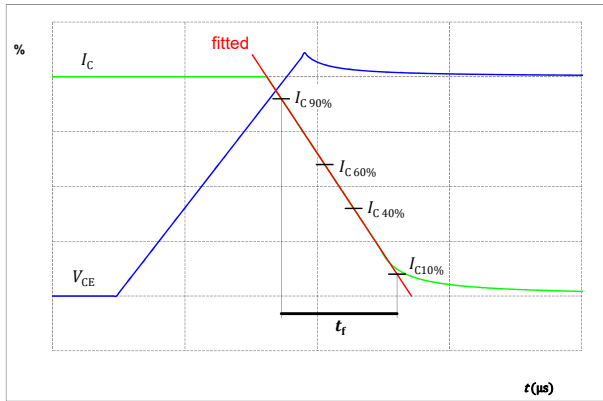
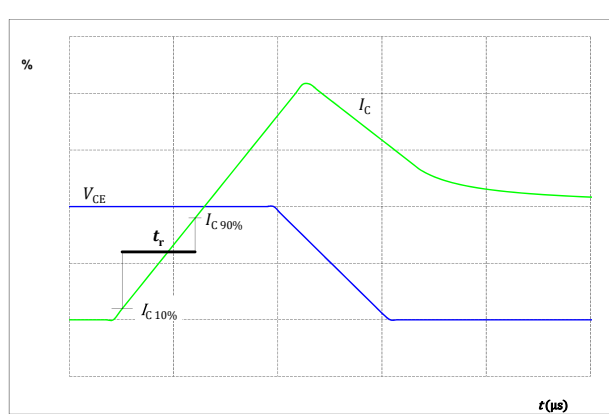


figure 49. IGBT

Turn-on Switching Waveforms & definition of t_r





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Switching Definitions

figure 50.

FWD

Turn-off Switching Waveforms & definition of t_{rr}

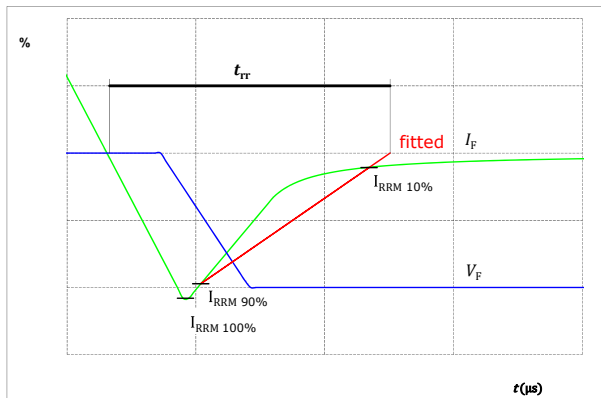
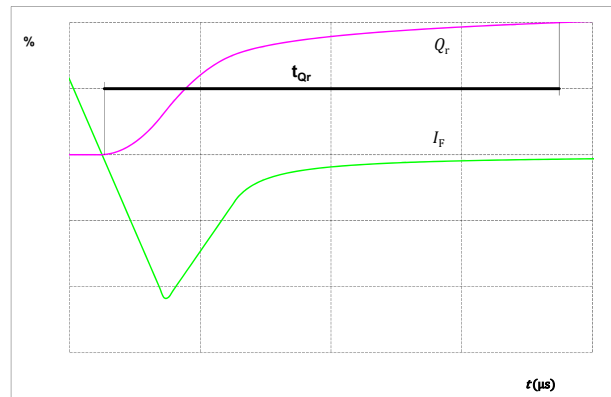


figure 51.

FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)





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10-PZ12NMA080SH-M269FY

datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-PZ12NMA080SH-M269FY
With thermal paste (5,2 W/mK, PTM6000HV)	10-PZ12NMA080SH-M269FY-/7/
With thermal paste (3,4 W/mK, PSX-P7)	10-PZ12NMA080SH-M269FY-/3/

Marking						
Text	Name		Date code	UL & VIN	Lot	Serial
	NN-NNNNNNNNNNNNNN- TTTTTUVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	Type&Ver	Lot number	Serial	Date code		
	TTTTTUVV	LLLLL	SSSS	WWYY		

Outline

Pin table [mm]			
Pin	X	Y	Function
1	33,6	0	S2
2	30,8	0	G2
3	22	0	-DC
4	19,2	0	-DC
5	10,1	0	GND
6	2,8	0	S4
7	0	0	G4
8	0	7,1	Line
9	0	9,9	Line
10	0	12,7	Line
11	0	15,5	Line
12	0	22,6	G3
13	2,8	22,6	S3
14	10,1	22,6	GND
15	19,2	22,6	+DC
16	22	22,6	+DC
17	30,8	22,6	G1
18	33,6	22,6	S1
19	33,6	14,8	NTC1
20	33,6	8,2	NTC2
21	not assembled		
22	not assembled		

center of press-fit pinhead
for connection parameter see the handling instruction

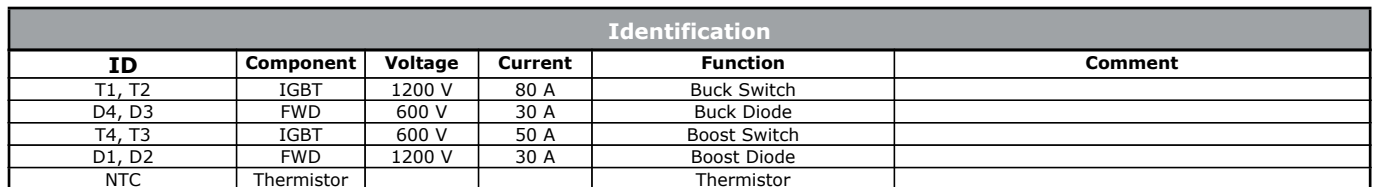
12,93 ±0,1
16,2 ±0,5
16,8

11,3
Y
X
16,8

Tolerance of pinpositions: ±0,5mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



datasheet





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datasheet

Packaging instruction				
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ	Sample
Handling instruction				
Handling instructions for <i>flow 0</i> packages see vincotech.com website.				
Package data				
Package data for <i>flow 0</i> packages see vincotech.com website.				
Vincotech thermistor reference				
See Vincotech thermistor reference table at vincotech.com website.				
UL recognition and file number				
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.				

Document No.:	Date:	Modification:	Pages
10-PZ12NMA080SH-M269FY-D5-14	11 Sep. 2021	New Datasheet format, module is unchanged Introduce Rth values with PSX-P7 TIM Separate datasheet for pressfit pin version	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.