


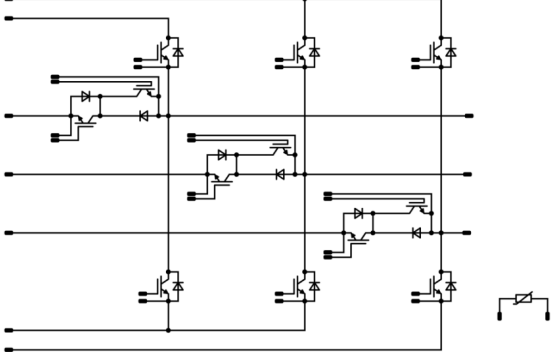
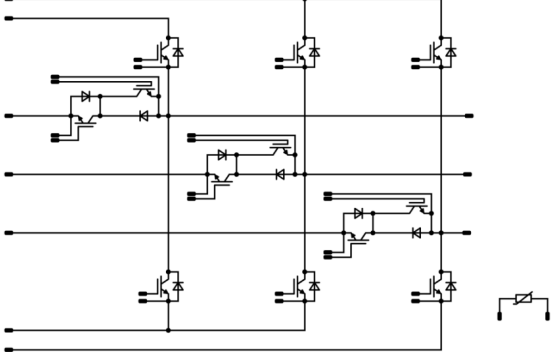
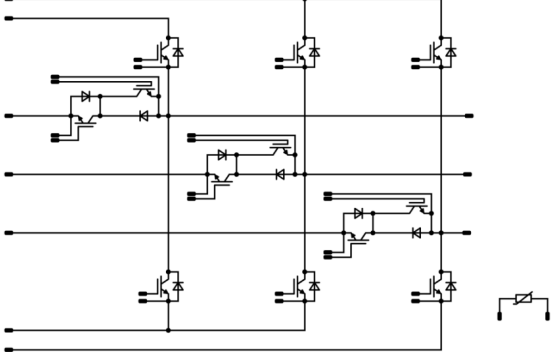




Vincotech

<i>flow 3xMNPC 1</i>	1200 V / 25 A				
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="background-color: #cccccc; padding: 2px;">Features</th> </tr> </thead> <tbody> <tr> <td style="padding: 5px;"> <ul style="list-style-type: none"> 3 phase mixed voltage component topology Neutral point clamped inverter Reactive power capability Low inductance layout </td> </tr> </tbody> </table>	Features	<ul style="list-style-type: none"> 3 phase mixed voltage component topology Neutral point clamped inverter Reactive power capability Low inductance layout 	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="background-color: #cccccc; padding: 2px;"><i>flow 1 12 mm housing</i></th> </tr> </thead> <tbody> <tr> <td style="text-align: center; padding: 10px;">  </td> </tr> </tbody> </table>	<i>flow 1 12 mm housing</i>	
Features					
<ul style="list-style-type: none"> 3 phase mixed voltage component topology Neutral point clamped inverter Reactive power capability Low inductance layout 					
<i>flow 1 12 mm housing</i>					
					
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="background-color: #cccccc; padding: 2px;">Target applications</th> </tr> </thead> <tbody> <tr> <td style="padding: 5px;"> <ul style="list-style-type: none"> Solar Inverters UPS </td> </tr> </tbody> </table>	Target applications	<ul style="list-style-type: none"> Solar Inverters UPS 	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="background-color: #cccccc; padding: 2px;">Schematic</th> </tr> </thead> <tbody> <tr> <td style="text-align: center; padding: 10px;">  </td> </tr> </tbody> </table>	Schematic	
Target applications					
<ul style="list-style-type: none"> Solar Inverters UPS 					
Schematic					
					
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="background-color: #cccccc; padding: 2px;">Types</th> </tr> </thead> <tbody> <tr> <td style="padding: 5px;"> <ul style="list-style-type: none"> 10-PY12M3A025SH04-M746F43Y </td> </tr> </tbody> </table>	Types	<ul style="list-style-type: none"> 10-PY12M3A025SH04-M746F43Y 			
Types					
<ul style="list-style-type: none"> 10-PY12M3A025SH04-M746F43Y 					

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Buck Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	31	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	75	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	94	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150\text{ °C}$ $V_{GE} = 15\text{ V}$	10 800	μs V
Maximum Junction Temperature	T_{jmax}		175	°C



Vincotech

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Buck Diode				
Peak repetitive reverse voltage	V_{RRM}		650	V
Continuous (direct) forward current	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	28	A
Repetitive peak forward current	I_{FRM}		40	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	51	W
Maximum junction temperature	T_{jmax}		175	°C
Boost Switch				
Collector-emitter voltage	V_{CES}		650	V
Collector current	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	27	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	60	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	57	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$T_j \leq 150\text{ °C}$	6	μs
	V_{CC}	$V_{GE} = 15\text{ V}$	360	V
Maximum Junction Temperature	T_{jmax}		175	°C
Boost Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
Continuous (direct) forward current	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	21	A
Repetitive peak forward current	I_{FRM}		30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	56	W
Maximum Junction Temperature	T_{jmax}		175	°C



Vincotech

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{top}		-40...(T _{max} - 25)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
		AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			min. 12,7	mm
Clearance			7,89	mm
Comparative Tracking Index	CTI		> 200	

*100 % tested in production



Vincotech

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	I_D [A]	T_j [°C]	Min	Typ	Max	

Buck Switch

Static

Parameter	Symbol	$V_{GE} = V_{CE}$	V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	I_D [A]	T_j [°C]	Min	Typ	Max	Unit
Gate-emitter threshold voltage	$V_{GE(th)}$					0,00085	25	5,3	5,8	6,3	V
Collector-emitter saturation voltage	V_{CEsat}		15			25	25 125 150	1,78	1,98 2,38 2,49	2,42	V
Collector-emitter cut-off current	I_{CES}		0	1200			25			6	μA
Gate-emitter leakage current	I_{GES}		20	0			25			120	nA
Internal gate resistance	r_g								none		Ω
Input capacitance	C_{ies}								1430		pF
Output capacitance	C_{oes}	$f = 1$ MHz	0	25		25			115		
Reverse transfer capacitance	C_{res}								75		
Gate charge	Q_g		15	960		25	25		115		nC

Thermal

Parameter	Symbol	Conditions	V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	I_D [A]	T_j [°C]	Min	Typ	Max	Unit
Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)							1,01		K/W

Dynamic

Parameter	Symbol	$R_{goff} = 16$ Ω $R_{gon} = 16$ Ω	V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	I_D [A]	T_j [°C]	Min	Typ	Max	Unit	
Turn-on delay time	$t_{d(on)}$					25	25		71		ns	
Rise time	t_r					125	25		74			
Turn-off delay time	$t_{d(off)}$					150	25		72			
Turn-off delay time	$t_{d(off)}$					25	25		16		ns	
Rise time	t_r					125	25		19			
Turn-off delay time	$t_{d(off)}$					150	25		20			
Fall time	t_f					25	±15	350	15	25		mWs
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD} = 0,6$ μC				125			24	0,240		
Turn-off energy (per pulse)	E_{off}	$Q_{tFWD} = 1$ μC				150			84	0,368		
Turn-off energy (per pulse)	E_{off}	$Q_{tFWD} = 1,2$ μC				150			81	0,410	mWs	
Turn-off energy (per pulse)	E_{off}					25			0,380			
Turn-off energy (per pulse)	E_{off}					125			0,651			
Turn-off energy (per pulse)	E_{off}					150			0,730			



Vincotech

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V]	I_C [A] I_D [A]	I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Buck Diode										
Static										
Forward voltage	V_F			20		25 125 150		1,56 1,51 1,51	1,92	V
Thermal										
Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,88		K/W
Dynamic										
Peak recovery current	I_{RRM}					25 125 150		17 19 19		A
Reverse recovery time	t_{rr}					25 125 150		64 100 113		ns
Recovered charge	Q_r	$di/dt = 1013$ A/ μ s $di/dt = 1058$ A/ μ s $di/dt = 1027$ A/ μ s	± 15	350	15	25 125 150		0,619 1,020 1,182		μ C
Reverse recovered energy	E_{rec}					25 125 150		0,131 0,210 0,246		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		871 132 130		A/ μ s



Vincotech

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Boost Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE} = V_{CE}$			0,00029	25	5,1	5,8	6,4	V
Collector-emitter saturation voltage	V_{CEsat}		15		20	25 125	1,03	1,49 1,67	1,87	V
Collector-emitter cut-off current	I_{CES}		0	650		25			5	μA
Gate-emitter leakage current	I_{GES}		20	0		25			150	nA
Internal gate resistance	r_g							none		Ω
Input capacitance	C_{ies}							1100		pF
Output capacitance	C_{oes}	$f = 1$ MHz	0	25		25		71		
Reverse transfer capacitance	C_{res}							32		
Gate charge	Q_g		15	480	20	25		120		nC

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,66		K/W
-------------------------------------	---------------	---------------------------------------	--	--	--	--	--	------	--	-----

Dynamic

Turn-on delay time	$t_{d(on)}$					25 125 150		86 85 85		ns
Rise time	t_r	$R_{gon} = 16$ Ω $R_{goff} = 16$ Ω				25 125 150		16 18 18		
Turn-off delay time	$t_{d(off)}$		±15	350	15	25 125 150		132 155 159		
Fall time	t_f					25 125 150		27 93 89		
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD} = 1,3$ μC $Q_{tFWD} = 2,3$ μC $Q_{tFWD} = 2,6$ μC				25 125 150		0,289 0,395 0,416		
Turn-off energy (per pulse)	E_{off}					25 125 150		0,487 0,651 0,685		



Vincotech

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit	
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V]	I_C [A] I_D [A]	I_F [A]	T_j [°C]	Min	Typ	Max		
Boost Diode											
Static											
Forward voltage	V_F			15		25 150		1,80 1,77	2,05	V	
Thermal											
Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)							1,70		K/W
Dynamic											
Peak recovery current	I_{RRM}					25 125 150		21 24 24		A	
Reverse recovery time	t_{rr}					25 125 150		143 309 327		ns	
Recovered charge	Q_r	$di/dt = 1226 \text{ A}/\mu\text{s}$ $di/dt = 1048 \text{ A}/\mu\text{s}$ $di/dt = 1011 \text{ A}/\mu\text{s}$	± 15	350	15	25 125 150		1,321 2,345 2,626		μC	
Reverse recovered energy	E_{rec}					25 125 150		0,357 0,643 0,721		mWs	
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		1617 1191 1065		A/ μs	
Thermistor											
Rated resistance	R					25		22		k Ω	
Deviation of R_{100}	$\Delta_{R/R}$	$R_{100} = 1484 \Omega$				100	-5		5	%	
Power dissipation	P					25		5		mW	
Power dissipation constant						25		1,5		mW/K	
B-value	$B_{(25/50)}$	Tol. $\pm 1 \%$				25		3962		K	
B-value	$B_{(25/100)}$	Tol. $\pm 1 \%$				25		4000		K	
Vincotech NTC Reference									I		

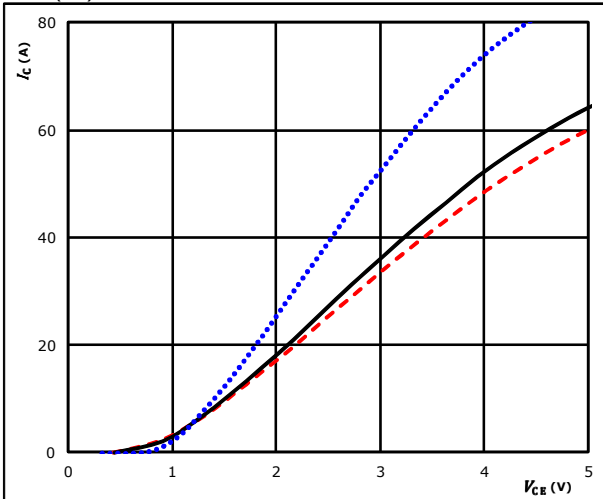


Buck Switch Characteristics

figure 1. IGBT

Typical output characteristics

$I_C = f(V_{CE})$

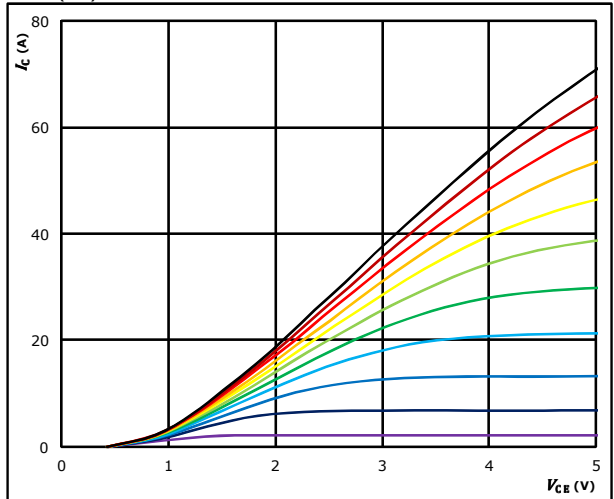


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

figure 2. IGBT

Typical output characteristics

$I_C = f(V_{CE})$

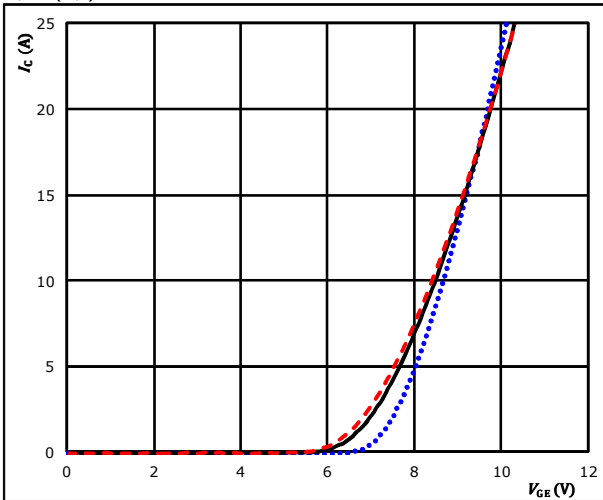


$t_p = 250 \mu s$
 $T_j = 150 \text{ }^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$

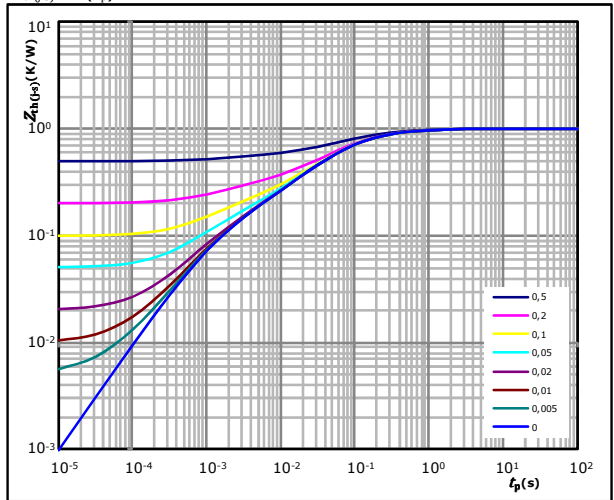


$t_p = 100 \mu s$
 $V_{CE} = 10 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

figure 4. IGBT

Transient Thermal Impedance as function of Pulse duration

$Z_{th(j-s)} = f(t_p)$



$D = t_p / T$
 $R_{th(j-s)} = 1,01 \text{ K/W}$

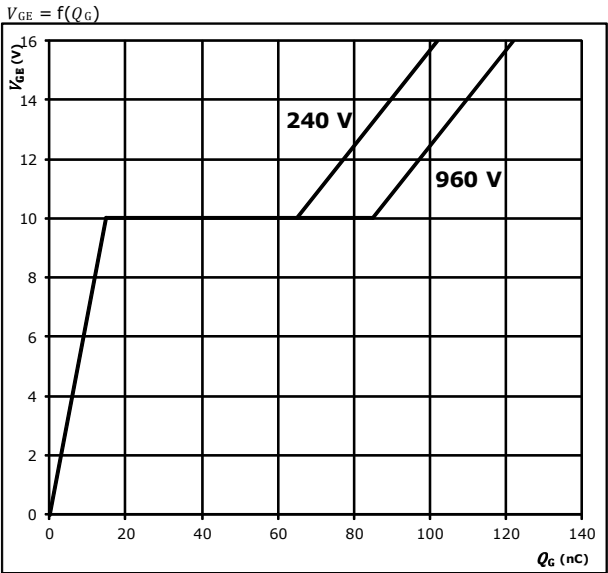
IGBT thermal model values

R (K/W)	τ (s)
8,44E-02	1,03E+00
2,46E-01	1,79E-01
4,48E-01	5,38E-02
1,38E-01	1,04E-02
5,48E-02	1,66E-03
3,85E-02	8,73E-04



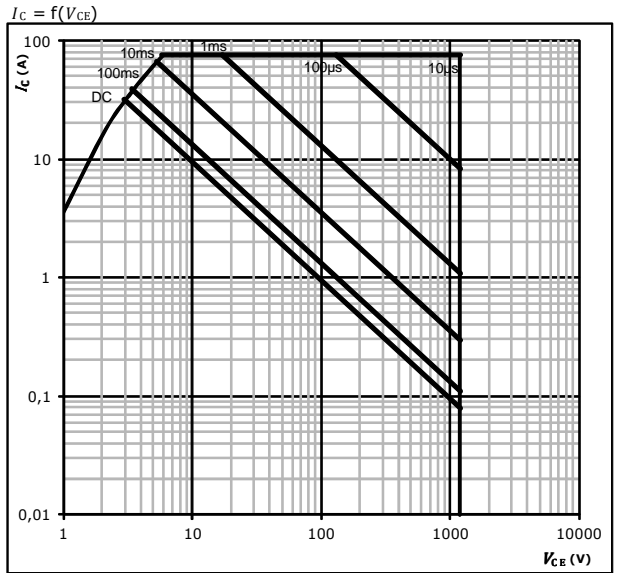
Buck Switch Characteristics

figure 5. IGBT
Gate voltage vs Gate charge



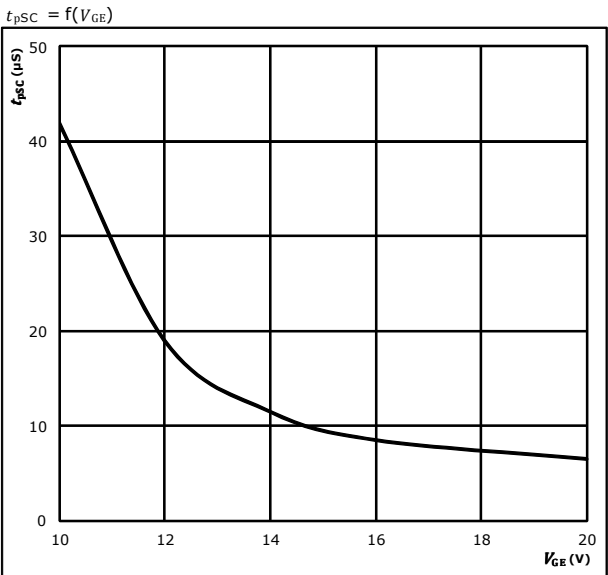
At
 $I_C = 25 \text{ A}$

figure 6. IGBT
Safe operating area



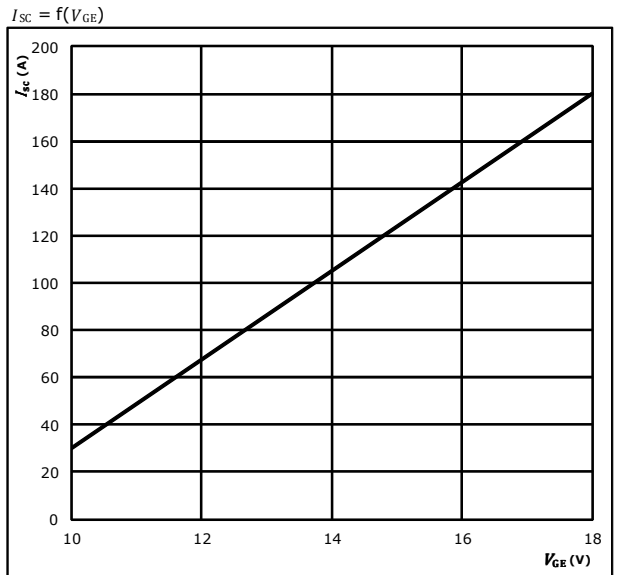
At
 $D =$ single pulse
 $T_s = 80 \text{ }^\circ\text{C}$
 $V_{GE} = \pm 15 \text{ V}$
 $T_j = T_{jmax}$

figure 7. IGBT
Short circuit duration as a function of V_{GE}



At
 $V_{CE} = 600 \text{ V}$
 $T_j \leq 150 \text{ }^\circ\text{C}$

figure 8. IGBT
Typical short circuit current as a function of V_{GE}



At
 $V_{CE} \leq 600 \text{ V}$
 $T_j \leq 25 \text{ }^\circ\text{C}$

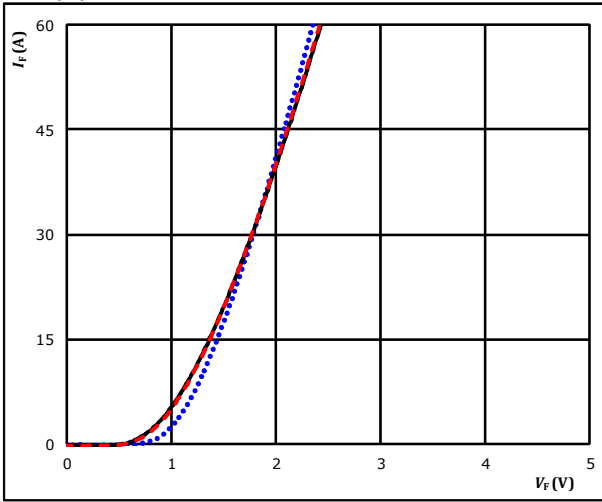


Buck Diode Characteristics

figure 1. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

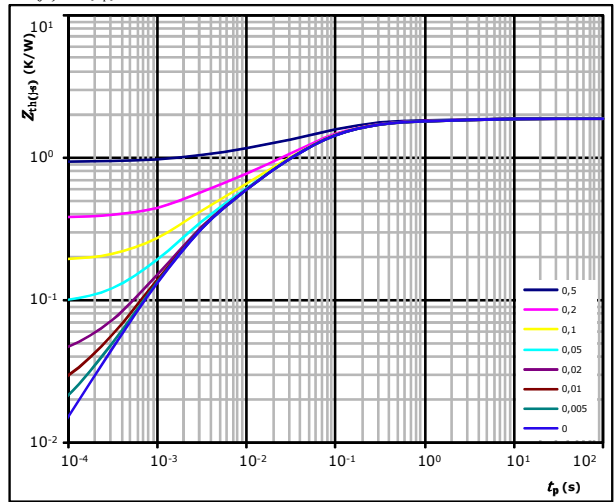


$t_p = 250 \mu s$
 T_j : 25 °C
 125 °C ———
 150 °C - - - -

figure 2. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,88 \text{ K/W}$
 FWD thermal model values

R (K/W)	τ (s)
8,42E-02	3,60E+00
1,79E-01	3,95E-01
8,86E-01	7,08E-02
4,50E-01	1,69E-02
2,75E-01	2,45E-03

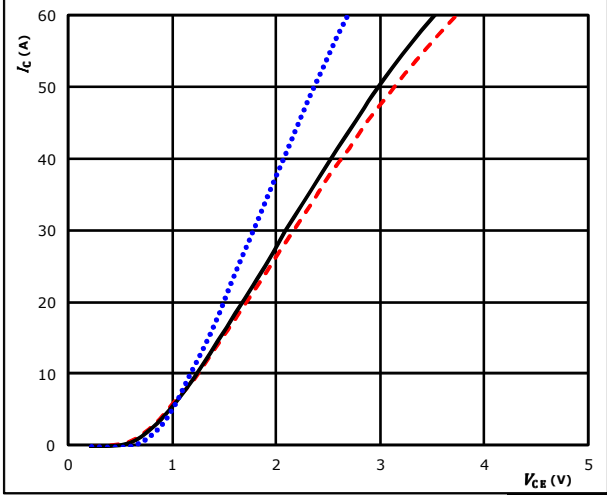


Boost Switch Characteristics

figure 1. IGBT

Typical output characteristics

$I_C = f(V_{CE})$

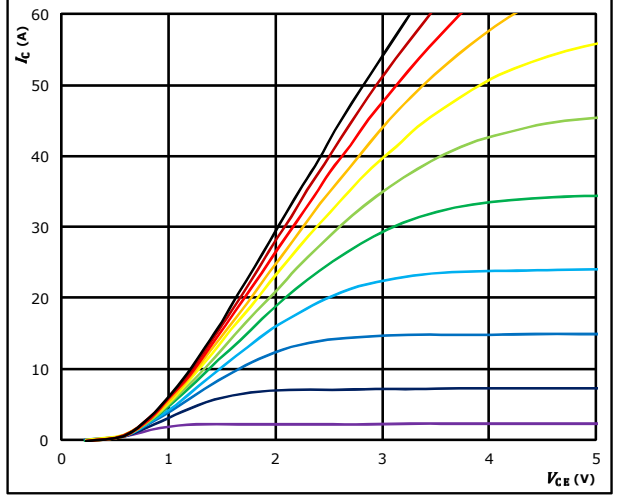


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

figure 2. IGBT

Typical output characteristics

$I_C = f(V_{CE})$

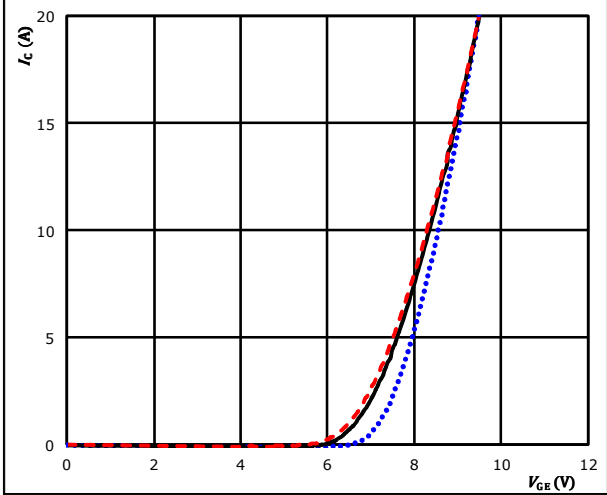


$t_p = 250 \mu s$
 $T_j = 150 \text{ }^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$

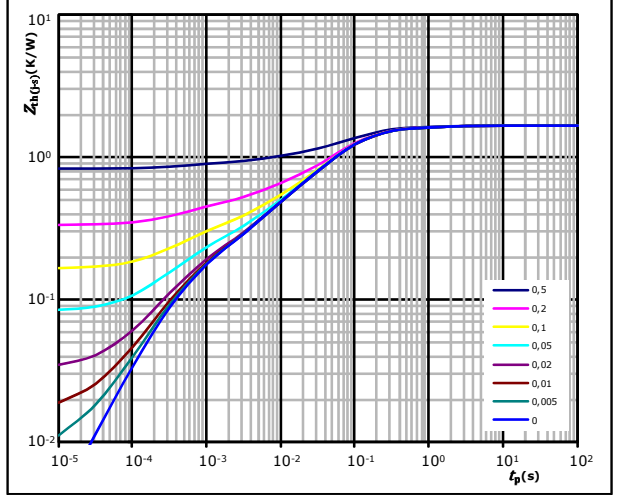


$t_p = 100 \mu s$
 $V_{CE} = 10 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

figure 4. IGBT

Transient Thermal Impedance as function of Pulse duration

$Z_{th(j-s)} = f(t_p)$



$D = t_p / T$
 $R_{th(j-s)} = 1,66 \text{ K/W}$

IGBT thermal model values

R (K/W)	τ (s)
7,52E-02	1,78E+00
1,97E-01	2,71E-01
9,36E-01	6,94E-02
1,96E-01	1,36E-02
1,32E-01	3,45E-03
1,27E-01	4,12E-04

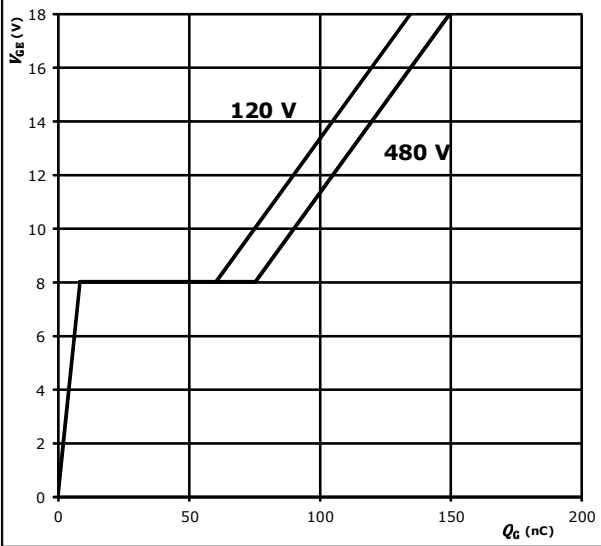


Boost Switch Characteristics

figure 5. IGBT

Gate voltage vs Gate charge

$$V_{GE} = f(Q_G)$$

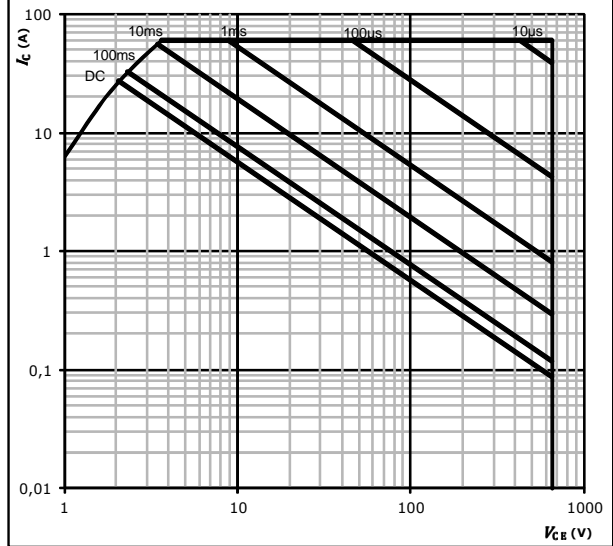


At
 $I_C = 20 \text{ A}$

figure 6. IGBT

Safe operating area

$$I_C = f(V_{CE})$$

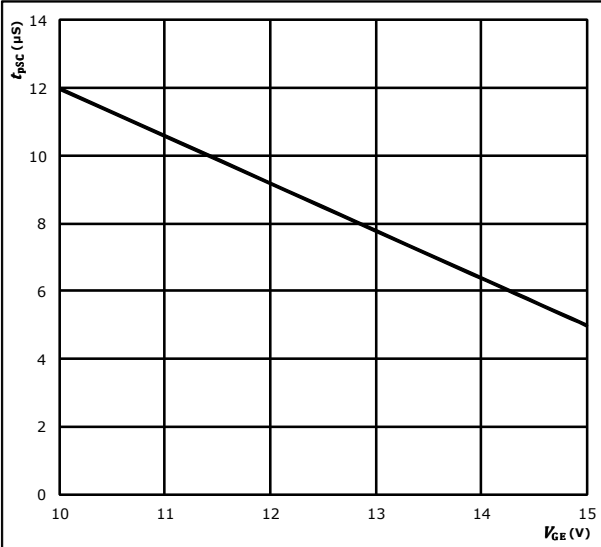


At
 $D = \text{single pulse}$
 $T_s = 80 \text{ }^\circ\text{C}$
 $V_{GE} = \pm 15 \text{ V}$
 $T_j = T_{jmax}$

figure 7. IGBT

Short circuit duration as a function of V_{GE}

$$t_{pSC} = f(V_{GE})$$

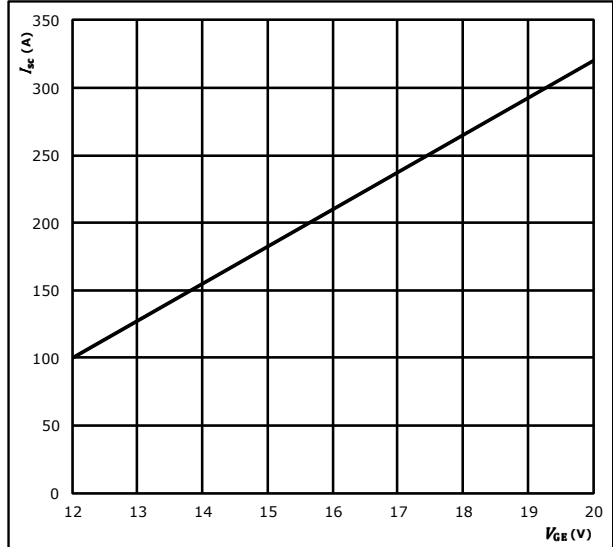


At
 $V_{CE} = 650 \text{ V}$
 $T_j \leq 175 \text{ }^\circ\text{C}$

figure 8. IGBT

Typical short circuit current as a function of V_{GE}

$$I_{SC} = f(V_{GE})$$



At
 $V_{CE} \leq 650 \text{ V}$
 $T_j \leq 175 \text{ }^\circ\text{C}$

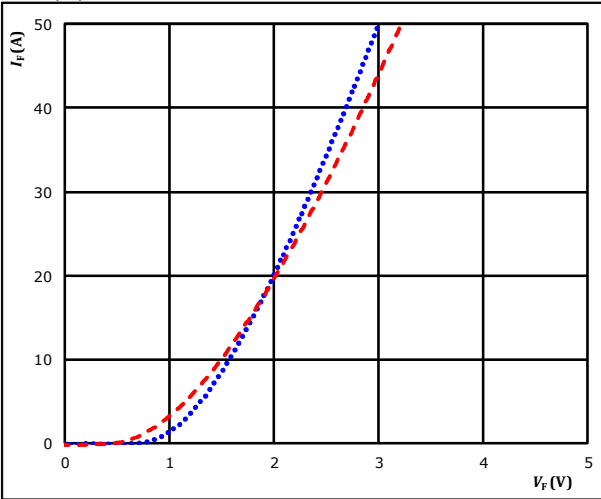


Boost Diode Characteristics

figure 1. FWD

Typical forward characteristics

$I_F = f(V_F)$

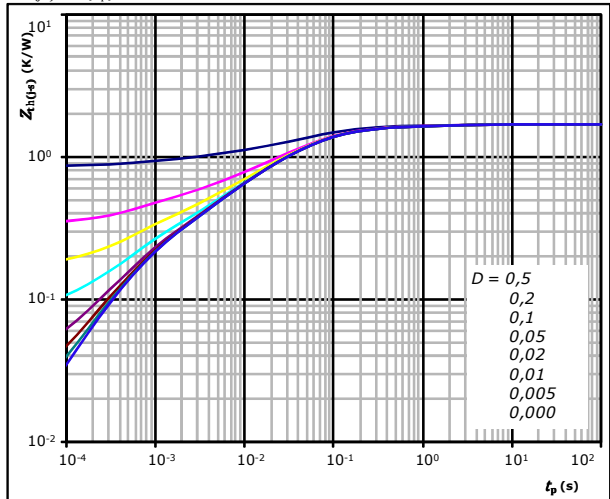


$t_p = 250\ \mu\text{s}$
 $T_j: 25\text{ °C}$ (blue dotted line)
 150 °C (red dashed line)

figure 2. FWD

Transient thermal impedance as a function of pulse width

$Z_{th(0-s)} = f(t_p)$



$D = t_p / T$
 $R_{th(0-s)} = 1,70\text{ K/W}$

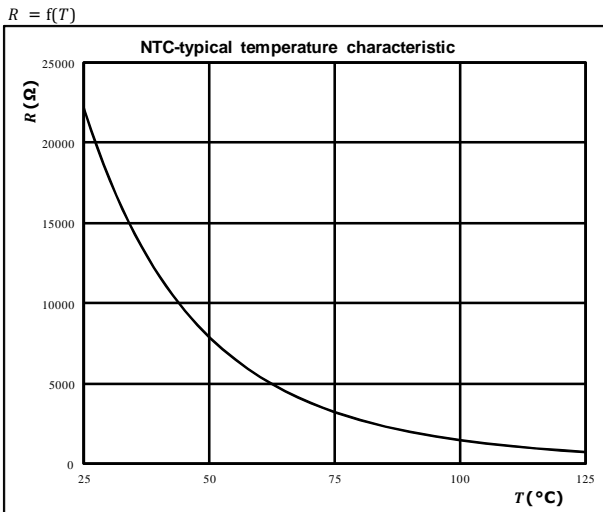
FWD thermal model values

R (K/W)	τ (s)
5,72E-02	2,79E+00
1,30E-01	3,93E-01
6,56E-01	6,76E-02
4,62E-01	1,96E-02
2,31E-01	4,04E-03
1,63E-01	5,86E-04



Thermistor Characteristics

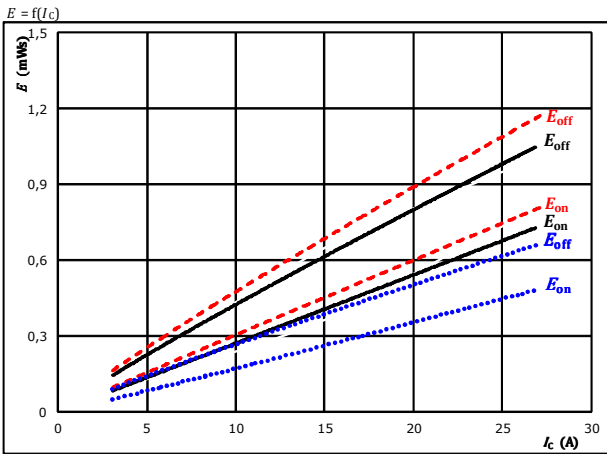
figure 1. Thermistor
Typical NTC characteristic as a function of temperature





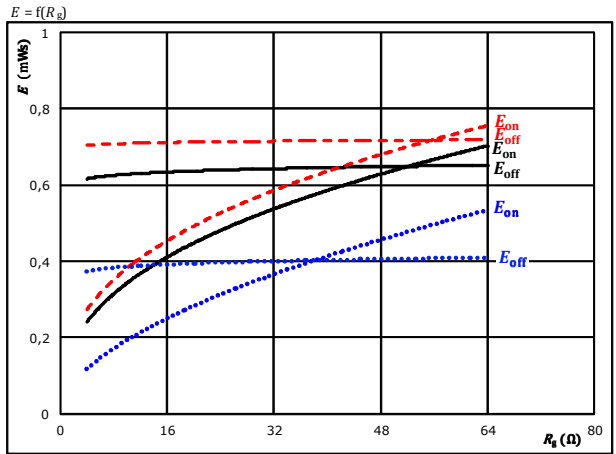
Buck Switching Characteristics

figure 1. IGBT
Typical switching energy losses as a function of collector current



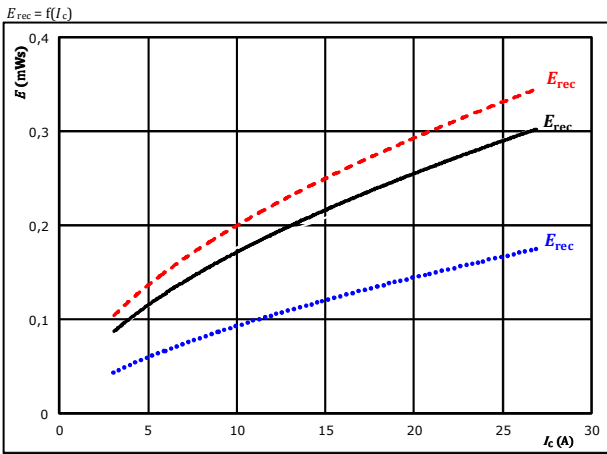
With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω
 $R_{goff} = 16$ Ω
 T_j : 25 °C (dotted blue), 125 °C (solid black), 150 °C (dashed red)

figure 2. IGBT
Typical switching energy losses as a function of gate resistor



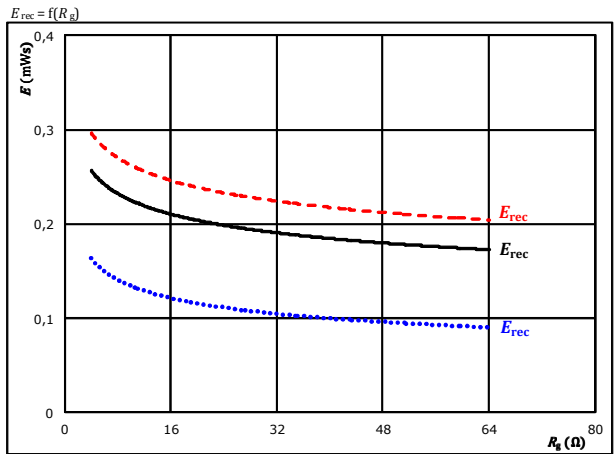
With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 15$ A
 T_j : 25 °C (dotted blue), 125 °C (solid black), 150 °C (dashed red)

figure 3. FWD
Typical reverse recovered energy loss as a function of collector current



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω
 T_j : 25 °C (dotted blue), 125 °C (solid black), 150 °C (dashed red)

figure 4. FWD
Typical reverse recovered energy loss as a function of gate resistor



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_C = 15$ A
 T_j : 25 °C (dotted blue), 125 °C (solid black), 150 °C (dashed red)

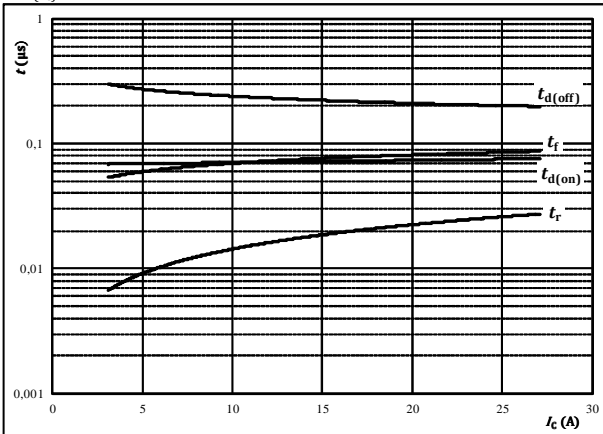


Buck Switching Characteristics

figure 5. IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



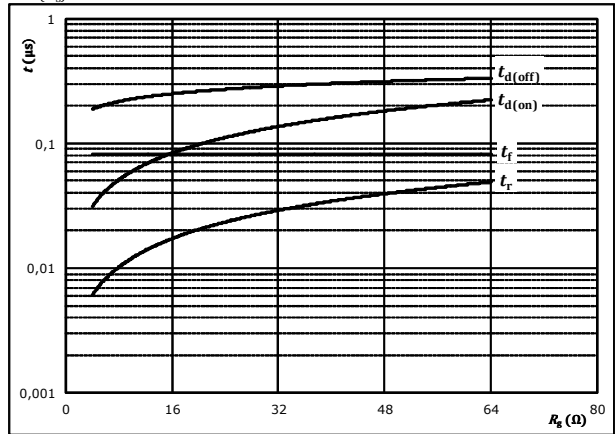
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	350	V
$V_{GE} =$	±15	V
$R_{gon} =$	16	Ω
$R_{goff} =$	16	Ω

figure 6. IGBT

Typical switching times as a function of gate resistor

$$t = f(R_g)$$



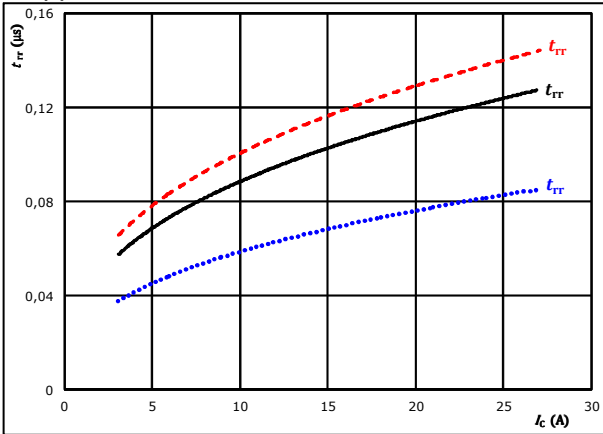
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	350	V
$V_{GE} =$	±15	V
$I_C =$	15	A

figure 7. FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$

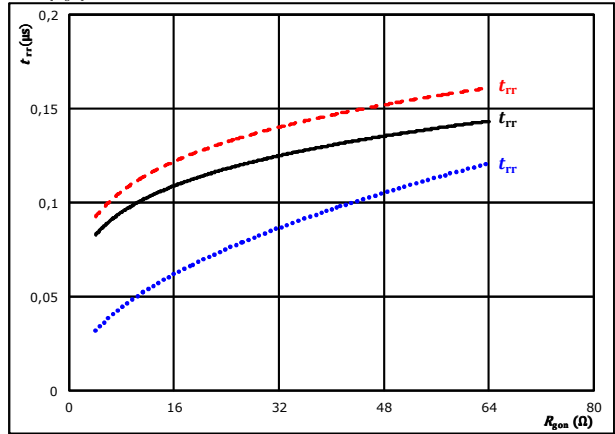


At	$V_{CE} =$	350	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		125 °C	————
	$R_{gon} =$	16	Ω		150 °C	- - - -

figure 8. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At	$V_{CE} =$	350	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		125 °C	————
	$I_C =$	15	A		150 °C	- - - -

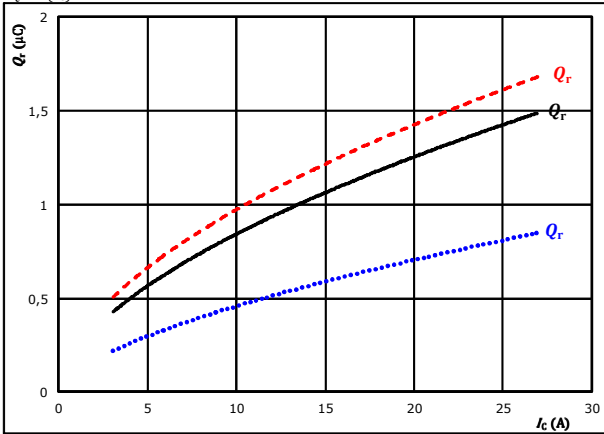


Buck Switching Characteristics

figure 9. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$

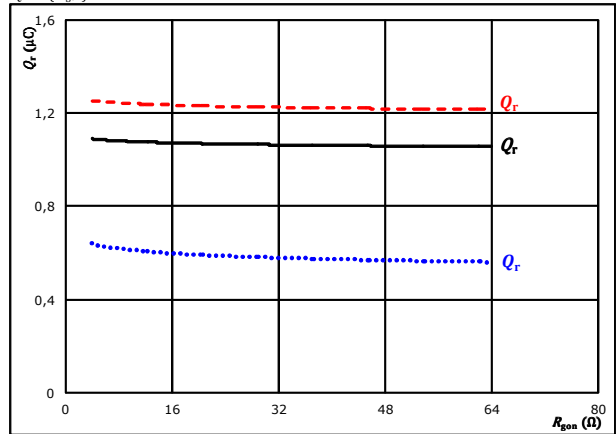


At $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gpn} = 16$ Ω
 $T_j: 25$ °C
 125 °C
 150 °C

figure 10. FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gpn})$$

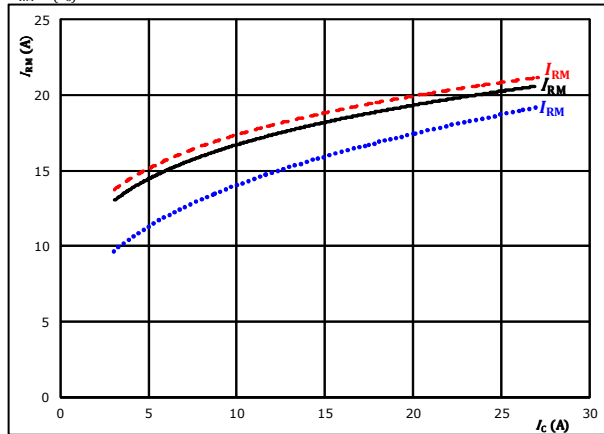


At $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 15$ A
 $T_j: 25$ °C
 125 °C
 150 °C

figure 11. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$

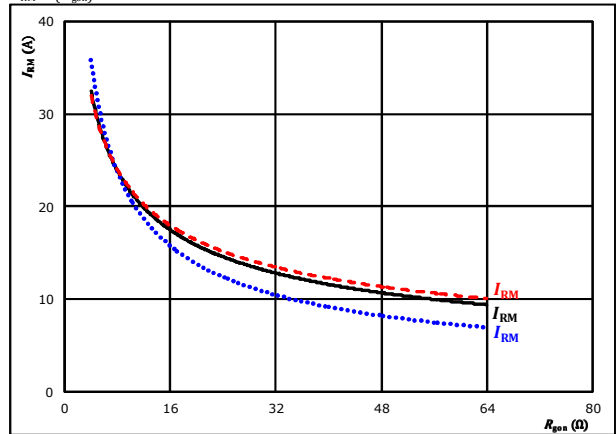


At $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gpn} = 16$ Ω
 $T_j: 25$ °C
 125 °C
 150 °C

figure 12. FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gpn})$$



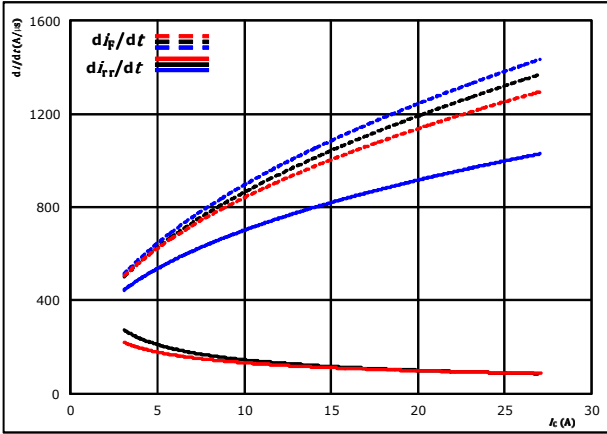
At $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 15$ A
 $T_j: 25$ °C
 125 °C
 150 °C



Buck Switching Characteristics

figure 13. FWD

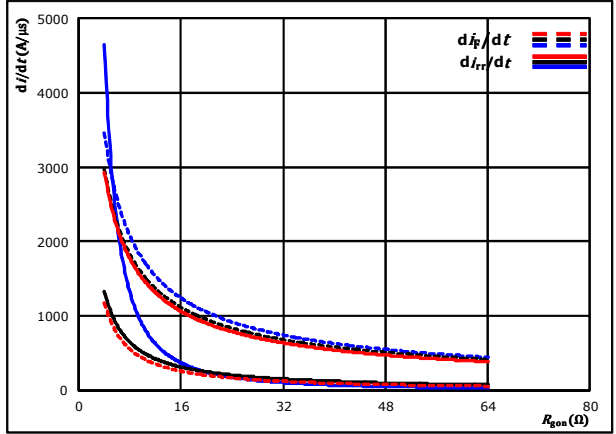
Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_{rr}/dt = f(I_c)$



At $V_{CE} = 350$ V $T_j = 25$ °C (.....)
 $V_{GE} = \pm 15$ V $T_j = 125$ °C (—)
 $R_{g(on)} = 16$ Ω $T_j = 150$ °C (---)

figure 14. FWD

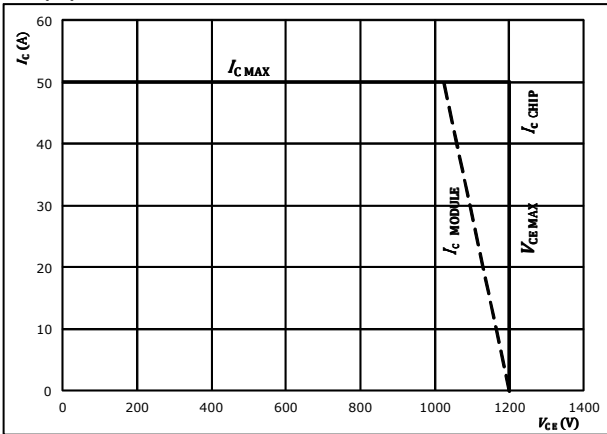
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{g(on)})$



At $V_{CE} = 350$ V $T_j = 25$ °C (.....)
 $V_{GE} = \pm 15$ V $T_j = 125$ °C (—)
 $I_c = 15$ A $T_j = 150$ °C (---)

figure 15. IGBT

Reverse bias safe operating area
 $I_c = f(V_{CE})$



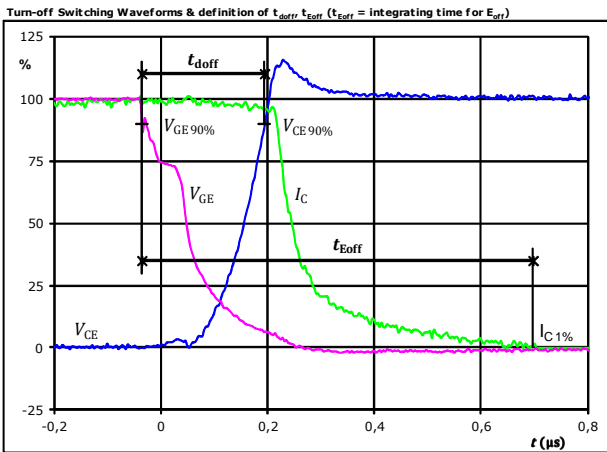
At $T_j = 175$ °C
 $R_{g(on)} = 16$ Ω
 $R_{g(off)} = 16$ Ω



Buck Switching Definitions

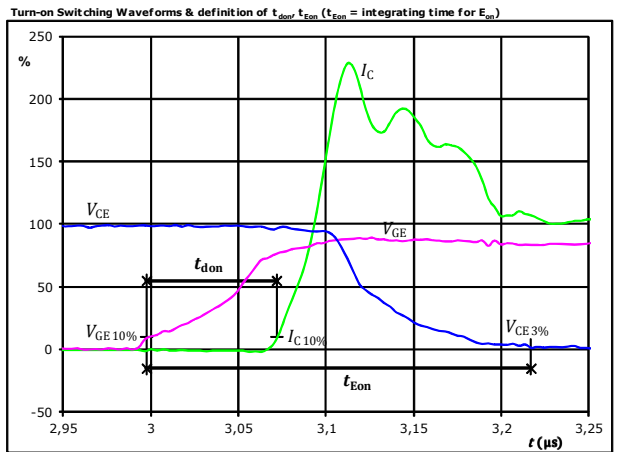
General conditions		
T_j	=	150 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

figure 1. IGBT



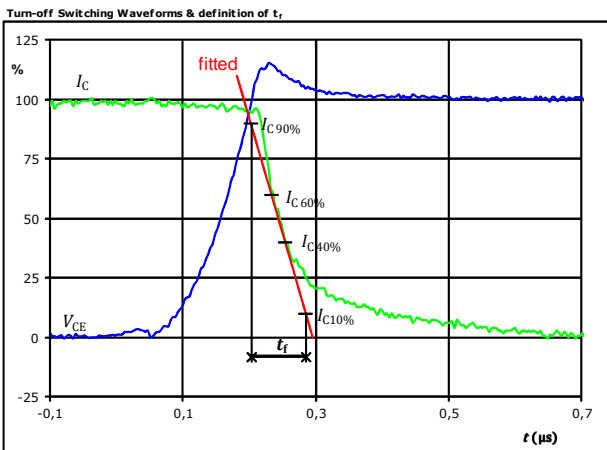
$V_{CE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_{doff} =$	0,230	μs
$t_{Eoff} =$	0,733	μs

figure 2. IGBT



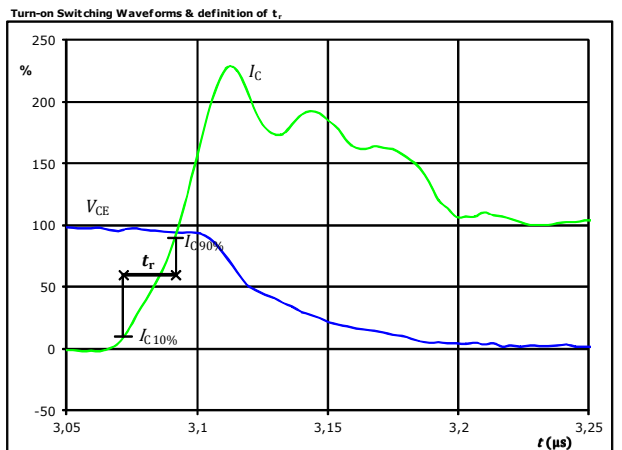
$V_{CE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_{don} =$	0,072	μs
$t_{Eon} =$	0,219	μs

figure 3. IGBT



$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_f =$	0,081	μs

figure 4. IGBT



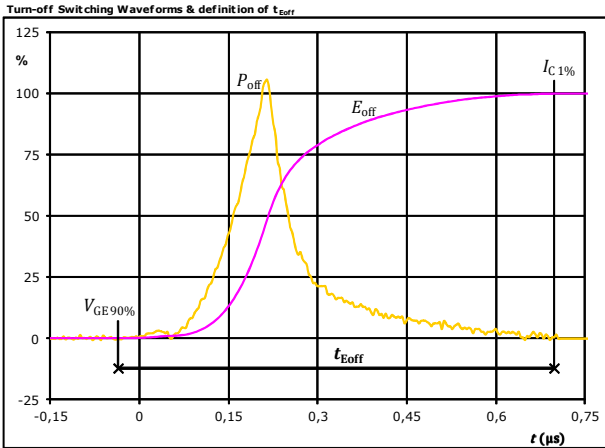
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_r =$	0,020	μs



Vincotech

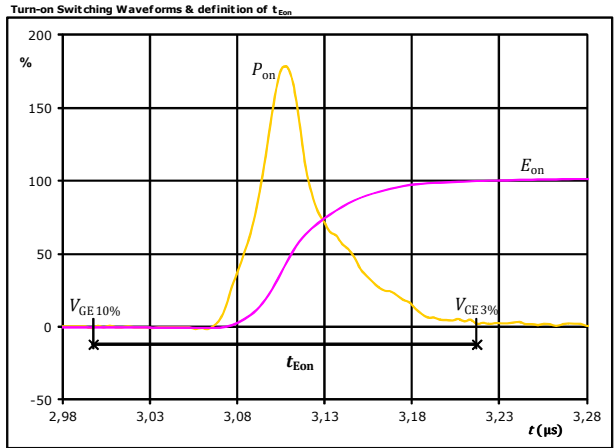
Buck Switching Characteristics

figure 5. IGBT



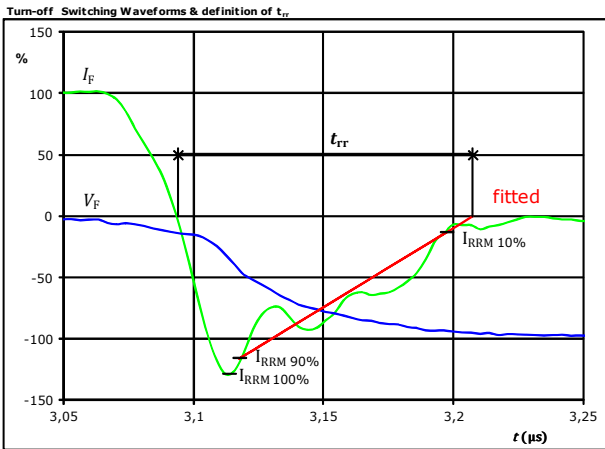
$P_{off}(100\%) =$	5,28	kW
$E_{off}(100\%) =$	0,73	mJ
$t_{Eoff} =$	0,73	µs

figure 6. IGBT



$P_{on}(100\%) =$	5,28	kW
$E_{on}(100\%) =$	0,41	mJ
$t_{Eon} =$	0,22	µs

figure 7. FWD



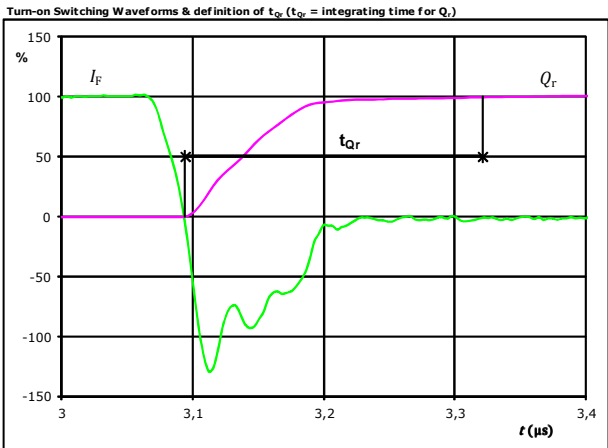
$V_F(100\%) =$	350	V
$I_F(100\%) =$	15	A
$I_{RRM}(100\%) =$	-19	A
$t_{rr} =$	0,113	µs



Vincotech

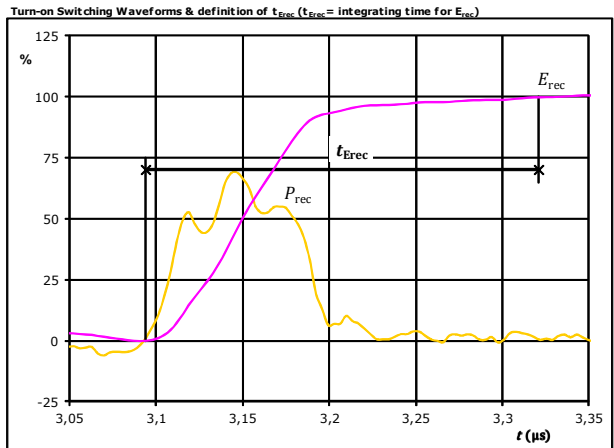
Buck Switching Characteristics

figure 8. FWD



I_F (100%) =	15	A
Q_r (100%) =	1,18	μC
t_{Qr} =	0,23	μs

figure 9. FWD



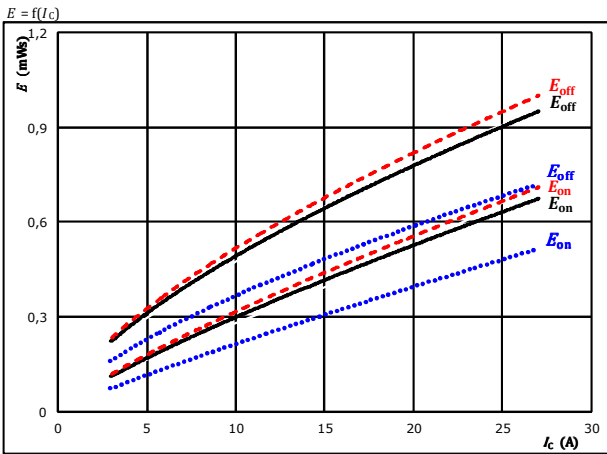
P_{rec} (100%) =	5,28	kW
E_{rec} (100%) =	0,25	mJ
t_{Erec} =	0,23	μs



Boost Switching Characteristics

figure 1. IGBT

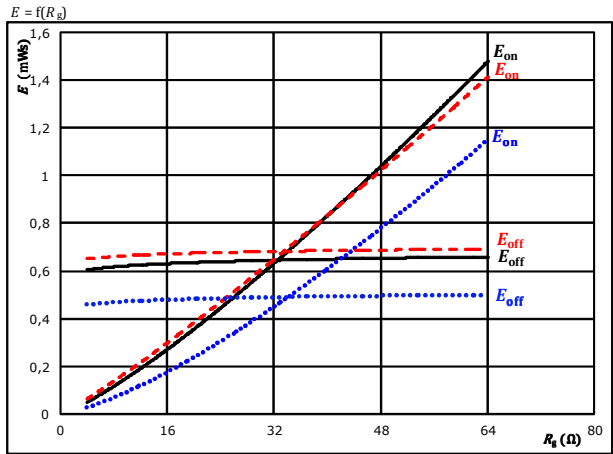
Typical switching energy losses as a function of collector current



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω
 $R_{goff} = 16$ Ω
 T_j : 25 °C (dotted), 125 °C (solid), 150 °C (dashed)

figure 2. IGBT

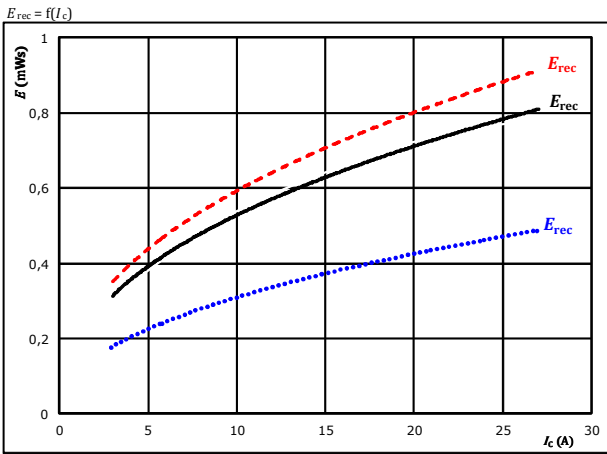
Typical switching energy losses as a function of gate resistor



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 15$ A
 T_j : 25 °C (dotted), 125 °C (solid), 150 °C (dashed)

figure 3. FWD

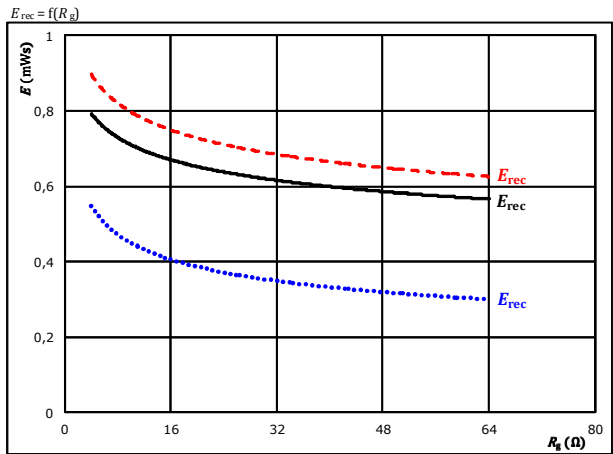
Typical reverse recovered energy loss as a function of collector current



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω
 T_j : 25 °C (dotted), 125 °C (solid), 150 °C (dashed)

figure 4. FWD

Typical reverse recovered energy loss as a function of gate resistor



With an inductive load at
 $V_{CE} = 350$ V
 $V_{GE} = \pm 15$ V
 $I_c = 15$ A
 T_j : 25 °C (dotted), 125 °C (solid), 150 °C (dashed)

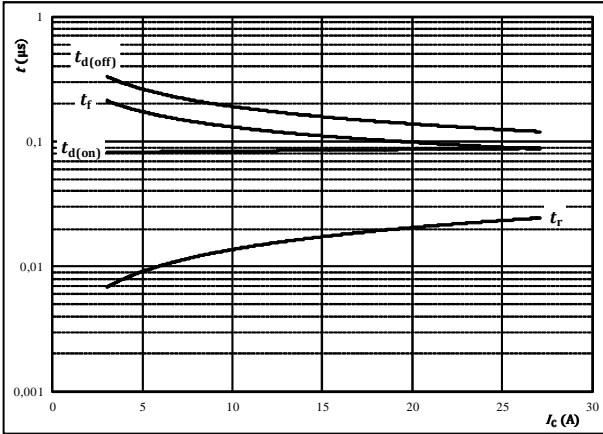


Boost Switching Characteristics

figure 5. IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



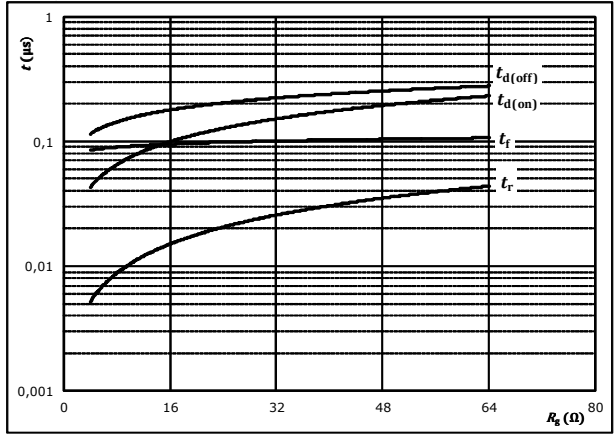
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	350	V
$V_{GE} =$	±15	V
$R_{gon} =$	16	Ω
$R_{goff} =$	16	Ω

figure 6. IGBT

Typical switching times as a function of gate resistor

$$t = f(R_g)$$



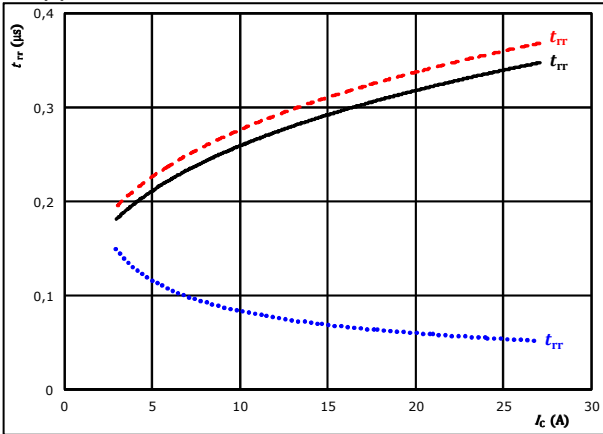
With an inductive load at

$T_j =$	150	°C
$V_{CE} =$	350	V
$V_{GE} =$	±15	V
$I_C =$	15	A

figure 7. FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$

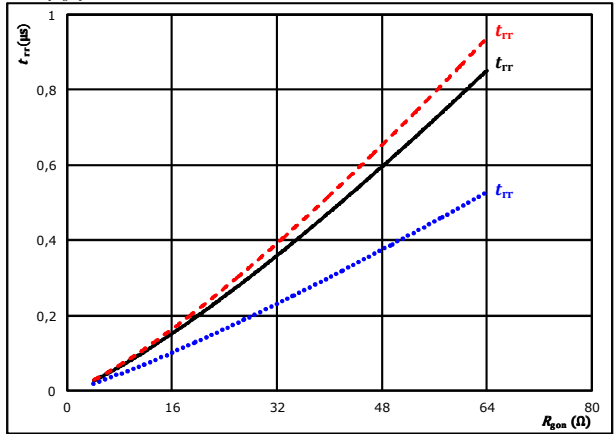


At	$V_{CE} =$	350	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		125 °C	————
	$R_{gon} =$	16	Ω		150 °C	-----

figure 8. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At	$V_{CE} =$	350	V	$T_j:$	25 °C
	$V_{GE} =$	±15	V		125 °C	————
	$I_C =$	15	A		150 °C	-----

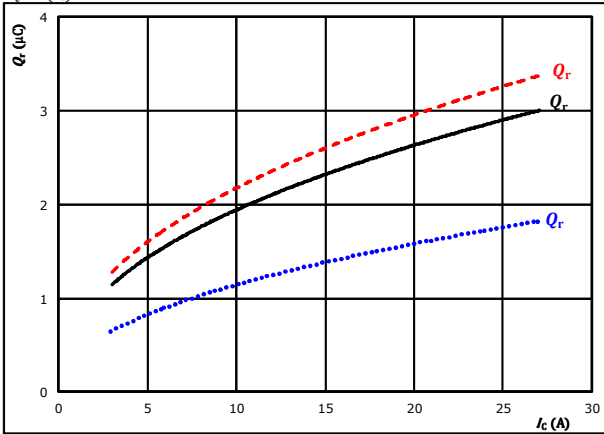


Boost Switching Characteristics

figure 9. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$

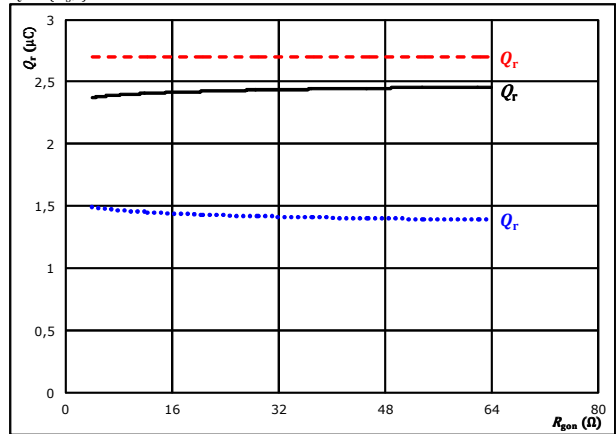


At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $R_{gpn} = 16$ Ω $T_j = 150$ °C - - - - -

figure 10. FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gpn})$$

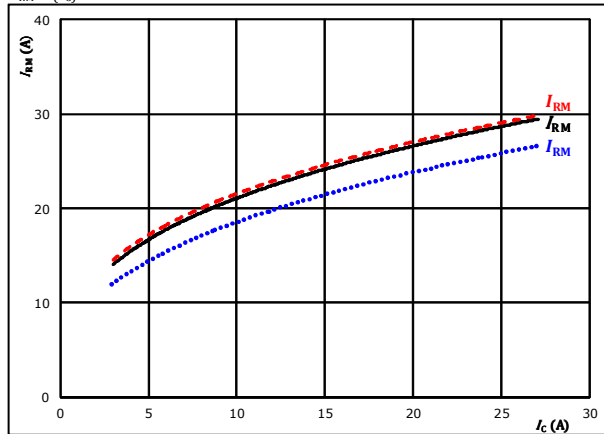


At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $I_c = 15$ A $T_j = 150$ °C - - - - -

figure 11. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$

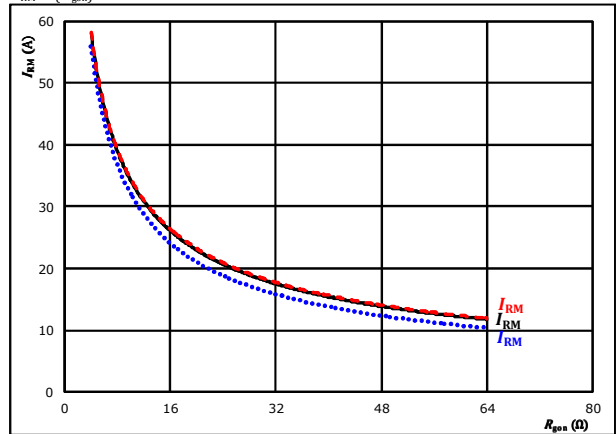


At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $R_{gpn} = 16$ Ω $T_j = 150$ °C - - - - -

figure 12. FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gpn})$$



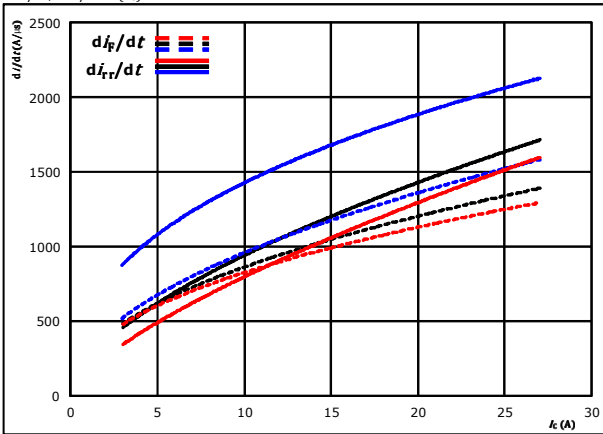
At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $I_c = 15$ A $T_j = 150$ °C - - - - -



Boost Switching Characteristics

figure 13. FWD

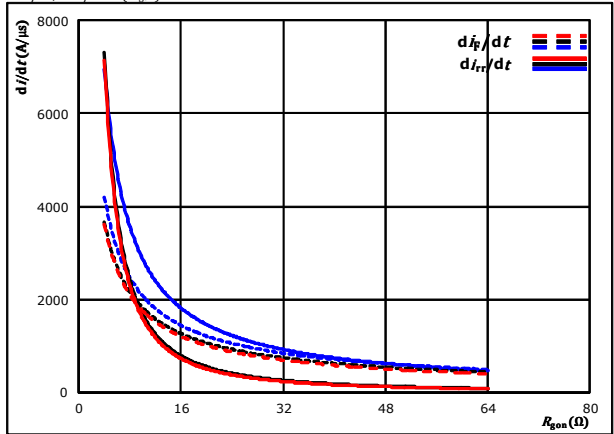
Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_{rr}/dt = f(I_c)$



At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $R_{g(on)} = 16$ Ω $T_j = 150$ °C - - - - -

figure 14. FWD

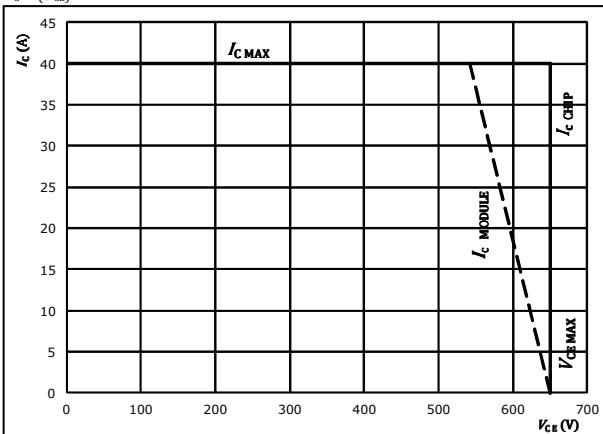
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{g(on)})$



At $V_{CE} = 350$ V $T_j = 25$ °C
 $V_{GE} = \pm 15$ V $T_j = 125$ °C ———
 $I_c = 15$ A $T_j = 150$ °C - - - - -

figure 15. IGBT

Reverse bias safe operating area
 $I_c = f(V_{CE})$



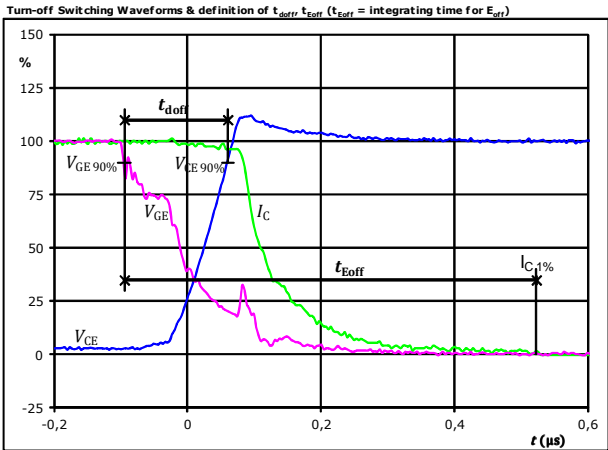
At $T_j = 175$ °C
 $R_{g(on)} = 16$ Ω
 $R_{g(off)} = 16$ Ω



Boost Switching Definitions

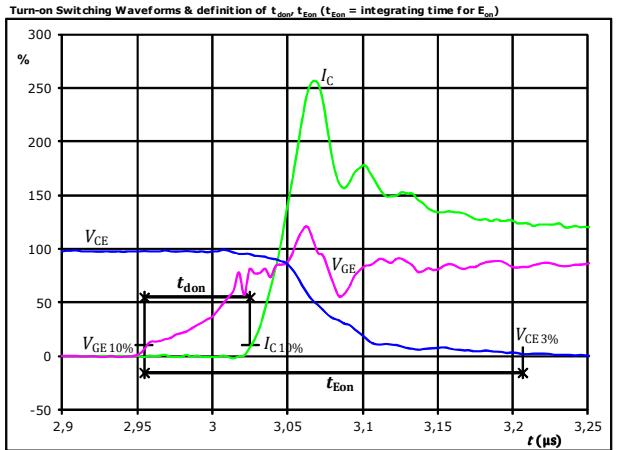
General conditions		
T_j	=	125 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

figure 1. IGBT



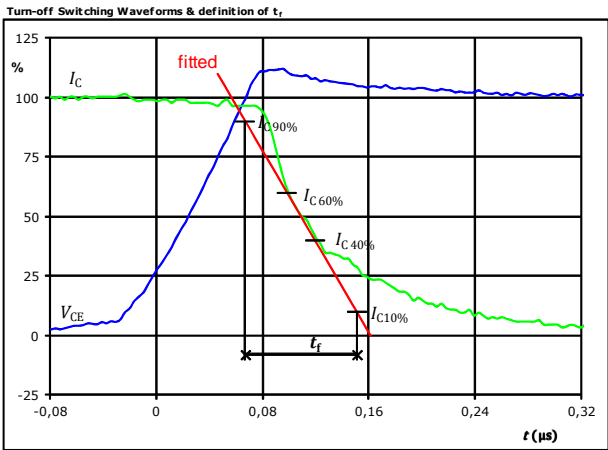
$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_{doff} =$	0,155	μs
$t_{Eoff} =$	0,618	μs

figure 2. IGBT



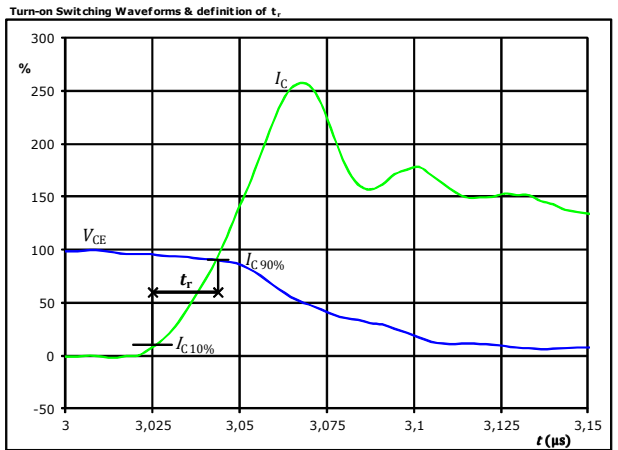
$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_{don} =$	0,085	μs
$t_{Eon} =$	0,252	μs

figure 3. IGBT



$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_f =$	0,093	μs

figure 4. IGBT



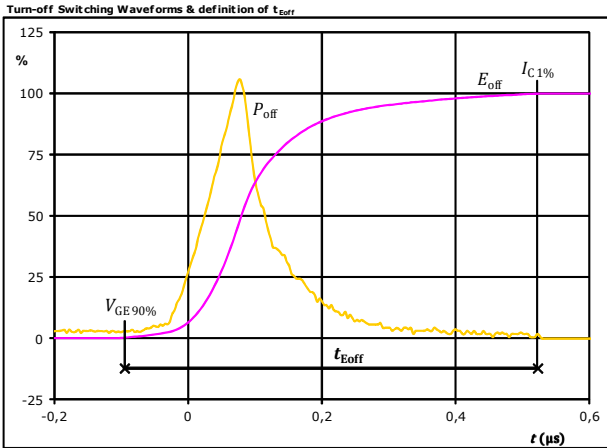
$V_C(100\%) =$	350	V
$I_C(100\%) =$	15	A
$t_r =$	0,018	μs



Vincotech

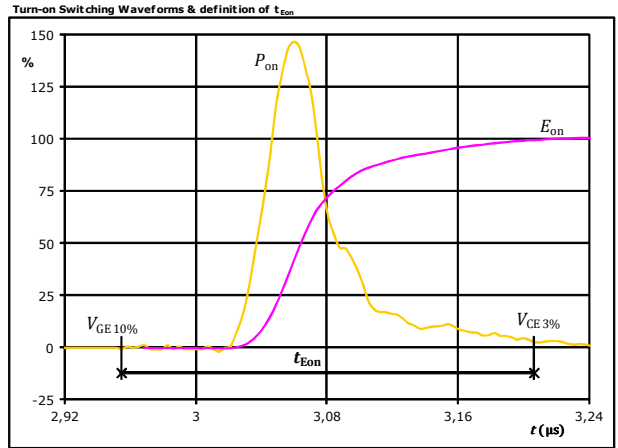
Boost Switching Characteristics

figure 5. IGBT



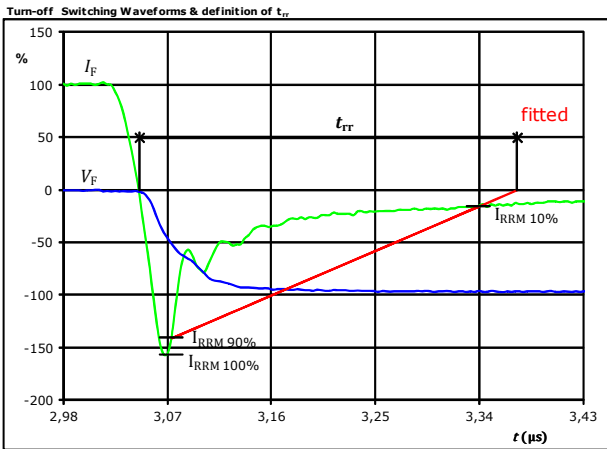
$P_{off}(100\%) =$	5,26	kW
$E_{off}(100\%) =$	0,65	mJ
$t_{Eoff} =$	0,62	µs

figure 6. IGBT



$P_{on}(100\%) =$	5,26	kW
$E_{on}(100\%) =$	0,40	mJ
$t_{Eon} =$	0,25	µs

figure 7. FWD



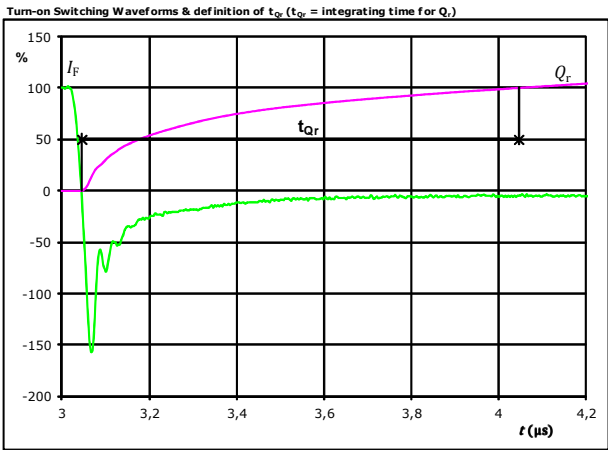
$V_F(100\%) =$	350	V
$I_F(100\%) =$	15	A
$I_{RRM}(100\%) =$	-24	A
$t_{rr} =$	0,309	µs



Vincotech

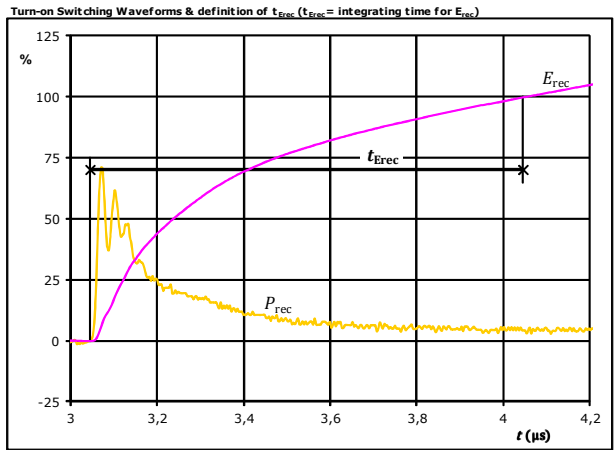
Boost Switching Characteristics

figure 8. FWD



I_F (100%) =	15	A
Q_r (100%) =	2,35	μC
t_{Qr} =	1,00	μs

figure 9. FWD



P_{rec} (100%) =	5,26	kW
E_{rec} (100%) =	0,64	mJ
t_{Erec} =	1,00	μs



Vincotech

Ordering Code & Marking							
Version			Ordering Code				
with thermal paste 12 mm housing with press-fit pins			10-PY12M3A025SH04-M746F43Y-/3/				
NN-NNNNNNNNNNNN TTTTWW WWYY UL VIN LLLL SSSS		Text	Name	Date code	UL & VIN	Lot	Serial
			NN-NNNNNNNNNNNN-TTTTWW	WWYY	UL VIN	LLLL	SSSS
			Datamatrix	Type&Ver	Lot number	Serial	Date code
			TTTTTWW	LLLL	SSSS	WWYY	

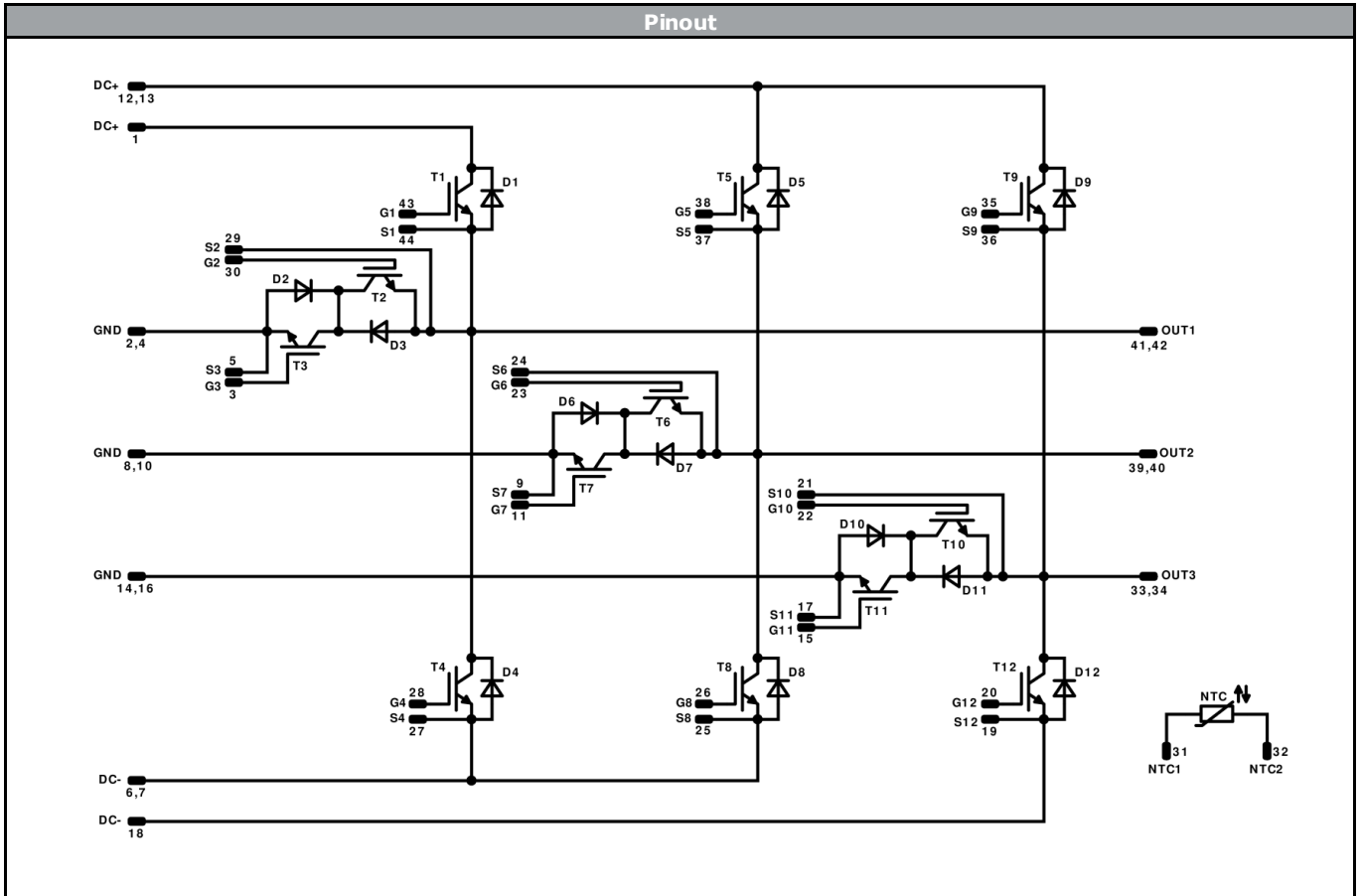
Pin table			
Pin	X	Y	Function
1	52,2	0	+DC
2	46,2	0	GND
3	47	3	G3
4	40,9	0	GND
5	44	3	S3
6	34,9	0	-DC
7	34,9	3	-DC
8	28,9	0	GND
9	25,9	2	S7
10	22,9	0	GND
11	22,9	3	G7
12	16,9	0	+DC
13	16,9	3	+DC
14	10,9	0	GND
15	10,9	3	G11
16	6	0	GND
17	7,9	3	S11
18	0	0	-DC
19	4,75	8,9	S12
20	1,75	7,9	G12
21	13,25	13,7	S10
22	13,25	10,7	G10
23	21,25	10,7	G6
24	21,25	13,7	S6
25	30,4	9,7	S8
26	33,4	9,7	G8
27	40,15	11,2	S4
28	40,15	8,2	G4
29	50,45	10,7	S2
30	50,45	13,7	G2
31	0	16,35	NTC
32	0	19,35	NTC
33	5,45	28,2	OUT3
34	8,25	28,2	OUT3
35	11,25	28,2	G9
36	14,25	28,2	S9
37	23	28,2	S5
38	26	28,2	G5
39	29	28,2	OUT2
40	31,8	28,2	OUT2
41	40,4	28,2	OUT1
42	43,2	28,2	OUT1
43	46,2	28,2	G1
44	49,2	28,2	S1

Outline

Tolerance of pinpositions: ±0,5mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



Vincotech



Identification					
ID	Component	Voltage	Current	Function	Comment
T1, T4, T5, T8, T9, T12	IGBT	1200 V	25 A	Buck Switch	
D2, D3, D6, D7, D10, D11	FWD	650 V	20 A	Buck Diode	
T2, T3, T6, T7, T10, T11	IGBT	650 V	20 A	Boost Switch	
D1, D4, D5, D8, D9, D12	FWD	1200 V	15 A	Boost Diode	
NTC	NTC			Thermistor	




Vincotech

Packaging instruction			
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ Sample

Handling instruction
Handling instructions for <i>flow 1</i> packages see vincotech.com website.

Package data
Package data for <i>flow 1</i> packages see vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website. 

Document No.:	Date:	Modification:	Pages
10-PY12M3A025SH04-M746F43Y-D1-14	21 Sep. 2017		

DISCLAIMER

The information, specifications, procedures, methods and recommendations herein (together "information") are presented by Vincotech to reader in good faith, are believed to be accurate and reliable, but may well be incomplete and/or not applicable to all conditions or situations that may exist or occur. Vincotech reserves the right to make any changes without further notice to any products to improve reliability, function or design. No representation, guarantee or warranty is made to reader as to the accuracy, reliability or completeness of said information or that the application or use of any of the same will avoid hazards, accidents, losses, damages or injury of any kind to persons or property or that the same will not infringe third parties rights or give desired results. It is reader's sole responsibility to test and determine the suitability of the information and the product for reader's intended use.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.