10-P106PPA050SJ-PD54B09Y

datasheet

Vincotech

***flow*PIM 1 + PFC** **600 V / 50 A**

Topology features

- Open Emitter configuration
- Temperature sensor
- On-board Capacitors
- Converter + 2-leg interleaved PFC + Inverter
- 2x Shunts

Component features

- 5us short circuit withstand time
- High speed switching
- Low EMI
- Short tail current

Housing features

- Base isolation: Al_2O_3
- Convex shaped substrate for superior thermal contact
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

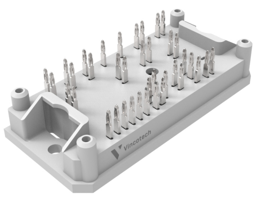
Target applications

- Embedded Drives
- Heat Pumps
- HVAC
- Industrial Drives

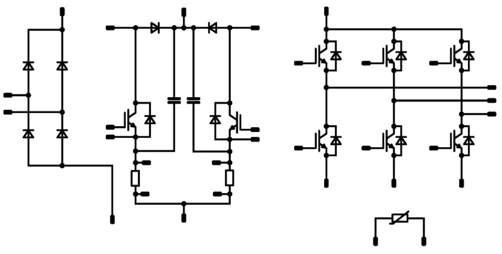
Types

- 10-P106PPA050SJ-PD54B09Y

flow 1 17 mm housing



Schematic



Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Inverter Switch					
Collector-emitter voltage	V_{CES}			600	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	48	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		150	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	79	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 400 V	T_j = 150 °C	5	µs
Maximum junction temperature	T_{jmax}			175	°C
Inverter Diode					
Peak repetitive reverse voltage	V_{RRM}			600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	33	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		60	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	52	W
Maximum junction temperature	T_{jmax}			175	°C
PFC Switch					
Collector-emitter voltage	V_{CES}			650	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	41	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		120	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	73	W
Gate-emitter voltage	V_{GES}			±20	V
Maximum junction temperature	T_{jmax}			175	°C

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
PFC Diode					
Peak repetitive reverse voltage	V_{RRM}			600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	55	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		120	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 25 °C	480	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	76	W
Maximum junction temperature	T_{jmax}			175	°C
PFC Sw. Protection Diode					
Peak repetitive reverse voltage	V_{RRM}			650	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s <= 80 °C	20 [(1)]	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		20	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	33	W
Maximum junction temperature	T_{jmax}			175	°C

[(1)] limited by I_{FRM}

Parameter	Symbol	Conditions		Value	Unit
Rectifier Diode					
Peak repetitive reverse voltage	V_{RRM}			1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	86	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	890	A
Surge current capability	I^2t			3960	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	91	W
Maximum junction temperature	T_{jmax}			150	°C
PFC Shunt					
DC current	I			31,6	A
Power dissipation	P_{tot}		T_c = 70 °C	2	W
Operation Temperature	T_{op}			-65 ... 170	°C

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Capacitor (PFC)					
Maximum DC voltage	V_{MAX}			630	V
Operation Temperature	T_{op}			-55 ... 150	°C
Module Properties					
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	6000	V
Isolation voltage	V_{isol}	AC Voltage	t_p = 1 min	2500	V
Creepage distance				>12.7mm	mm
Clearance				7.82mm	mm
Comparative Tracking Index	CTI			≥ 600	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0008	25	4,1	5,1	5,7	V
Collector-emitter saturation voltage	V_{CEsat}		15		50	25 125 150		1,49 1,61 1,64	1,8[2]	V
Collector-emitter cut-off current	I_{CES}		0	600		25			2,8	µA
Gate-emitter leakage current	I_{GES}		20	0		25			100	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		1950		pF
Output capacitance	C_{oes}							83		pF
Reverse transfer capacitance	C_{res}							67		pF
Gate charge	Q_g	V_{CC} = 480 V	15		50	25		249		nC
Thermal										
Thermal resistance junction to sink[3]	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,2		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 8 Ω R_{goff} = 8 Ω	±15	350	50	25 125 150		70 70 71,2		ns
Rise time	t_r					25 125 150		45,2 43,2 42,8		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		114,8 133,6 138,6		ns
Fall time	t_f					25 125 150		22,47 34,2 41,12		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=1,62 µC Q_{rFWD}=3,09 µC Q_{rFWD}=3,57 µC				25 125 150		1,84 2,2 2,28		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,536 0,839 0,941		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Diode										
Static										
Forward voltage	V_F				30	25 150	1,25	1,64 1,55	1,95(2)	V
Reverse leakage current	I_R	V_r = 600 V				25			27	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,81		K/W
Dynamic										
Peak recovery current	I_{RM}	d*i*/d*t*=245 A/µs d*i*/d*t*=545 A/µs d*i*/d*t*=378 A/µs	±15	350	50	25 125 150		10,63 16,09 16,77		A
Reverse recovery time	t_{rr}					25 125 150		251,47 331,66 392,82		ns
Recovered charge	Q_r					25 125 150		1,62 3,09 3,57		µC
Reverse recovered energy	E_{rec}					25 125 150		0,406 0,762 0,892		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		76,03 88,46 100,72		A/µs

Characteristic Values

Parameter	Symbol	Conditions	V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	Unit
PFC Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$			0,0004	25	3,3	4	4,7	V
Collector-emitter saturation voltage	V_{CEsat}		15		40	25 125 150		1,54 1,69 1,74	2,22(2)	V
Collector-emitter cut-off current	I_{CES}		0	650		25			80	µA
Gate-emitter leakage current	I_{GES}		20	0		25			240	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	f = 1 Mhz	0	25		25		2400		pF
Output capacitance	C_{oes}							60		pF
Reverse transfer capacitance	C_{res}							10		pF
Gate charge	Q_g	V_{CC} = 520 V	15		40	25		96		nC
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,3		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 4 Ω R_{goff} = 4 Ω	0/15	400	50	25 125 150		14,85 14,55 14,29		ns
Rise time	t_r					25 125 150		8,26 9,49 9,93		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		68,26 81,94 85,21		ns
Fall time	t_f					25 125 150		4,89 11,54 16,52		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=0,759 µC Q_{rFWD}=2,06 µC Q_{rFWD}=2,6 µC				25 125 150		0,434 0,676 0,765		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,307 0,471 0,527		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
PFC Diode										
Static										
Forward voltage	V_F				60	25 125 150		1,89 1,57 1,5	2,5(2)	V
Reverse leakage current	I_R	V_r = 600 V				25			25	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,25		K/W
Dynamic										
Peak recovery current	I_{RM}	di/dt=4573 A/µs di/dt=4683 A/µs di/dt=4623 A/µs	0/15	400	50	25 125 150		57,46 98,57 112,75		A
Reverse recovery time	t_{rr}					25 125 150		24,63 35 38,46		ns
Recovered charge	Q_r					25 125 150		0,759 2,06 2,6		µC
Reverse recovered energy	E_{rec}					25 125 150		0,165 0,514 0,659		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		5595,29 6038,91 6707,51		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
PFC Sw. Protection Diode										
Static										
Forward voltage	V_F				10	25 125	1,23	1,67 1,54	1,87(2)	V
Reverse leakage current	I_R	V_r = 650 V				25			0,14	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						2,87		K/W
Rectifier Diode										
Static										
Forward voltage	V_F				60	25 125 150		1,04 0,973 0,956	1,5(2)	V
Reverse leakage current	I_R	V_r = 1600 V				25 150			100 2	µA
Thermal										
Thermal resistance junction to sink(3)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,77		K/W
PFC Shunt										
Static										
Resistance	R							2		mΩ
Temperature coeficient	tc								275	ppm/K

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Capacitor (PFC)										
Static										
Capacitance	C	DC bias voltage = 0 V				25		33		nF
Tolerance							-5		5	%
Thermistor										
Static										
Rated resistance	R					25		22		kΩ
Deviation of R100	$\Delta_{R/R}$	R_{100} = 1484 Ω				100	-5		5	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,5		mW/K
B-value	$B_{(25/50)}$	Tol. ±1 %						3962		K
B-value	$B_{(25/100)}$	Tol. ±1 %						4000		K
Vincotech Thermistor Reference									I	

(2) Value at chip level

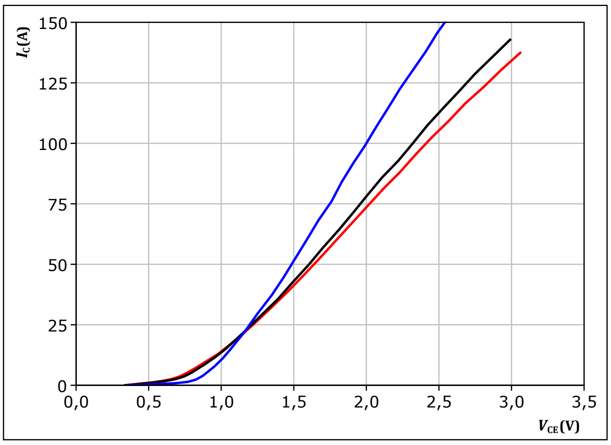
(3) Only valid with pre-applied Vincotech thermal interface material.

Inverter Switch Characteristics

figure 1. IGBT

Typical output characteristics

I_C = f(V_{CE})

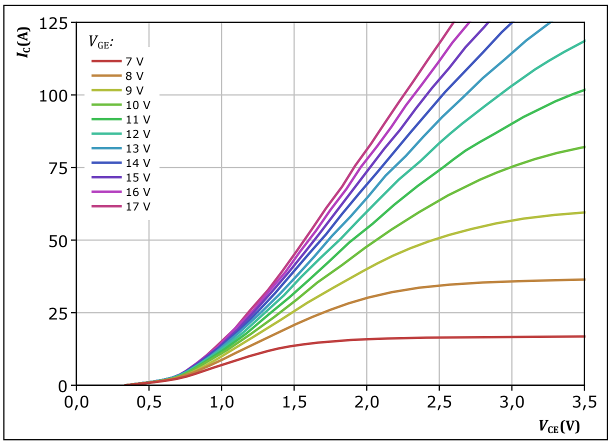


t_p =	250	µs		25 °C
V_{GE} =	15	V	T_j:	125 °C
				150 °C

figure 2. IGBT

Typical output characteristics

I_C = f(V_{CE})



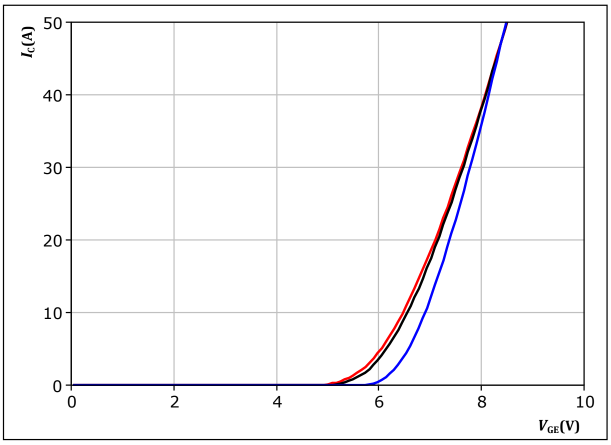
t_p =	250	µs
T_j =	150	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

I_C = f(V_{GE})

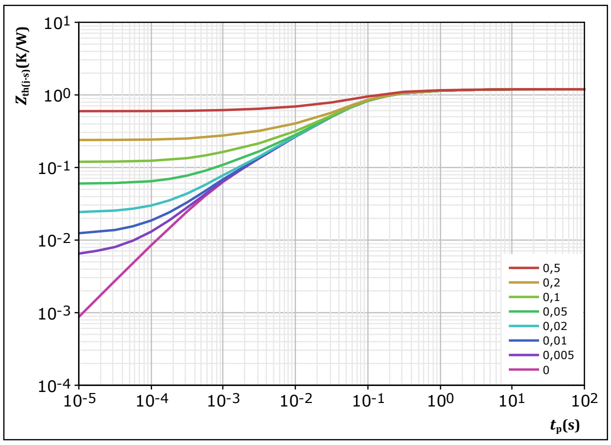


t_p =	250	µs		25 °C
V_{CE} =	10	V	T_j:	125 °C
				150 °C

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D =	t_p / T	
$R_{th(j-s)}$ =	1,195	K/W

IGBT thermal model values

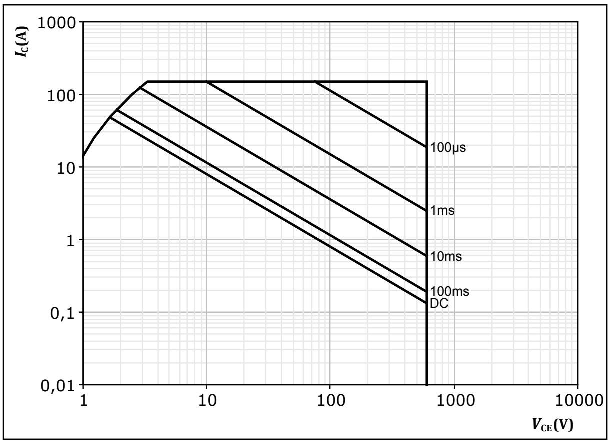
R (K/W)	τ (s)
7,02E-02	2,29E+00
1,93E-01	3,20E-01
7,24E-01	6,54E-02
1,54E-01	8,99E-03
5,44E-02	9,23E-04

Inverter Switch Characteristics

figure 5.	IGBT

Safe operating area

I_C = f(V_{CE})



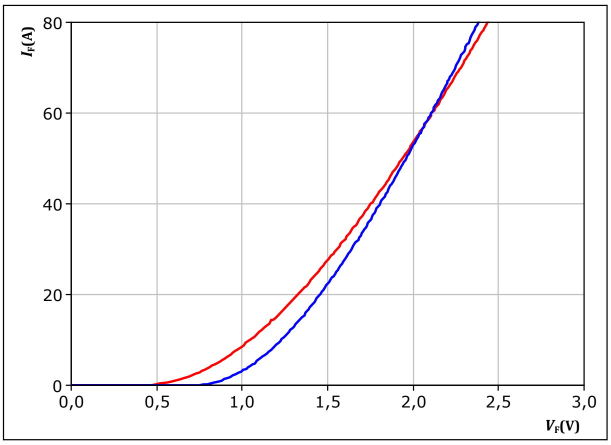
D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Inverter Diode Characteristics

figure 6. **FWD**

Typical forward characteristics

$I_F = f(V_F)$



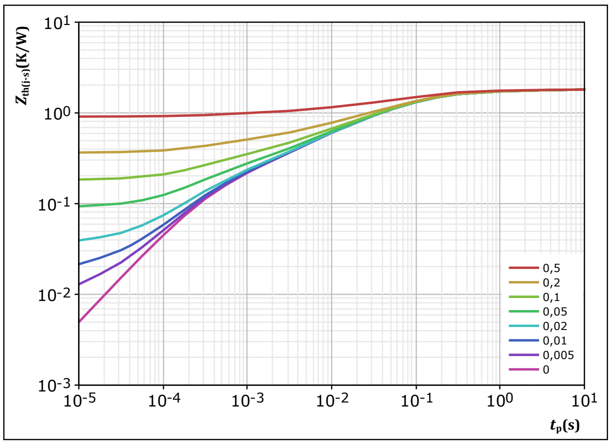
t_p = 250 µs

T_j: 25 °C, 150 °C

figure 7. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)} = f(t_p)$



D = t_p / T

$R_{th(j-s)}$ = 1,811 K/W

FWD thermal model values

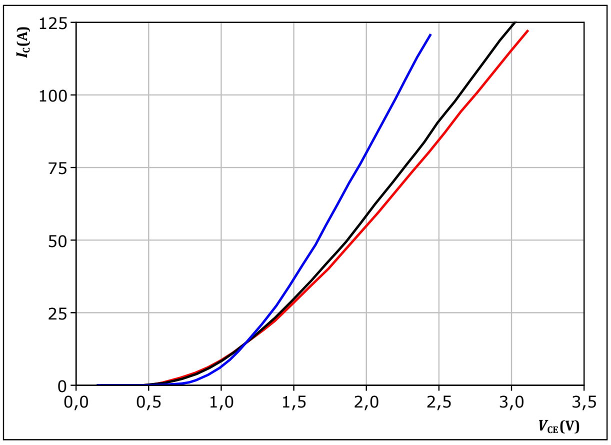
R (K/W)	τ (s)
8,35E-02	4,59E+00
2,01E-01	4,81E-01
7,60E-01	9,25E-02
4,22E-01	1,80E-02
2,13E-01	3,31E-03
1,40E-01	3,46E-04

PFC Switch Characteristics

figure 8. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



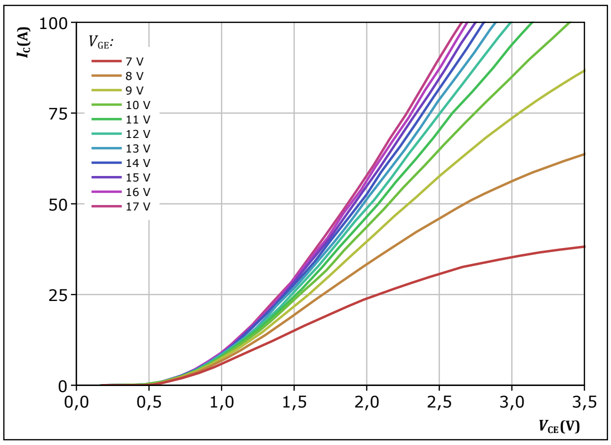
t_p =	250	µs
V_{GE} =	15	V

T_j: 25 °C, 125 °C, 150 °C

figure 9. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



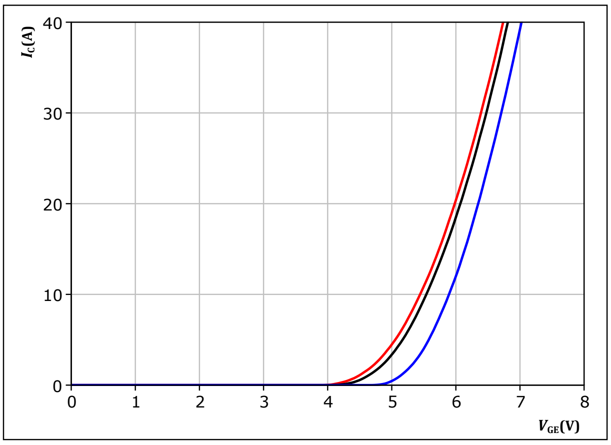
t_p =	250	µs
T_j =	150	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 10. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})



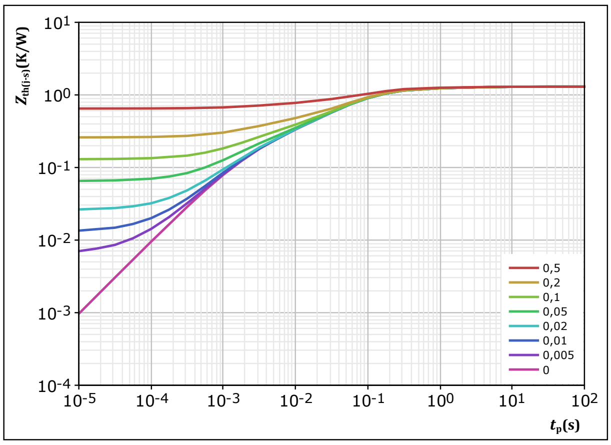
t_p =	250	µs
V_{CE} =	10	V

T_j: 25 °C, 125 °C, 150 °C

figure 11. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j\text{-}s)}$ = f(t_p)



D =	t_p / T	
$R_{th(j\text{-}s)}$ =	1,299	K/W

IGBT thermal model values

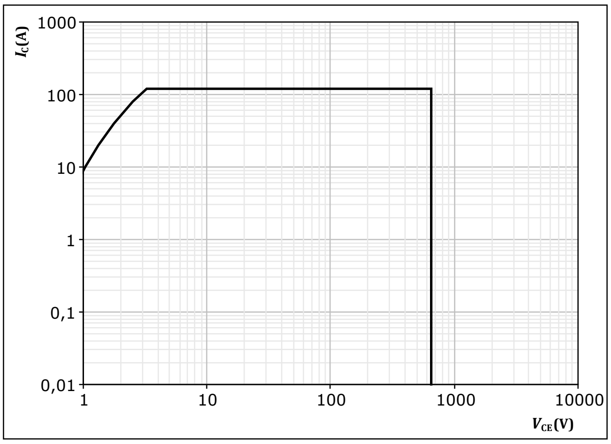
R (K/W)	τ (s)
7,67E-02	2,84E+00
1,78E-01	3,36E-01
7,37E-01	7,35E-02
1,98E-01	9,72E-03
1,09E-01	1,65E-03

PFC Switch Characteristics

figure 12. **IGBT**

Safe operating area

I_C = f(V_{CE})

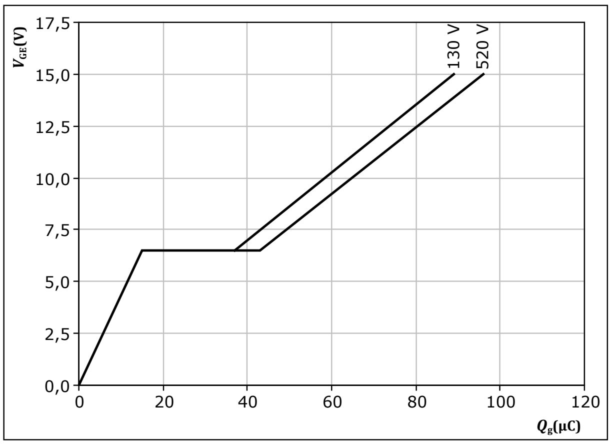


D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

figure 13. **IGBT**

Gate voltage vs gate charge

V_{GE} = f(Q_g)



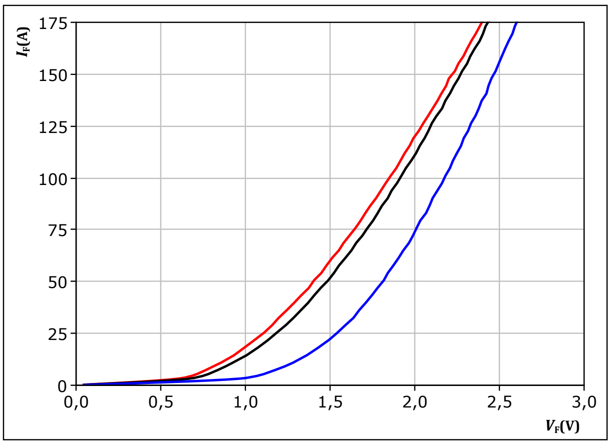
I_C =	20	A
T_j =	25	°C

PFC Diode Characteristics

figure 14. FWD

Typical forward characteristics

I_F = f(V_F)



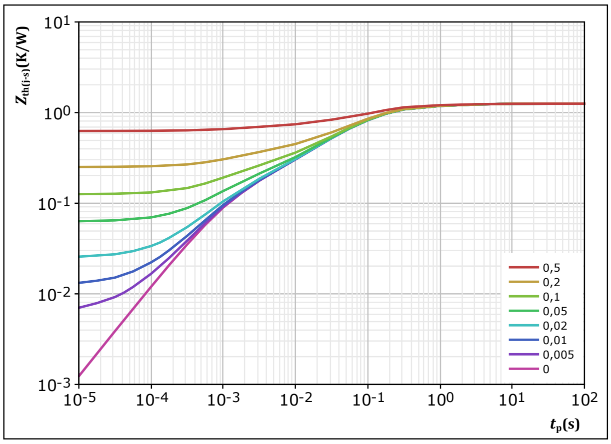
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 15. FWD

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,254 K/W

FWD thermal model values

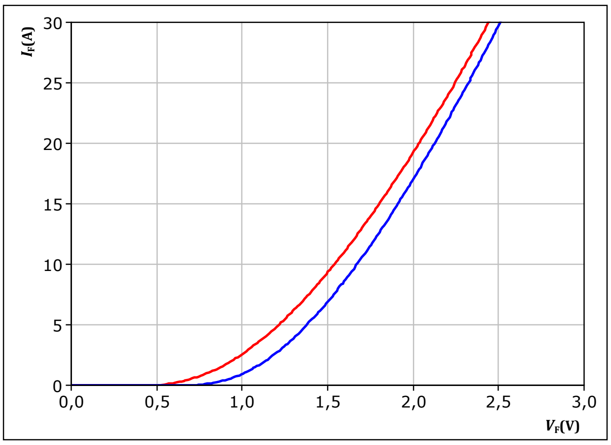
R (K/W)	τ (s)
6,51E-02	3,13E+00
1,82E-01	4,53E-01
6,95E-01	8,72E-02
2,01E-01	1,20E-02
1,12E-01	1,13E-03

PFC Sw. Protection Diode Characteristics

figure 16. **FWD**

Typical forward characteristics

I_F = f(V_F)



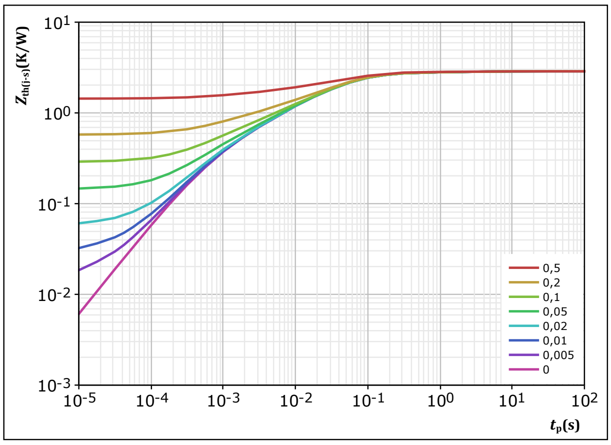
t_p = 250 µs

T_j: 25 °C, 125 °C

figure 17. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 2,873 K/W

FWD thermal model values

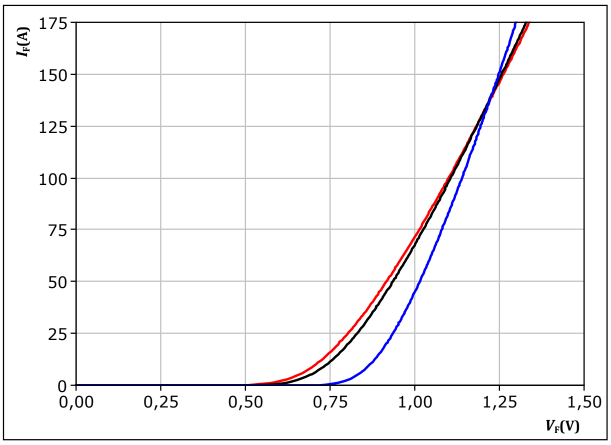
R (K/W)	τ (s)
6,53E-02	3,94E+00
1,48E-01	4,48E-01
1,31E+00	5,96E-02
7,32E-01	1,36E-02
4,04E-01	2,79E-03
2,11E-01	5,37E-04

Rectifier Diode Characteristics

figure 18. **Rectifier**

Typical forward characteristics

I_F = f(V_F)



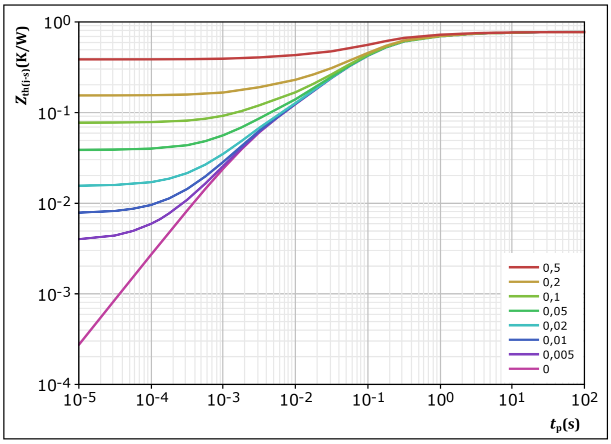
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 19. **Rectifier**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 0,772 K/W

Rectifier thermal model values

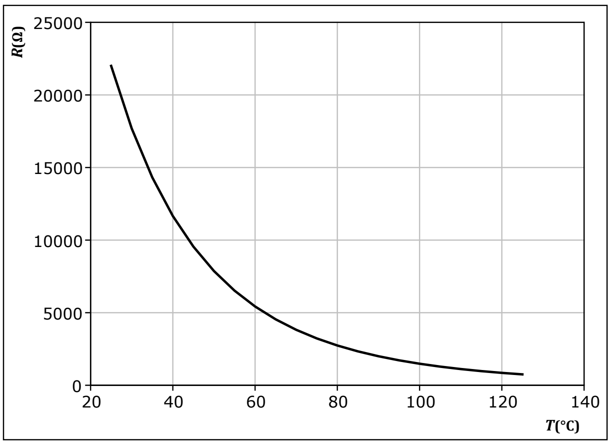
R (K/W)	τ (s)
2,82E-02	8,69E+00
1,16E-01	1,22E+00
4,16E-01	1,44E-01
1,62E-01	2,97E-02
5,02E-02	2,64E-03

Thermistor Characteristics

figure 20. **Thermistor**

Typical NTC characteristic as function of temperature

R_T = f(T)

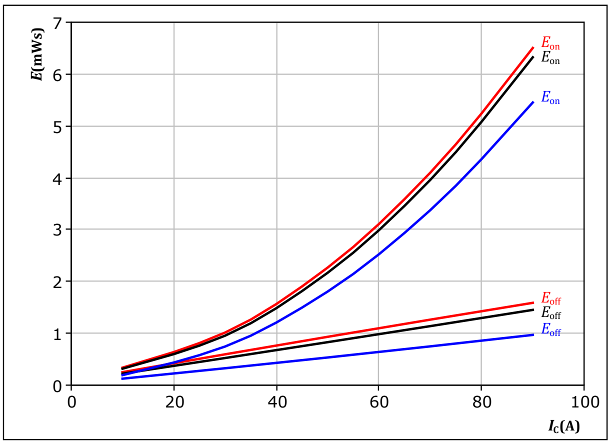


Inverter Switching Characteristics

figure 21. **IGBT**

Typical switching energy losses as a function of collector current

$E = f(I_C)$



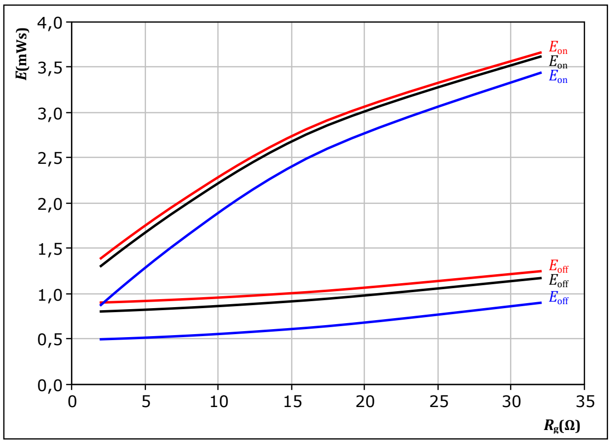
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	8	Ω		150 °C
R_{goff} =	8	Ω		

figure 22. **IGBT**

Typical switching energy losses as a function of IGBT turn on gate resistor

$E = f(R_g)$



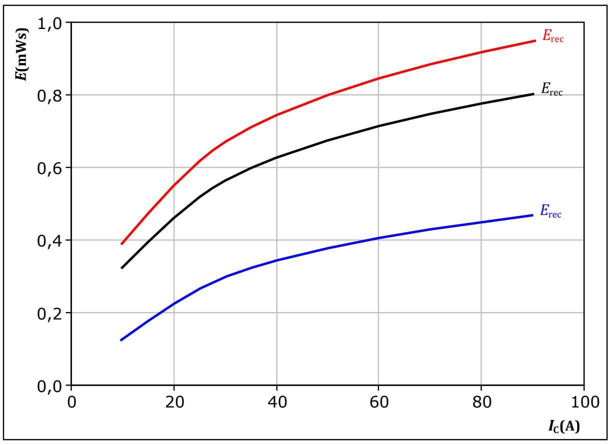
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	50	A		150 °C

figure 23. **FWD**

Typical reverse recovered energy loss as a function of collector current

$E_{rec} = f(I_C)$



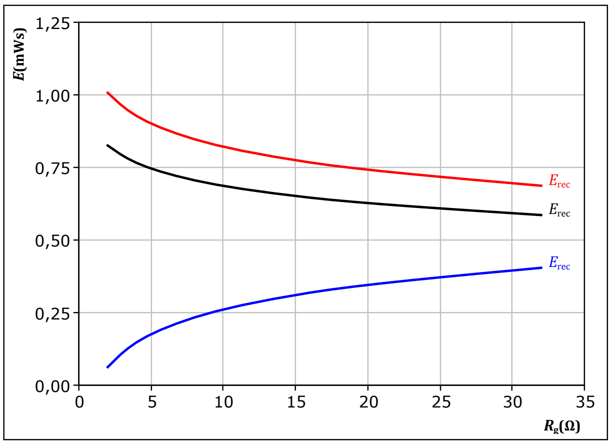
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 24. **FWD**

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

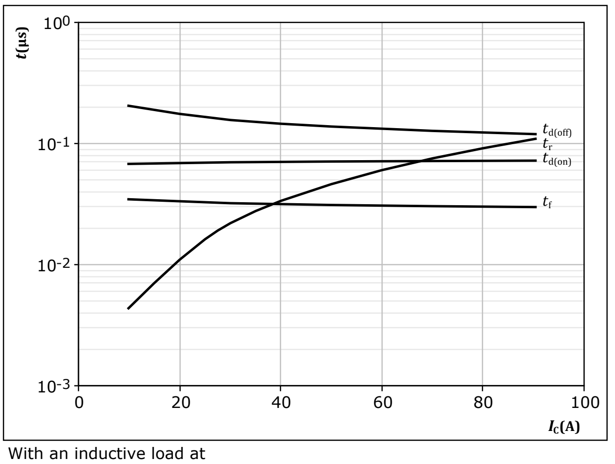
V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	50	A		150 °C

Inverter Switching Characteristics

figure 25. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



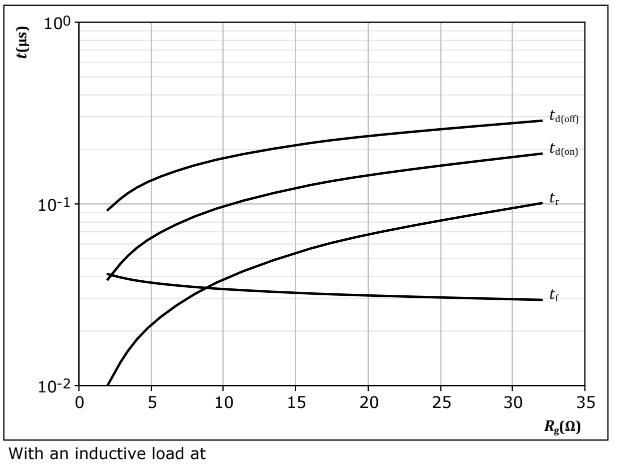
With an inductive load at

T_j =	150	°C
V_{CE} =	350	V
V_{GE} =	±15	V
R_{gon} =	8	Ω
R_{goff} =	8	Ω

figure 26. **IGBT**

Typical switching times as a function of IGBT turn on gate resistor

t = f(R_g)



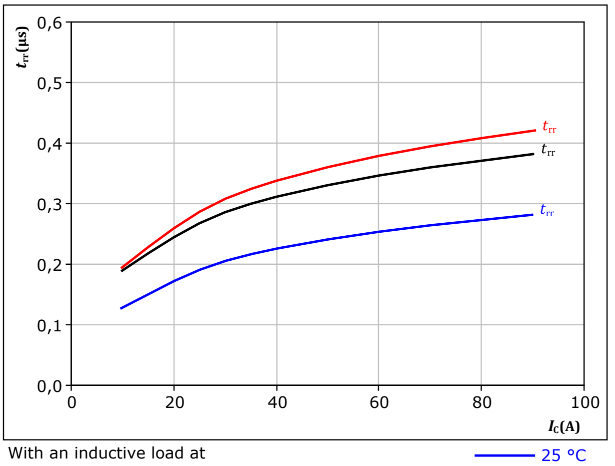
With an inductive load at

T_j =	150	°C
V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	50	A

figure 27. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



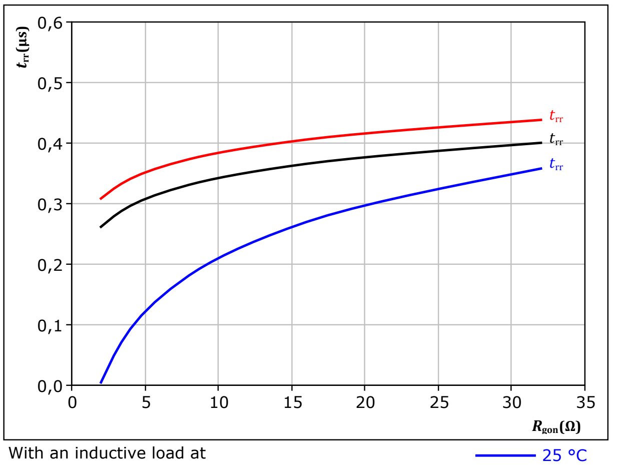
With an inductive load at

V_{CE} =	350	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

figure 28. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



With an inductive load at

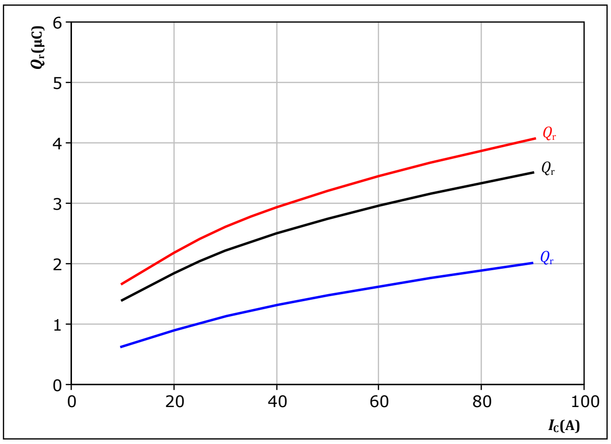
V_{CE} =	350	V
V_{GE} =	±15	V
I_C =	50	A

Inverter Switching Characteristics

figure 29.	**FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



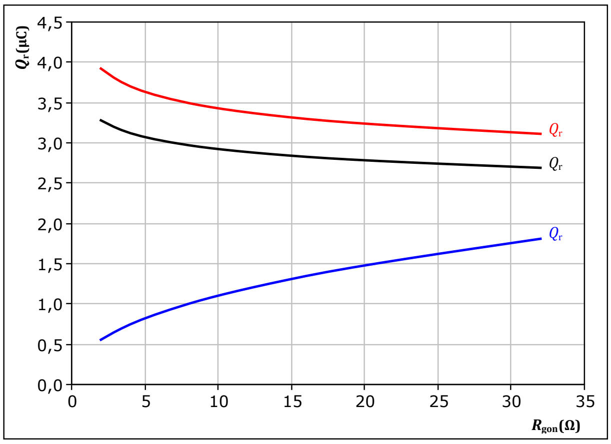
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 30.	**FWD**

Typical recovered charge as a function of IGBT turn on gate resistor

Q_r = f(R_{gon})



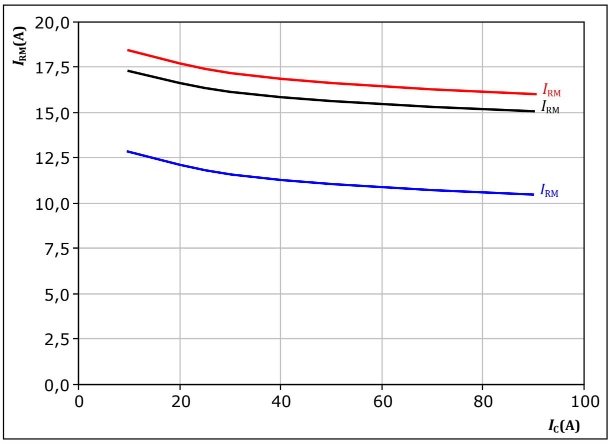
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	50	A		150 °C

figure 31.	**FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



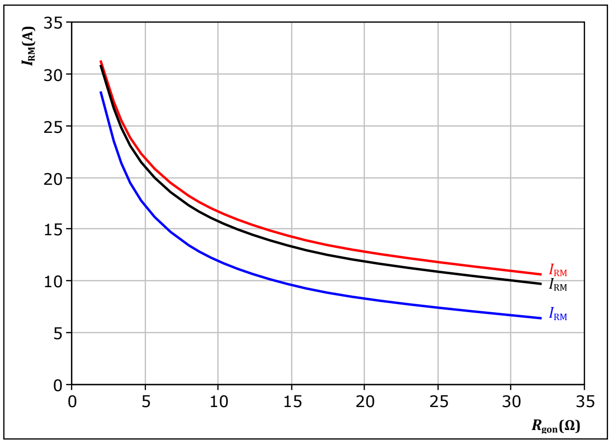
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 32.	**FWD**

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

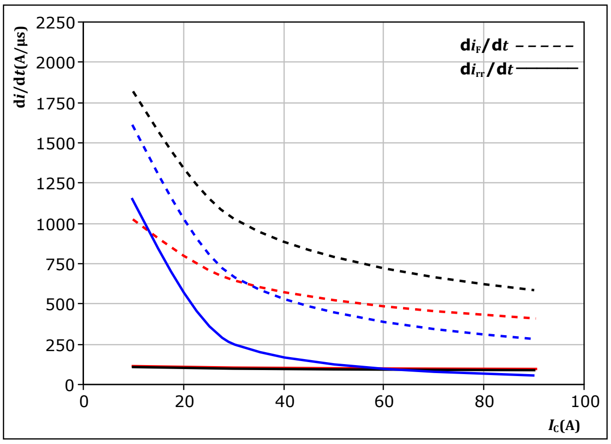
V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	50	A		150 °C

Inverter Switching Characteristics

figure 33. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



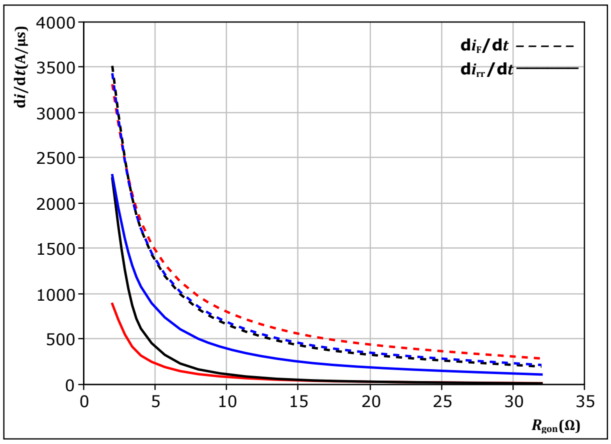
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 34. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

di_F/dt, di_{rr}/dt = f(R_{gon})



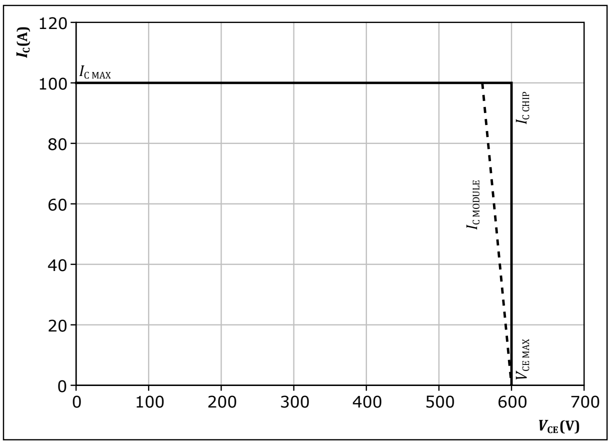
With an inductive load at

V_{CE} =	350	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	50	A		150 °C

figure 35. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



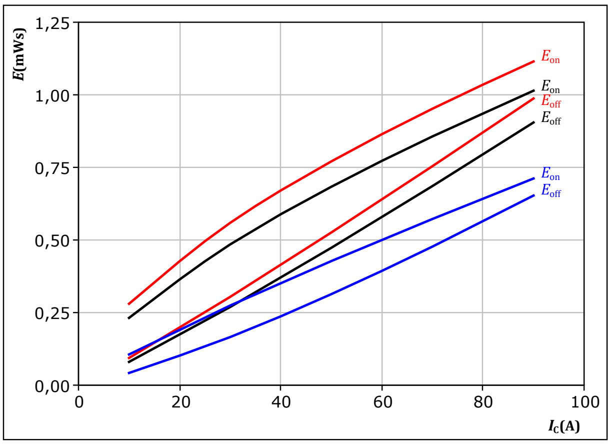
At	T_j =	150	°C
	R_{gon} =	8	Ω
	R_{goff} =	8	Ω

PFC Switching Characteristics

figure 36. **IGBT**

Typical switching energy losses as a function of collector current

$E = f(I_C)$



With an inductive load at

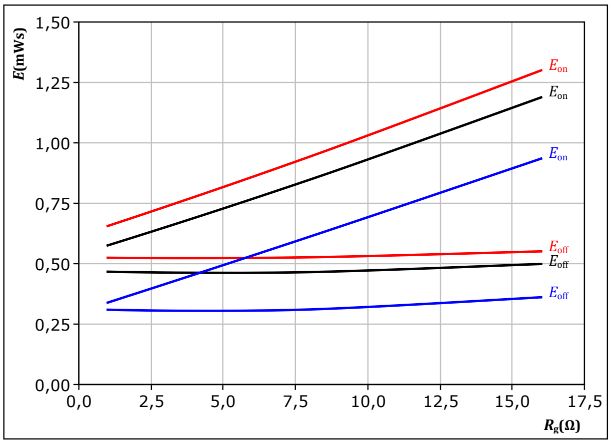
V_{CE} =	400	V	
V_{GE} =	0/15	V	
R_{gon} =	4	Ω	
R_{goff} =	4	Ω	

T_j: 25 °C, 125 °C, 150 °C

figure 37. **IGBT**

Typical switching energy losses as a function of IGBT turn on gate resistor

$E = f(R_g)$



With an inductive load at

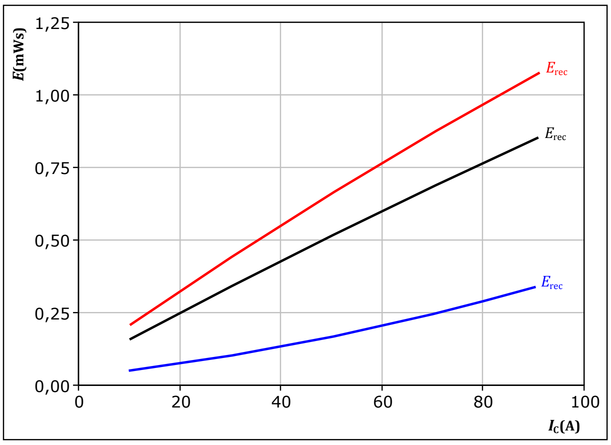
V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	50	A

T_j: 25 °C, 125 °C, 150 °C

figure 38. **FWD**

Typical reverse recovered energy loss as a function of collector current

$E_{rec} = f(I_C)$



With an inductive load at

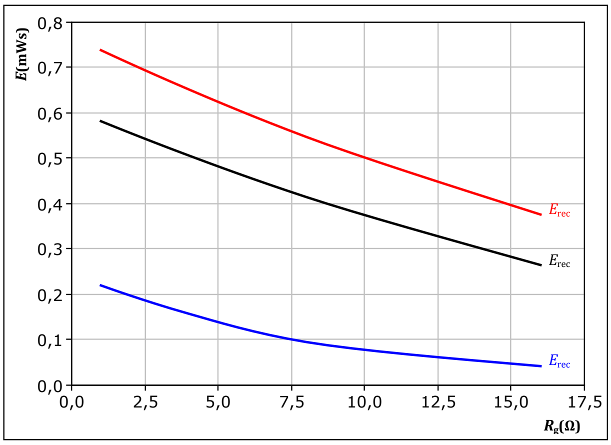
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	4	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 39. **FWD**

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	50	A

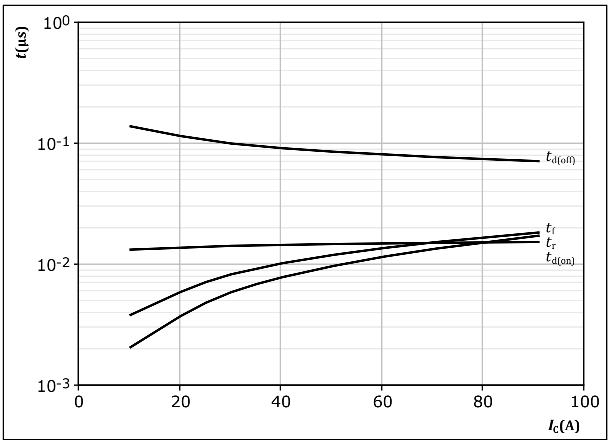
T_j: 25 °C, 125 °C, 150 °C

PFC Switching Characteristics

figure 40. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



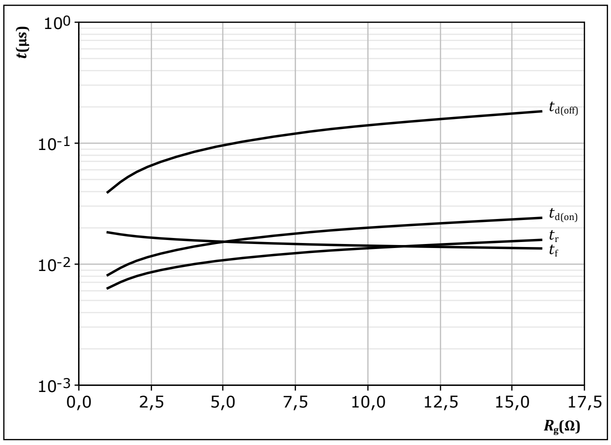
With an inductive load at

T_j =	150	°C
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	4	Ω
R_{goff} =	4	Ω

figure 41. **IGBT**

Typical switching times as a function of IGBT turn on gate resistor

t = f(R_g)



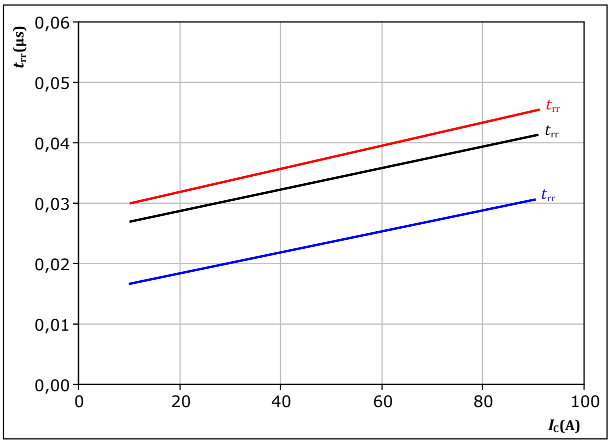
With an inductive load at

T_j =	150	°C
V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	50	A

figure 42. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

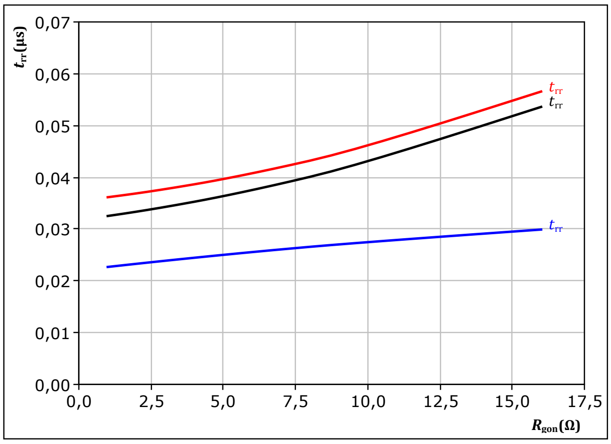
V_{CE} =	400	V
V_{GE} =	0/15	V
R_{gon} =	4	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 43. **FWD**

Typical reverse recovery time as a function of IGBT turn on gate resistor

t_{rr} = f(R_{gon})



With an inductive load at

V_{CE} =	400	V
V_{GE} =	0/15	V
I_C =	50	A

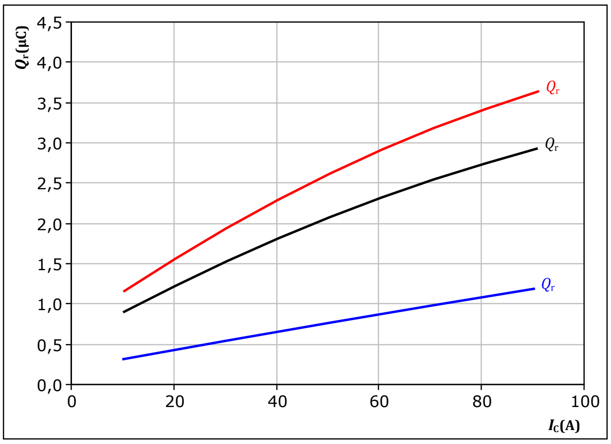
T_j: 25 °C, 125 °C, 150 °C

PFC Switching Characteristics

figure 44. **FWD**

Typical recovered charge as a function of collector current

Q_r = f(I_C)



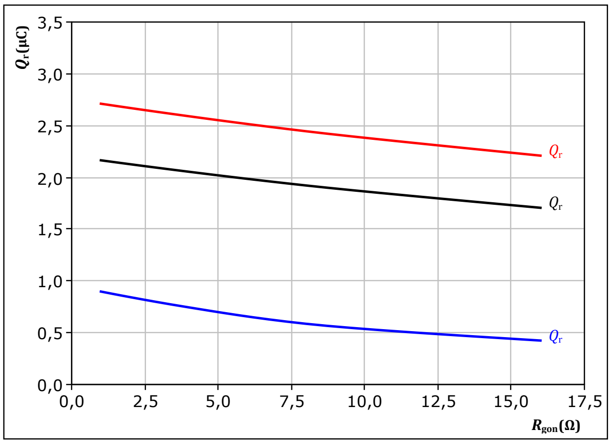
With an inductive load at

V_{CE} =	400	V	T_j: 25 °C
V_{GE} =	0/15	V	125 °C
R_{gon} =	4	Ω	150 °C

figure 45. **FWD**

Typical recovered charge as a function of IGBT turn on gate resistor

Q_r = f(R_{gon})



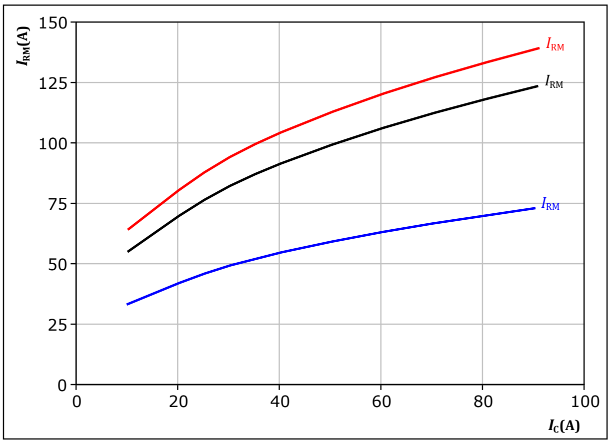
With an inductive load at

V_{CE} =	400	V	T_j: 25 °C
V_{GE} =	0/15	V	125 °C
I_C =	50	A	150 °C

figure 46. **FWD**

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



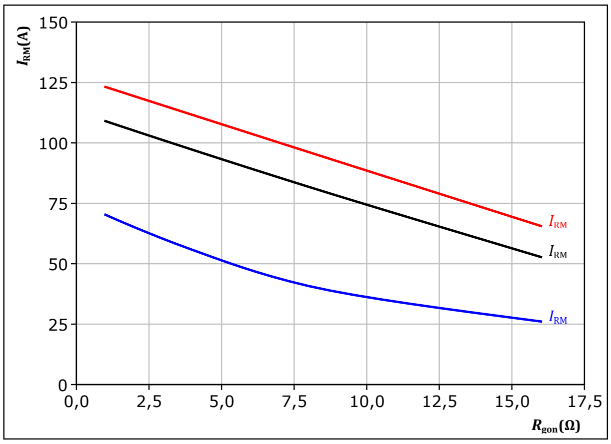
With an inductive load at

V_{CE} =	400	V	T_j: 25 °C
V_{GE} =	0/15	V	125 °C
R_{gon} =	4	Ω	150 °C

figure 47. **FWD**

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

I_{RM} = f(R_{gon})



With an inductive load at

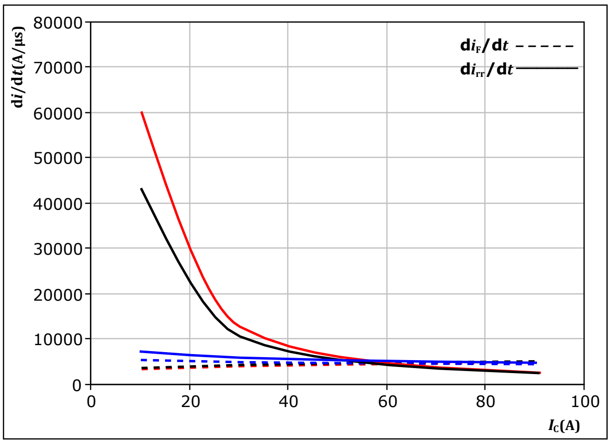
V_{CE} =	400	V	T_j: 25 °C
V_{GE} =	0/15	V	125 °C
I_C =	50	A	150 °C

PFC Switching Characteristics

figure 48. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



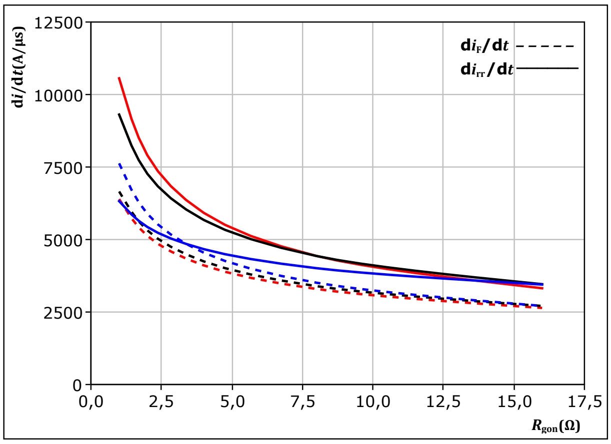
With an inductive load at

V_{CE} =	400	V		25 °C
V_{GE} =	0/15	V	T_j:	125 °C
R_{gon} =	4	Ω		150 °C

figure 49. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor

di_F/dt, di_{rr}/dt = f(R_{gon})



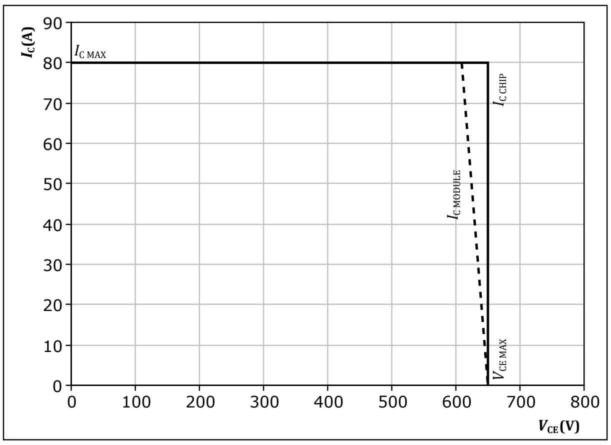
With an inductive load at

V_{CE} =	400	V		25 °C
V_{GE} =	0/15	V	T_j:	125 °C
I_C =	50	A		150 °C

figure 50. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



At	T_j =	150	°C
	R_{gon} =	4	Ω
	R_{goff} =	4	Ω

Switching Definitions

figure 51. **IGBT**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{tEoff} **= integrating time for** E_{off})

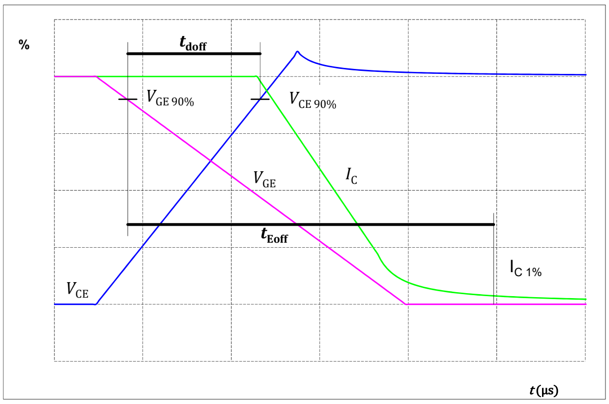


figure 52. **IGBT**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} **= integrating time for** E_{on})

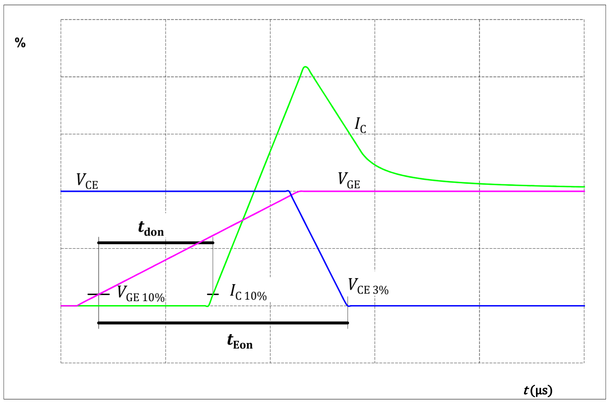


figure 53. **IGBT**

Turn-off Switching Waveforms & definition of t_f

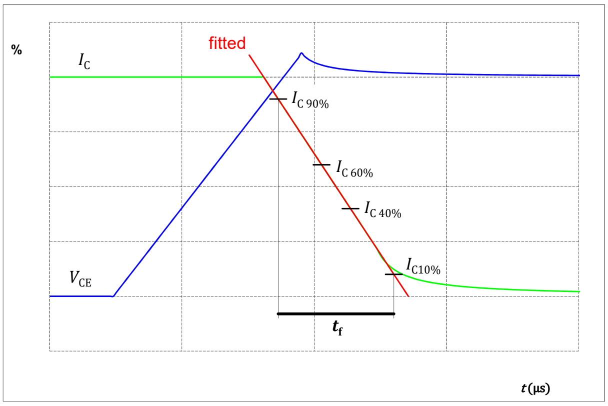
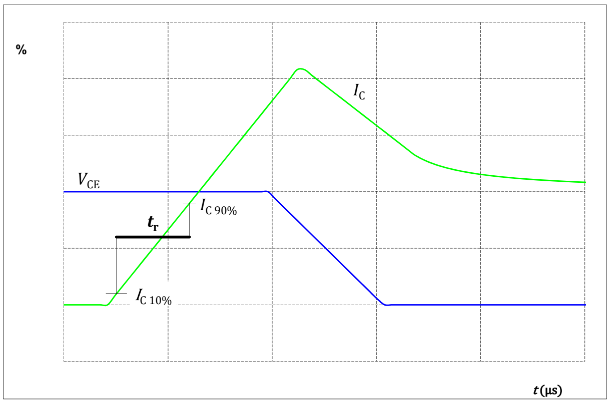


figure 54. **IGBT**

Turn-on Switching Waveforms & definition of t_r



Switching Definitions

figure 55. **FWD**

Turn-off Switching Waveforms & definition of t_{rr}

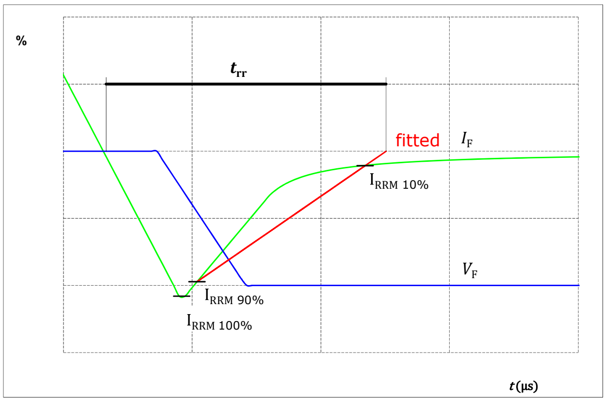
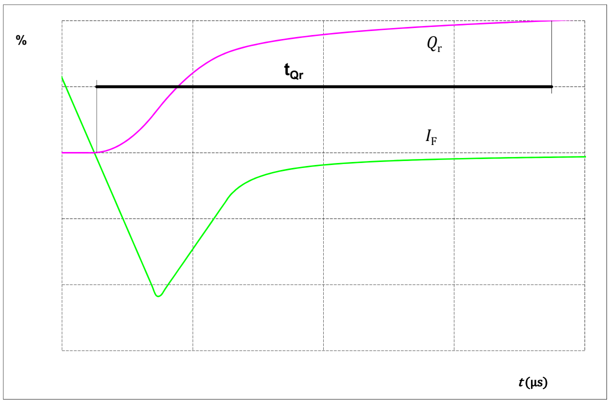


figure 56. **FWD**

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



Ordering Code

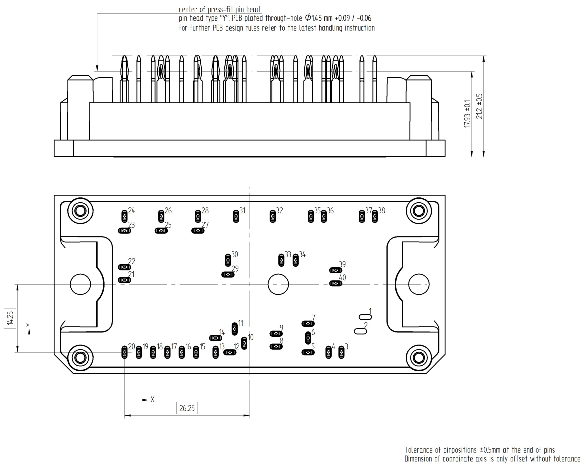
Version	Ordering Code
Without thermal paste	10-P106PPA050SJ-PD54B09Y
With thermal paste (5,2 W/mK, PTM6000HV)	10-P106PPA050SJ-PD54B09Y-/7/
With thermal paste (3,4 W/mK, PSX-P7)	10-P106PPA050SJ-PD54B09Y-/3/

Marking

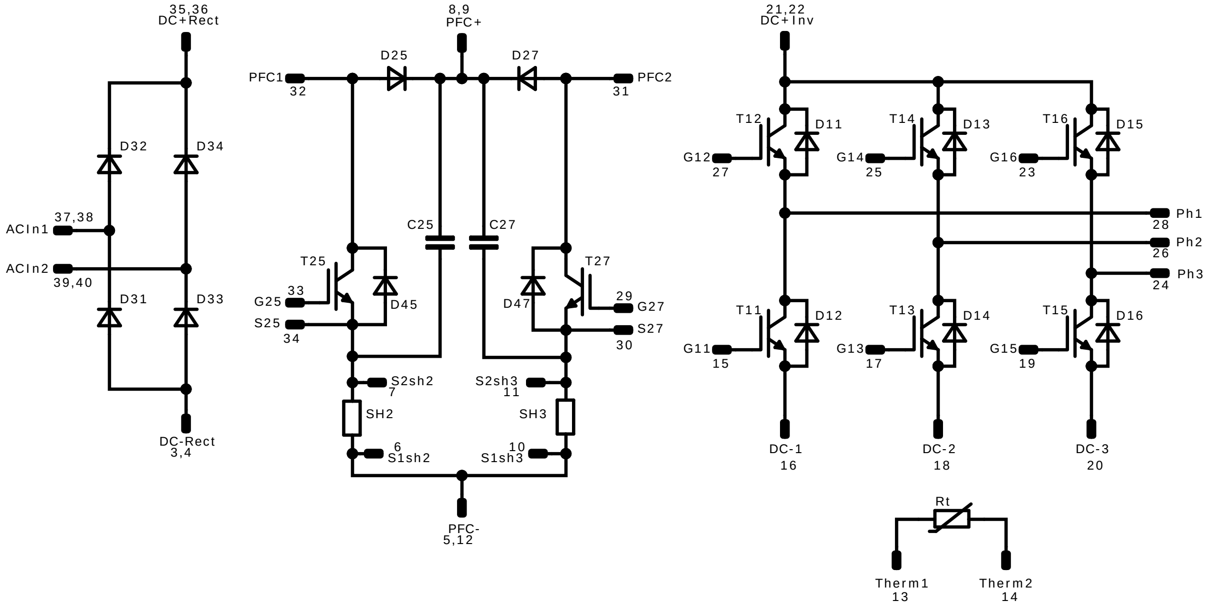
Text	Name		Date code	UL & VIN	Lot	Serial
	NN-NNNNNNNNNNNNNN-TTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	Type&Ver	Lot number	Serial	Date code		
	TTTTTTTVV	LLLLL	SSSS	WWYY		

Outline

Pin table [mm]			
Pin	X	Y	Function
1	not assembled		
2	not assembled		
3	45,5	0	DC-Rect
4	42,8	0	DC-Rect
5	38,5	0	PFC-
6	38,5	3	S1sh2
7	38,5	6	S2sh2
8	31,8	1,2	PFC+
9	31,8	3,9	PFC+
10	25,1	1,9	S1sh3
11	23,1	4,9	S2sh3
12	22,1	0	PFC-
13	19,1	0	Therm1
14	19,1	3	Therm2
15	15	0	G11
16	12	0	DC-1
17	9	0	G13
18	6	0	DC-2
19	3	0	G15
20	0	0	DC-3
21	0	15,15	DC+Inv
22	0	17,85	DC+Inv
23	0	25,5	G16
24	0	28,5	Ph3
25	7,7	25,5	G14
26	7,7	28,5	Ph2
27	15,4	25,5	G12
28	15,4	28,5	Ph1
29	21,7	16,3	G27
30	21,7	19,3	S27
31	23,4	28,5	PFC2
32	31,1	28,5	PFC1
33	32,9	19,3	G25
34	35,9	19,3	S25
35	39,1	28,5	DC+Rect
36	41,8	28,5	DC+Rect
37	49,8	28,5	ACIn1
38	52,5	28,5	ACIn1
39	44,3	17,2	ACIn2
40	44,3	14,45	ACIn2



Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	600 V	50 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	600 V	30 A	Inverter Diode	
T25, T27	IGBT	650 V	40 A	PFC Switch	
D25, D27	FWD	600 V	60 A	PFC Diode	
D45, D47	FWD	650 V	10 A	PFC Sw. Protection Diode	
D31, D32, D33, D34	Rectifier	1600 V	60 A	Rectifier Diode	
SH2, SH3	Shunt			PFC Shunt	
C25, C27	Capacitor	630 V		Capacitor (PFC)	
Rt	Thermistor			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow* 1 packages see vincotech.com website.

Package data
Package data for *flow* 1 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
10-P106PPA050SJ-PD54B09Y-D3-14	2 May. 2023	PFC Switch changed	