



10-FZ12PMA010M7-P849A28
10-F012PMA010M7-P849A29
datasheet

Vincotech

flow PIM 0		1200 V / 10 A
Features		
	<ul style="list-style-type: none">• IGBT M7 with low V_{CEsat} and improved EMC behavior• Open emitter configuration• Compact and low inductive design• Built-in NTC	
Target applications		Schematic
	<ul style="list-style-type: none">• Industrial Drives	
Types		
	<ul style="list-style-type: none">• 10-FZ12PMA010M7-P849A28• 10-F012PMA010M7-P849A29	

Maximum Ratings

$T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Rectifier Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		1600	V
Continuous (direct) forward current	I_F		25	A
Surge (non-repetitive) forward current	I_{FSM}	50 Hz Single Half Sine Wave $t_p = 10 \text{ ms}$	200	A
Surge current capability	I_{Ft}	$T_j = 150^\circ\text{C}$	200	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	44	W
Maximum Junction Temperature	T_{jmax}		150	$^\circ\text{C}$



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Maximum Ratings

$T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current	I_C		10	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	20	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	55	W
Gate-emitter voltage	V_{GES}		± 20	V
Maximum junction temperature	T_{jmax}		175	$^\circ\text{C}$
Inverter Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Continuous (direct) forward current	I_F		10	A
Repetitive peak forward current	I_{FRM}	T_j limited by T_{jmax}	20	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	50	W
Maximum junction temperature	T_{jmax}		175	$^\circ\text{C}$
Brake Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current	I_C		5	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	10	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	41	W
Gate-emitter voltage	V_{GES}		± 20	V
Maximum junction temperature	T_{jmax}		175	$^\circ\text{C}$
Brake Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Continuous (direct) forward current	I_F		5	A
Repetitive peak forward current	I_{FRM}	T_j limited by T_{jmax}	10	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80^\circ\text{C}$	27	W
Maximum junction temperature	T_{jmax}		175	$^\circ\text{C}$



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Maximum Ratings

$T_j = 25 \text{ } ^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
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Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...($T_{\text{jmax}} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage*	$t_p = 2 \text{ s}$	6000	V
		AC Voltage	$t_p = 1 \text{ min}$	2500	V
Creepage distance				min. 12,7	mm
Clearance		12 mm housing / 17 mm housing		9,29 / min. 12,7	mm
Comparative Tracking Index	CTI			> 200	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions						Value			Unit
		V_{GE} [V]	V_{GS} [V]	V_{CE} [V]	V_{DS} [V]	I_c [A]	I_D [A]	T_j [°C]	Min	Typ	Max

Rectifier Diode

Static

Forward voltage	V_F				30	25 125		1,22 1,21	1,8	V
Reverse leakage current	I_r			1600		25 145			50 1100	μA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						1,59		K/W
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Characteristic Values

Parameter	Symbol	Conditions						Value			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_c [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max			

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE} = V_{CE}$			0,001	25	5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CESat}		15		10	125 150		1,66 1,90 1,96	1,95	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			55	µA
Gate-emitter leakage current	I_{GES}		20	0		25			500	nA
Internal gate resistance	r_g						none			Ω
Input capacitance	C_{ies}		0	10	25		2000			pF
Output capacitance	C_{oes}						86			
Reverse transfer capacitance	C_{res}						23			
Gate charge	Q_g		15	600	10	25		80		nC

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						1,72		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{goff} = 32 \Omega$ $R_{gon} = 32 \Omega$	± 15	600	10	25		128		ns
Rise time	t_r					125		126		
Turn-off delay time	$t_{d(off)}$					150		123		
Fall time	t_f	$Q_{fFWD} = 1,1 \mu\text{C}$ $Q_{fFWD} = 1,7 \mu\text{C}$ $Q_{fFWD} = 1,8 \mu\text{C}$	± 15	600	10	25		29		mWs
Turn-on energy (per pulse)	E_{on}					125		32		
Turn-off energy (per pulse)	E_{off}					150		34		
						25		145		
						125		179		
						150		182		
						25		98		
						125		108		
						150		117		
						25		0,883		
						125		1,125		
						150		1,189		
						25		0,656		
						125		0,860		
						150		0,908		



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Characteristic Values

Parameter	Symbol	Conditions						Value			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_c [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Inverter Diode

Static

Forward voltage	V_F				10	25 125 150		1,61 1,69 1,69	2,1	V
Reverse leakage current	I_R			1200		25			25	μA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						1,91		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt = 278 \text{ A}/\mu\text{s}$ $di/dt = 270 \text{ A}/\mu\text{s}$ $di/dt = 272 \text{ A}/\mu\text{s}$	± 15	600	10	25 125 150		9 9 9		A
Reverse recovery time	t_{rr}					25 125 150		254 373 409		ns
Recovered charge	Q_r					25 125 150		1,088 1,664 1,808		μC
Reverse recovered energy	E_{rec}					25 125 150		0,374 0,620 0,680		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		85 54 49		$A/\mu\text{s}$



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Characteristic Values

Parameter	Symbol	Conditions						Value			Unit	
		V_{GE} [V]	V_{GS} [V]	V_{CE} [V]	V_{DS} [V]	I_c [A]	I_D [A]	T_j [°C]	I_F [A]	Min	Typ	Max

Brake Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE} = V_{CE}$			0,0005	25		5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CESat}		15		5	125 150			1,62 1,83 1,89	1,95	V
Collector-emitter cut-off current	I_{CES}		0	1200		25				50	µA
Gate-emitter leakage current	I_{GES}		20	0		25				500	nA
Internal gate resistance	r_g							none			Ω
Input capacitance	C_{ies}		0	10	25			1100			pF
Output capacitance	C_{oes}										
Reverse transfer capacitance	C_{res}										
Gate charge	Q_g		15	600	5	25			40		nC

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)							2,30		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{goff} = 64 \Omega$ $R_{gon} = 64 \Omega$	15/0	600	5	25		79			ns
Rise time	t_r					125		73			
						150		72			
Turn-off delay time	$t_{d(off)}$	$R_{goff} = 64 \Omega$ $R_{gon} = 64 \Omega$	15/0	600	5	25		45			mWs
Fall time	t_f					125		48			
						150		49			
Turn-on energy (per pulse)	E_{on}	$Q_{fFWD} = 0,6 \mu\text{C}$ $Q_{fFWD} = 0,8 \mu\text{C}$ $Q_{fFWD} = 0,9 \mu\text{C}$	15/0	600	5	25		234			mWs
Turn-off energy (per pulse)	E_{off}					125		262			
						150		270			
						25		101			
						125		114			
						150		117			
						25		0,480			
						125		0,609			
						150		0,634			
						25		0,345			
						125		0,454			
						150		0,474			



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Characteristic Values

Parameter	Symbol	Conditions						Value			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_c [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Brake Diode

Static

Forward voltage	V_F				5	25 125 150		1,57 1,65 1,65	2,1		V
Reverse leakage current	I_R			1200		25			20		µA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						3,50		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt = 85 \text{ A/}\mu\text{s}$ $di/dt = 102 \text{ A/}\mu\text{s}$ $di/dt = 87 \text{ A/}\mu\text{s}$	15/0	600	5	25 125 150		4 4 4			A
Reverse recovery time	t_{rr}					25 125 150		259 386 431			ns
Recovered charge	Q_r					25 125 150		0,558 0,833 0,935			µC
Reverse recovered energy	E_{rec}					25 125 150		0,200 0,314 0,363			mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		37 24 20			A/µs

Thermistor

Rated resistance	R					25		22			kΩ
Deviation of R_{100}	$\Delta_{R/R}$	$R_{100} = 1484 \Omega$				100	-5		5		%
Power dissipation	P					25		5			mW
Power dissipation constant						25		1,5			mW/K
B-value	$B_{(25/50)}$	Tol. ±1 %				25		3962			K
B-value	$B_{(25/100)}$	Tol. ±1 %				25		4000			K
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Rectifier Diode Characteristics

figure 1.
Typical forward characteristics

FWD

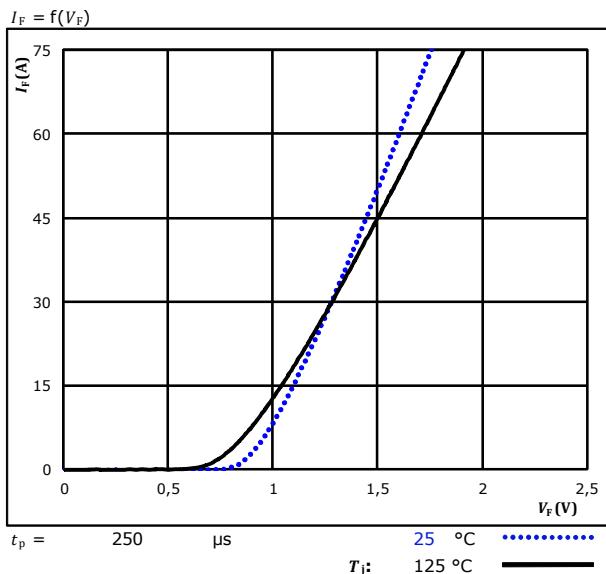
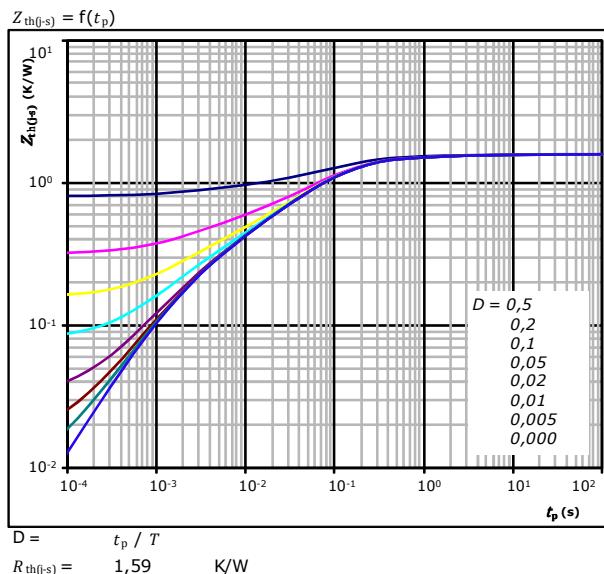


figure 2.
Transient thermal impedance as a function of pulse width

FWD



Diode thermal model values

R (K/W)	τ (s)
3,44E-02	9,66E+00
1,12E-01	1,22E+00
5,81E-01	1,45E-01
4,89E-01	5,05E-02
2,38E-01	9,26E-03
1,22E-01	1,79E-03
1,22E-01	1,79E-03



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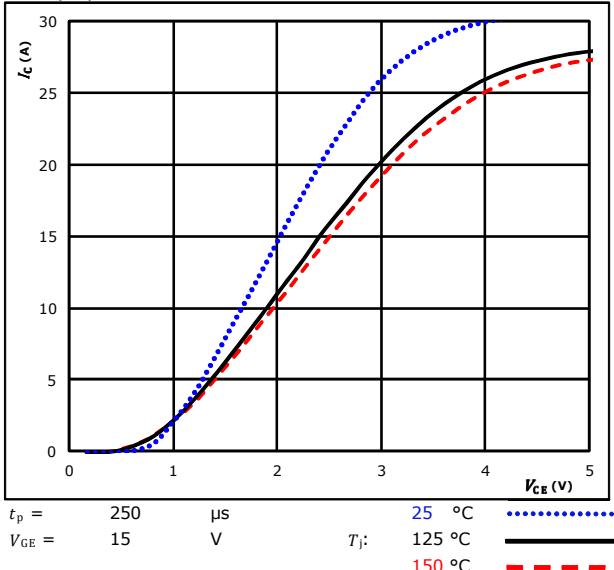
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Inverter Switch Characteristics

figure 1.

Typical output characteristics

$$I_C = f(V_{CE})$$

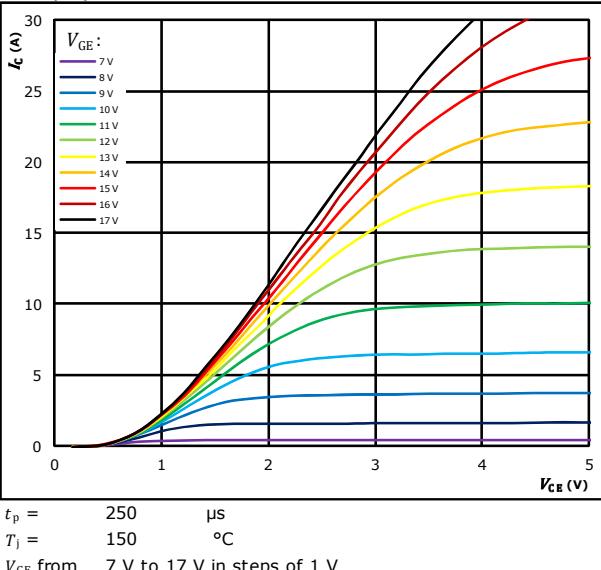


IGBT

figure 2.

Typical output characteristics

$$I_C = f(V_{CE})$$

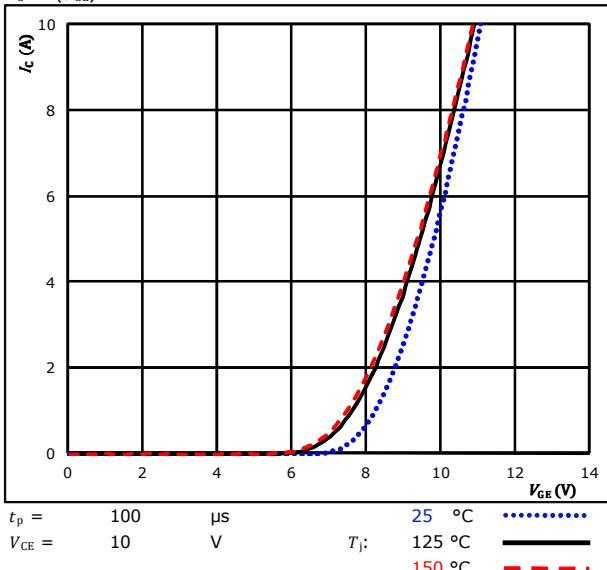


IGBT

figure 3.

Typical transfer characteristics

$$I_C = f(V_{GE})$$

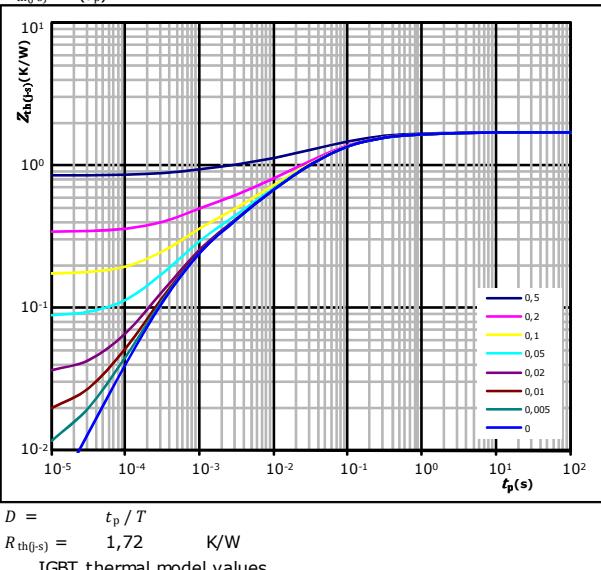


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figure 4.

Transient thermal impedance as function of pulse duration

$$Z_{th(\text{t}_p)} = f(t_p)$$



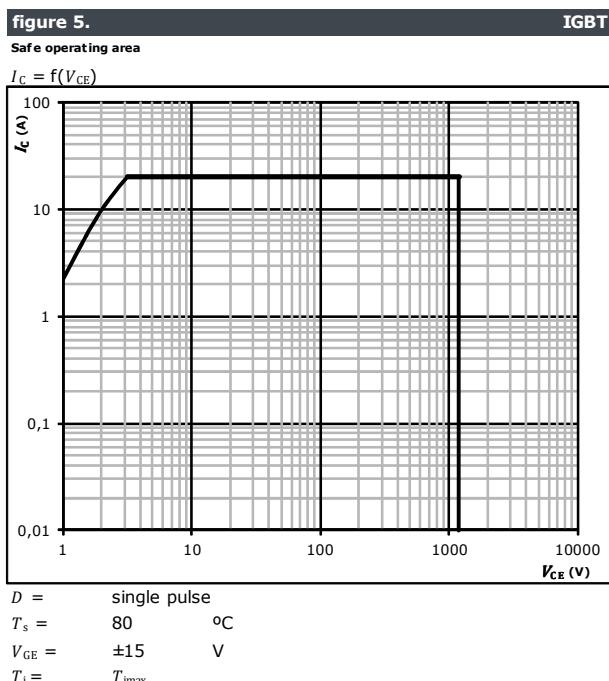
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Inverter Switch Characteristics





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Inverter Diode Characteristics

figure 1.

FWD

Typical forward characteristics

$$I_F = f(V_F)$$

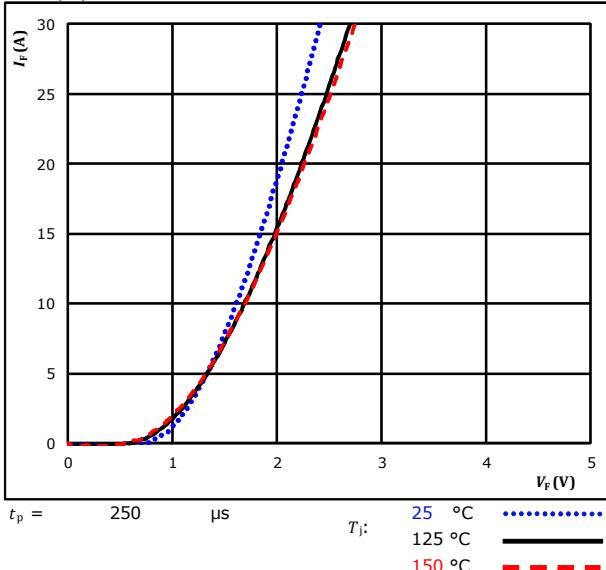
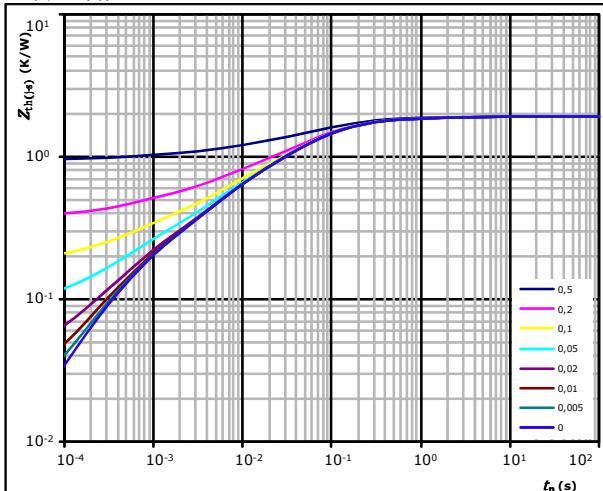


figure 2.

FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(t_p)} = f(t_p)$$



$$D = t_p / T$$

$$R_{th(t_p)} = 1,91 \text{ K/W}$$

FWD thermal model values

R (K/W)	τ (s)
9,38E-02	2,25E+00
3,43E-01	2,12E-01
8,53E-01	5,82E-02
3,59E-01	9,80E-03
1,37E-01	2,88E-03
1,26E-01	4,78E-04

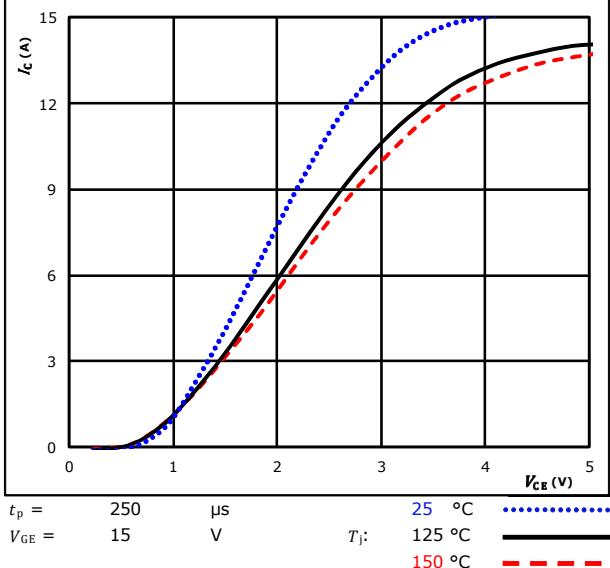


Brake Switch Characteristics

figure 1.

Typical output characteristics

$$I_C = f(V_{CE})$$

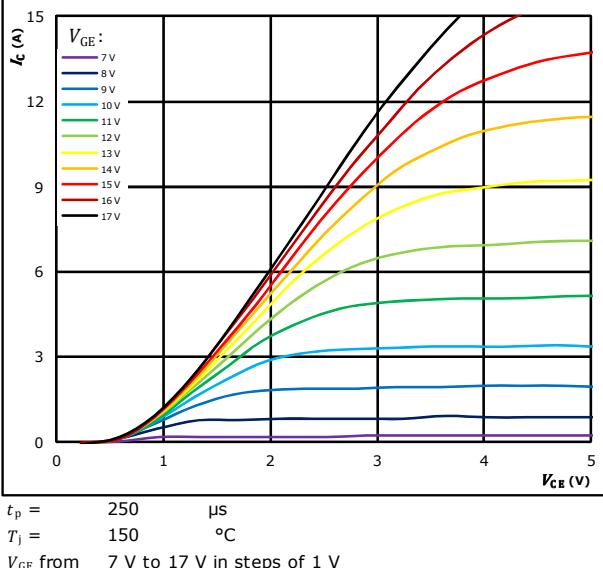


IGBT

figure 2.

Typical output characteristics

$$I_C = f(V_{CE})$$

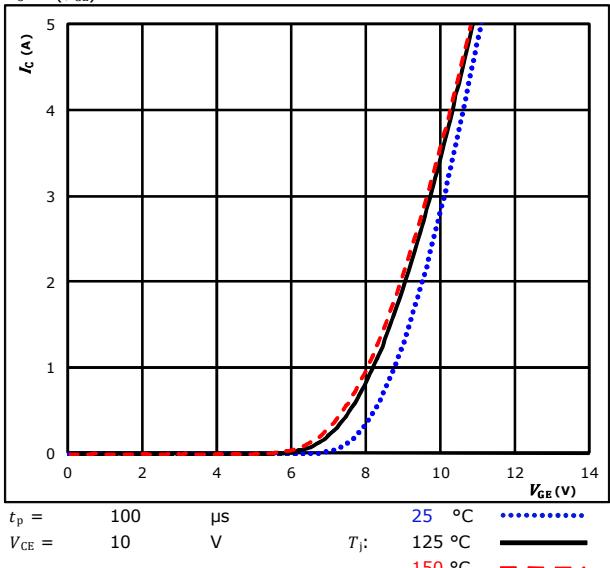


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figure 3.

Typical transfer characteristics

$$I_C = f(V_{GE})$$

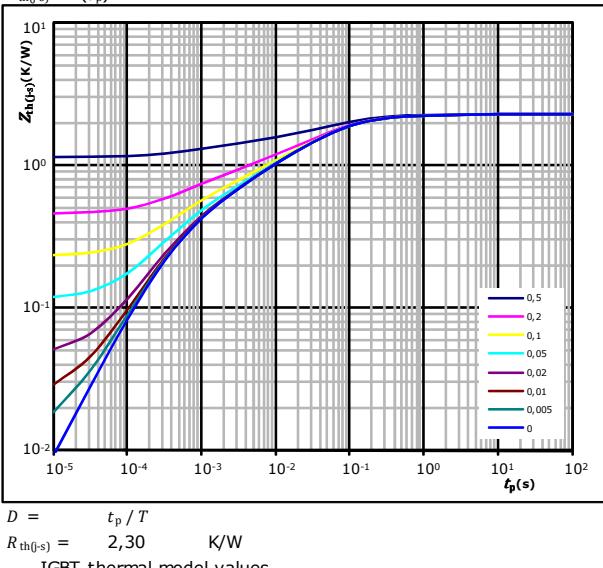


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figure 4.

Transient thermal impedance as function of pulse duration

$$Z_{th(\text{t}_p)} = f(t_p)$$



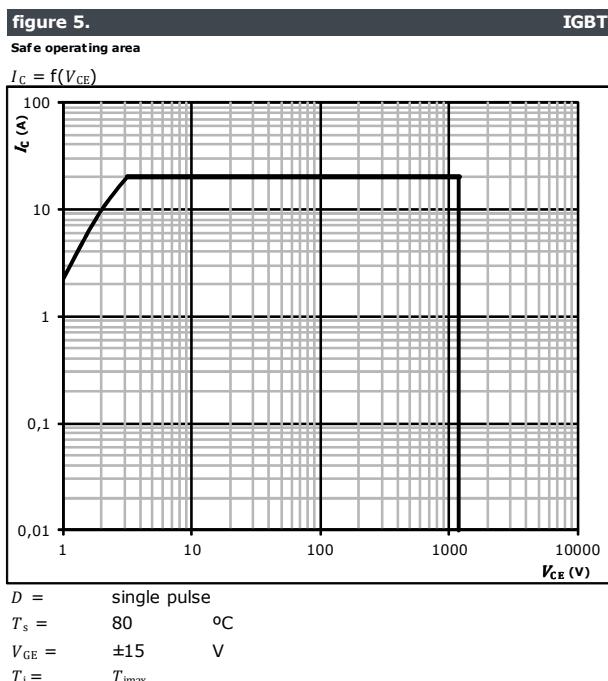
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Brake Switch Characteristics

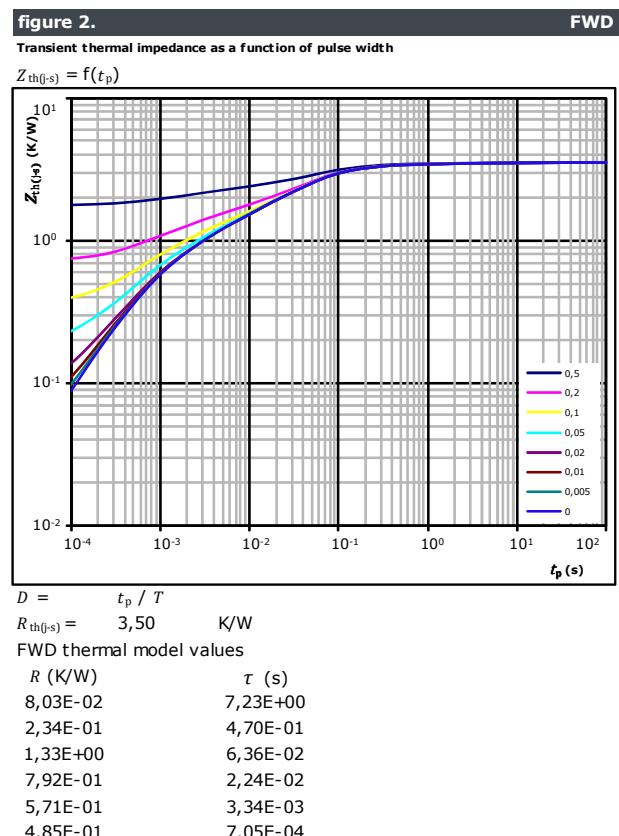
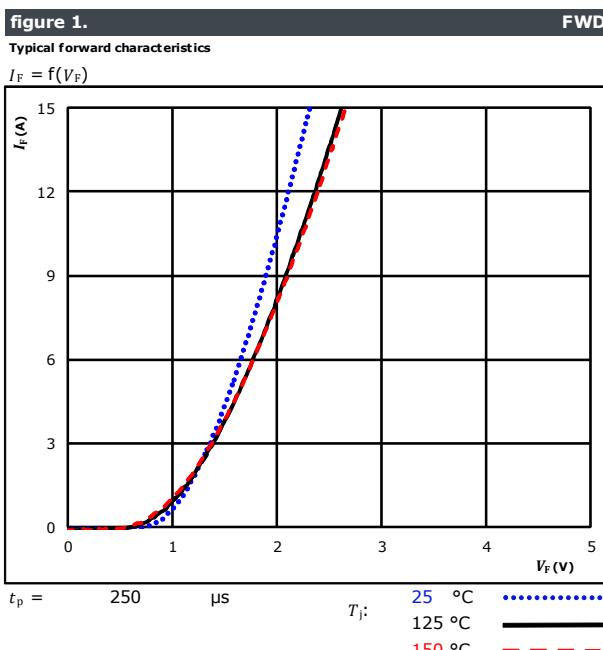




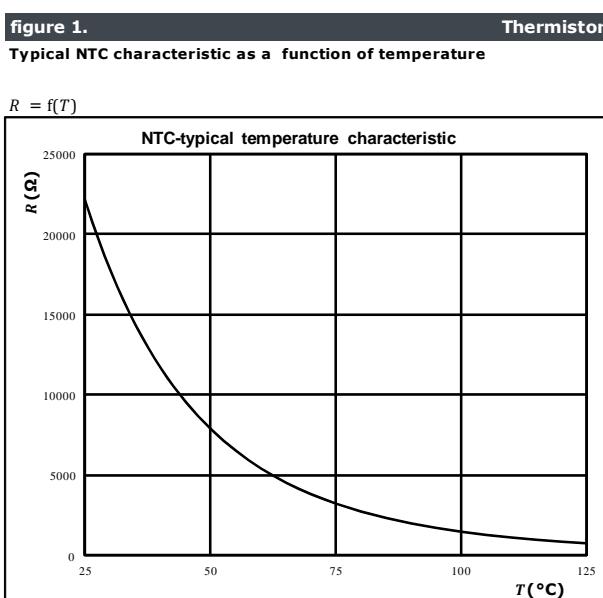
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Brake Diode Characteristics



NTC Characteristics





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Inverter Switching Characteristics

figure 1.

Typical switching energy losses as a function of collector current

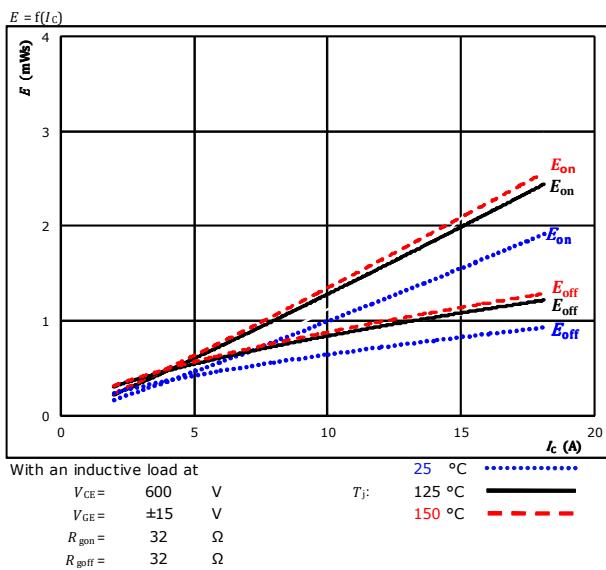


figure 2.

Typical switching energy losses as a function of gate resistor

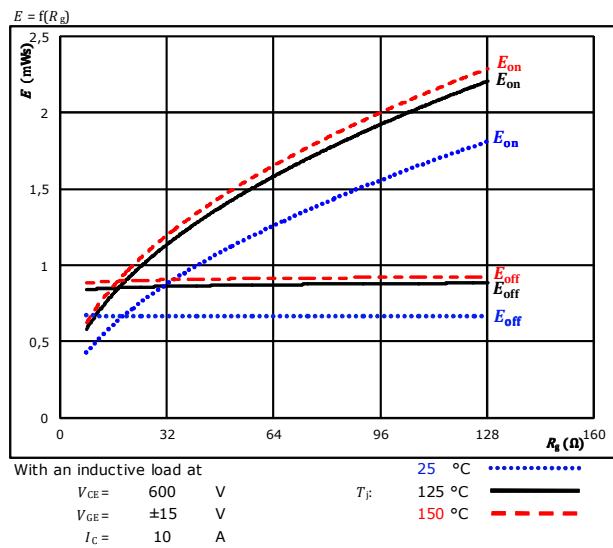


figure 3.

Typical reverse recovered energy loss as a function of collector current

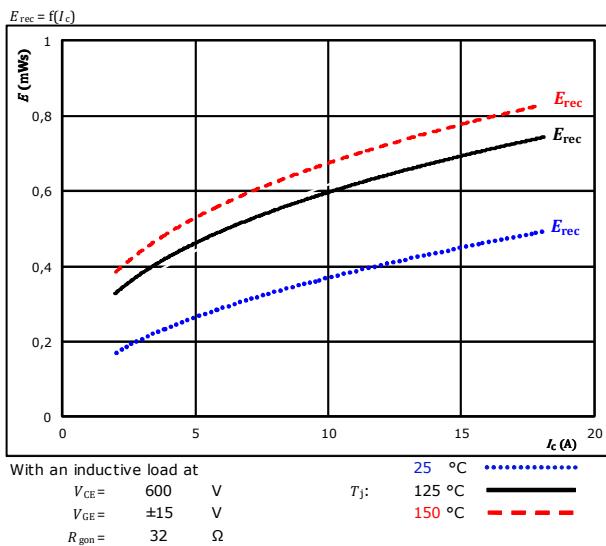
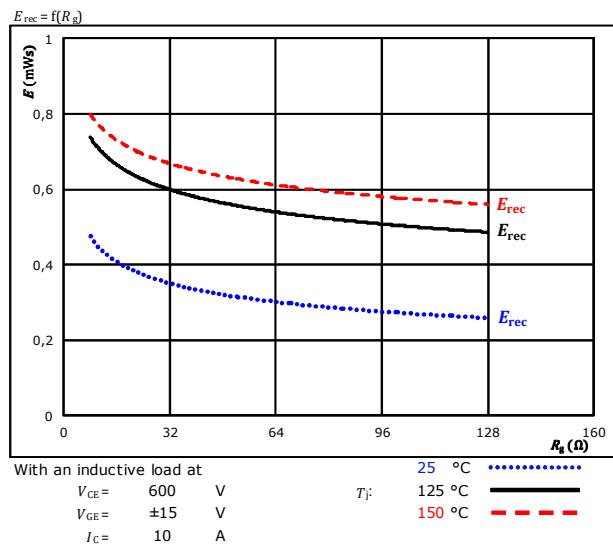


figure 4.

Typical reverse recovered energy loss as a function of gate resistor



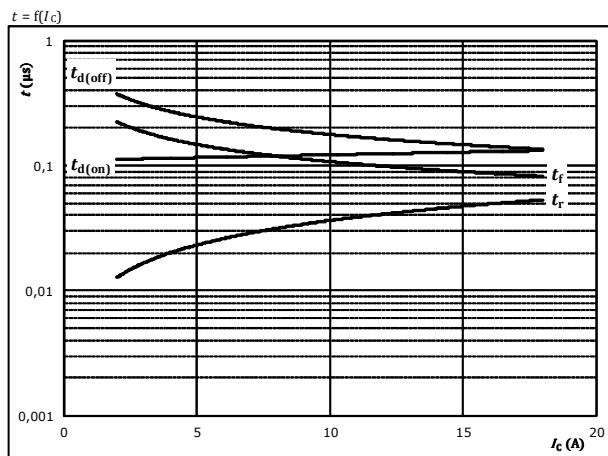


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Inverter Switching Characteristics

figure 5. IGBT

Typical switching times as a function of collector current

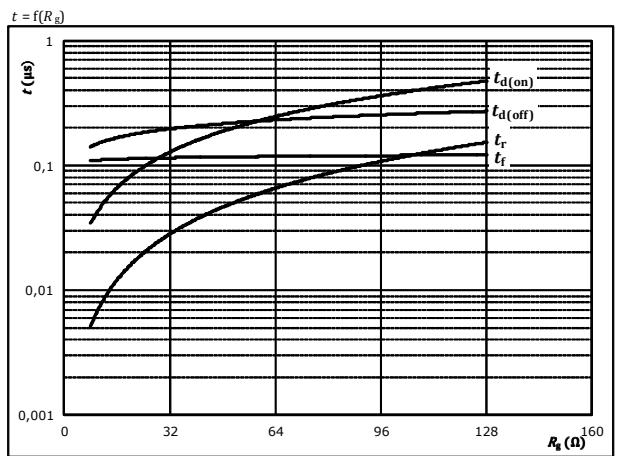


With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	32	Ω
$R_{goff} =$	32	Ω

figure 6. IGBT

Typical switching times as a function of gate resistor

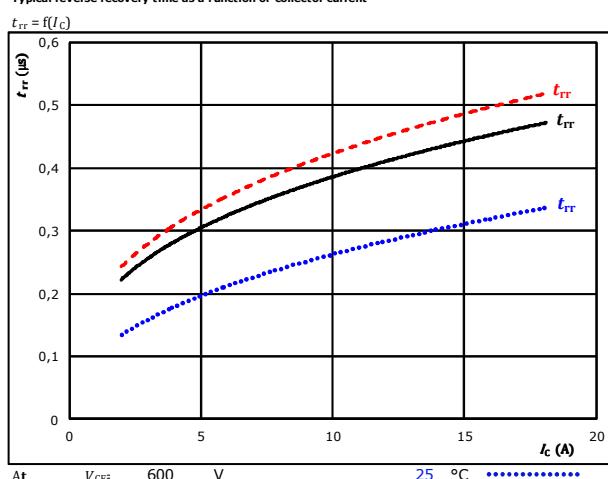


With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	10	A

figure 7. FWD

Typical reverse recovery time as a function of collector current

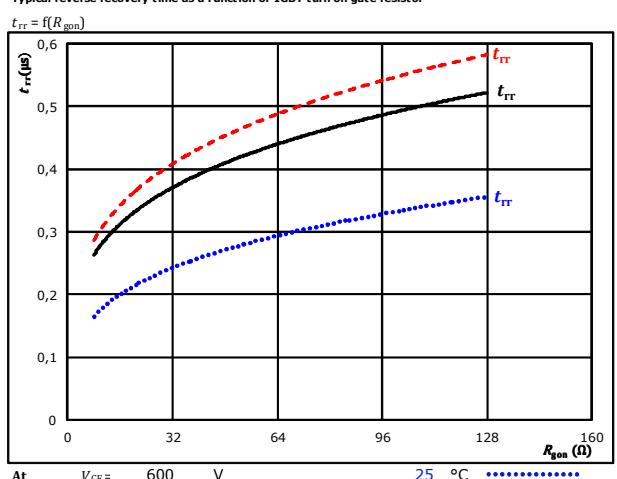


At

$V_{CE} =$	600	V	25 °C
$V_{GE} =$	±15	V	$T_J =$	125 °C —
$R_{gon} =$	32	Ω		150 °C - - -

figure 8. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor



At

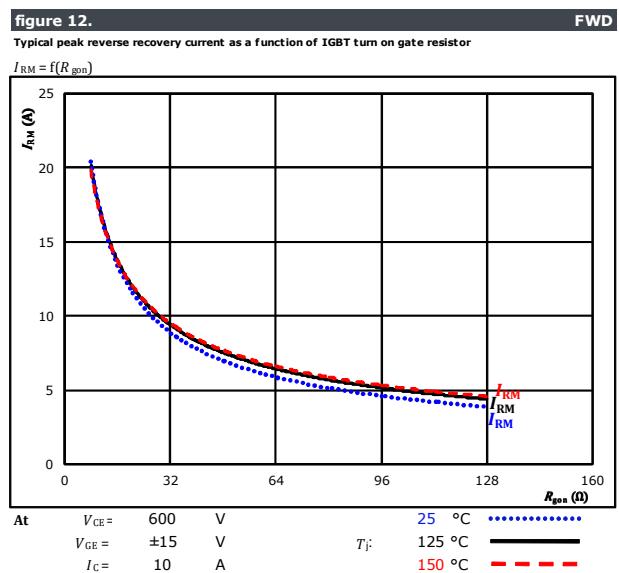
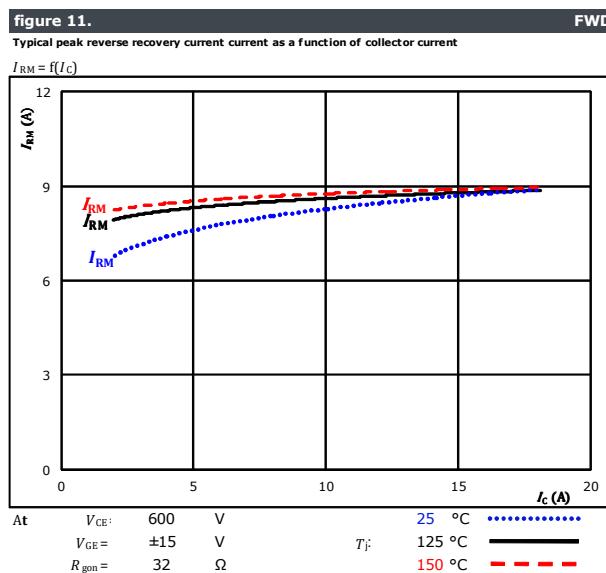
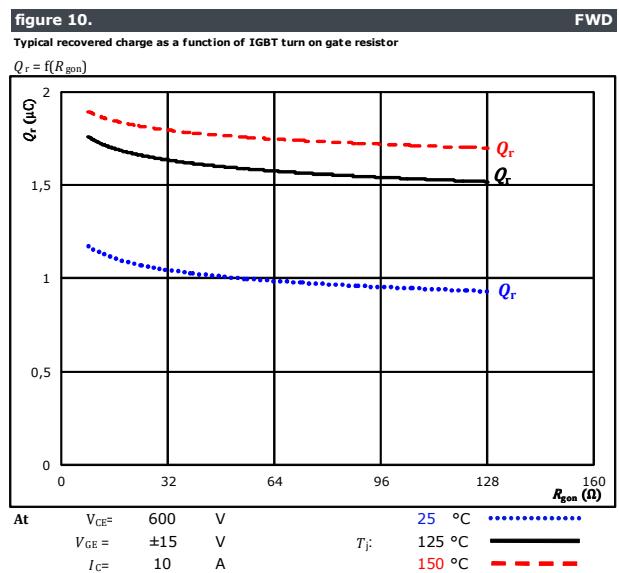
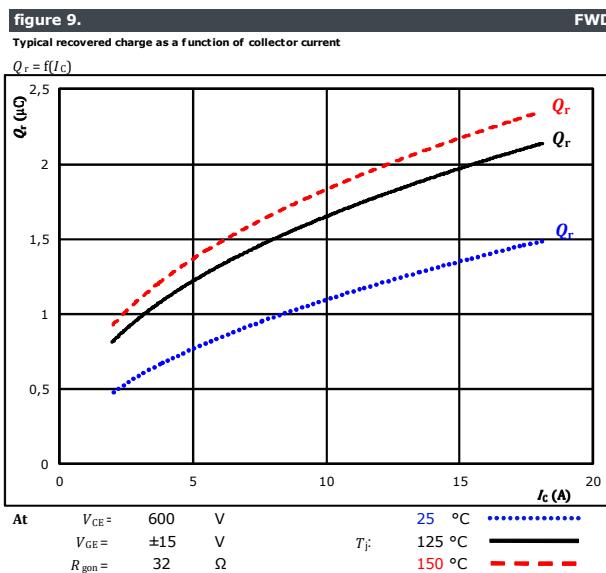
$V_{CE} =$	600	V	25 °C
$V_{GE} =$	±15	V	$T_J =$	125 °C —
$I_C =$	10	A		150 °C - - -



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datasheet

Inverter Switching Characteristics





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**10-FZ12PMA010M7-P849A28
10-F012PMA010M7-P849A29**
datasheet

Inverter Switching Characteristics

figure 13.

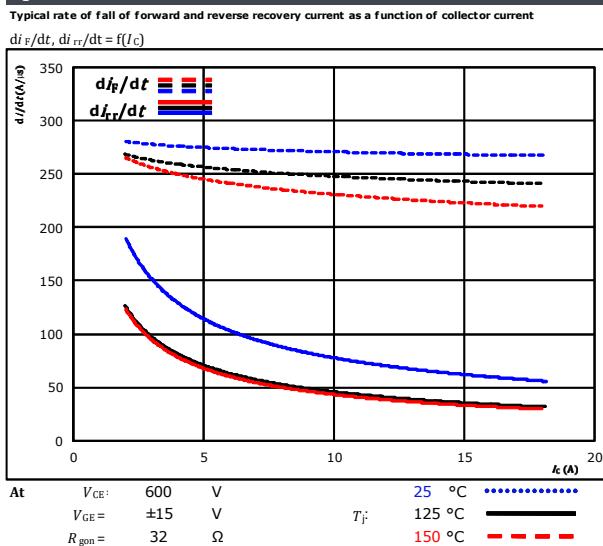


figure 14.

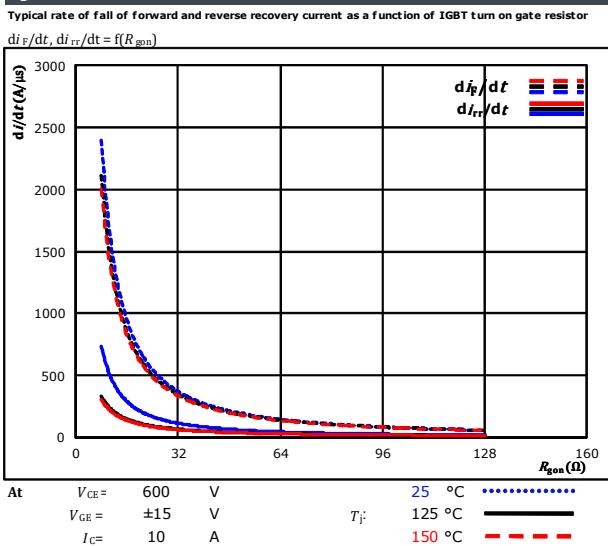
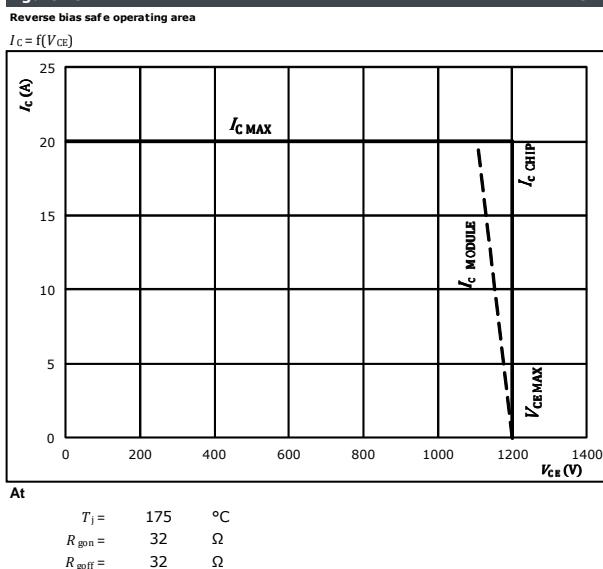


figure 15.





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datasheet

Inverter Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	32 Ω
R_{goff}	=	32 Ω

figure 1.

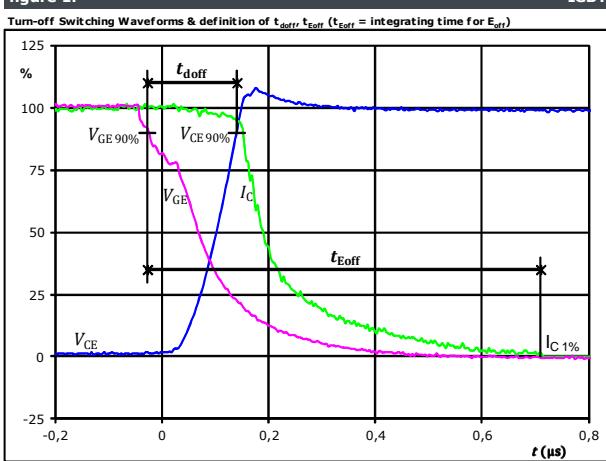
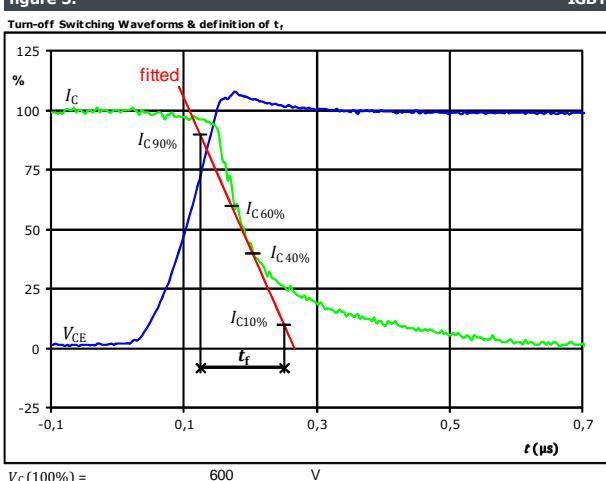


figure 3.



10-FZ12PMA010M7-P849A28
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datasheet

figure 2.

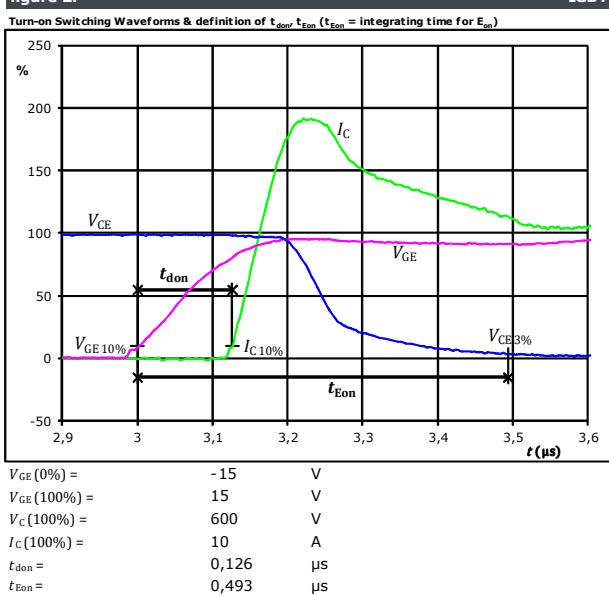
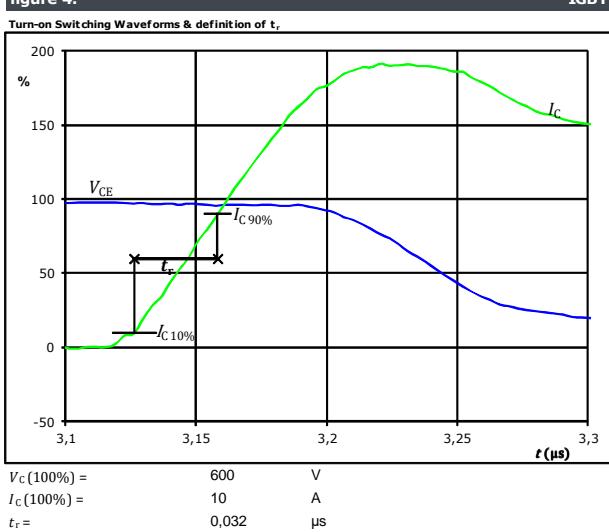


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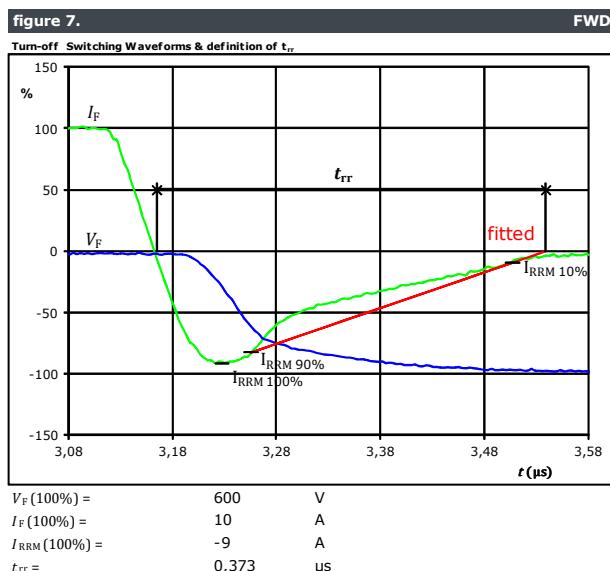
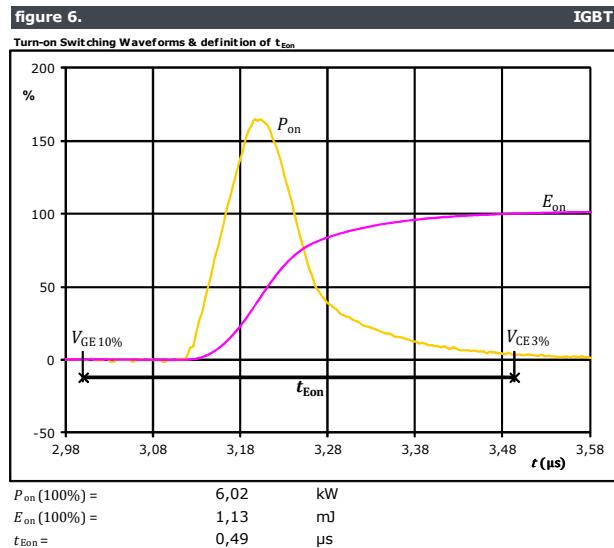
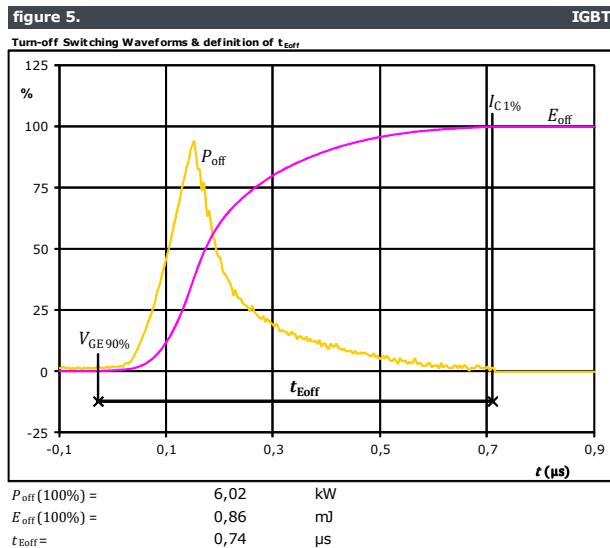




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datasheet

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Inverter Switching Characteristics

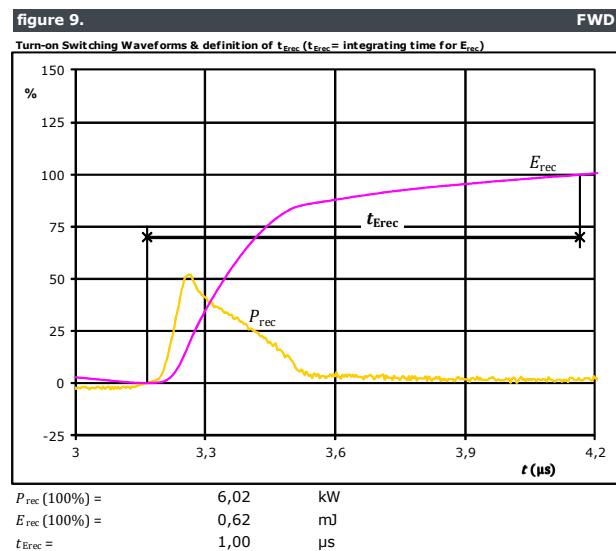
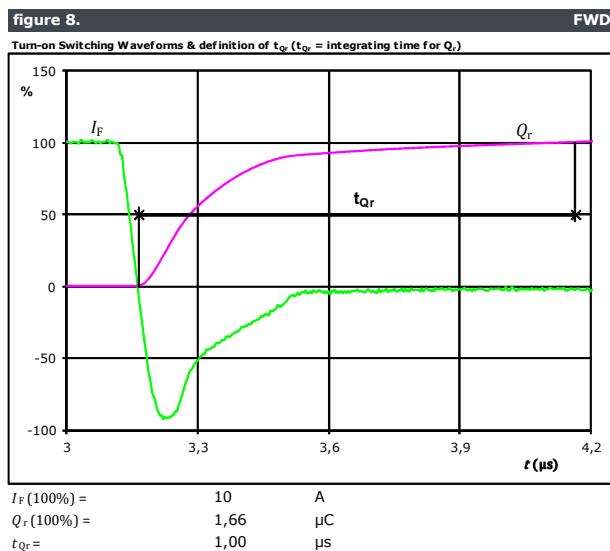




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datasheet

Inverter Switching Characteristics

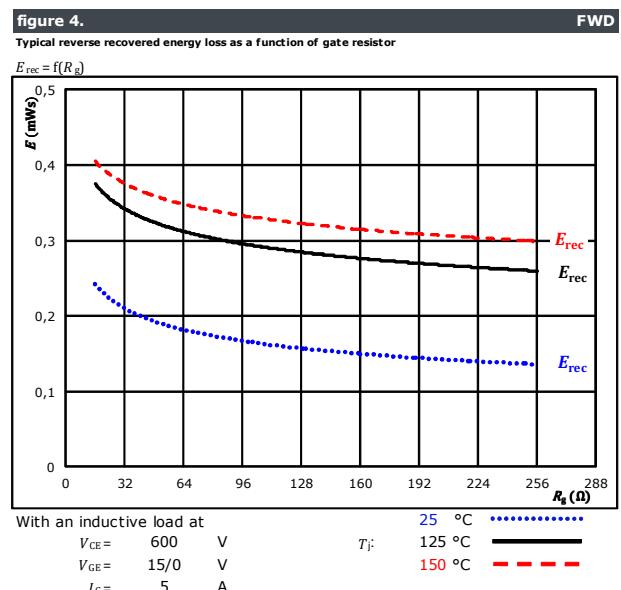
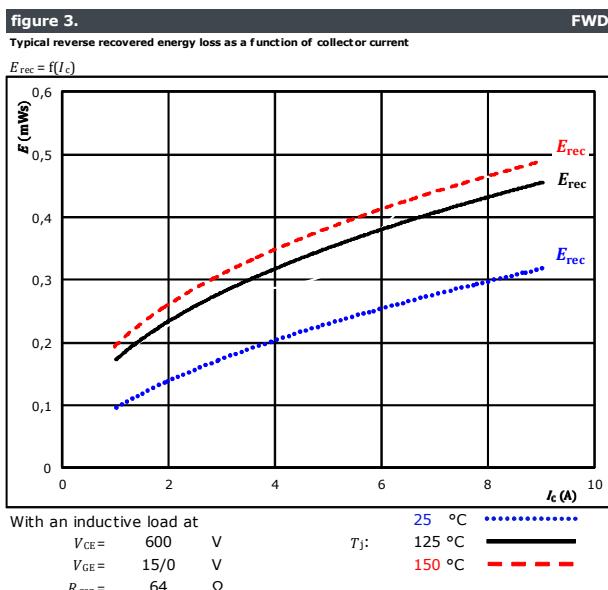
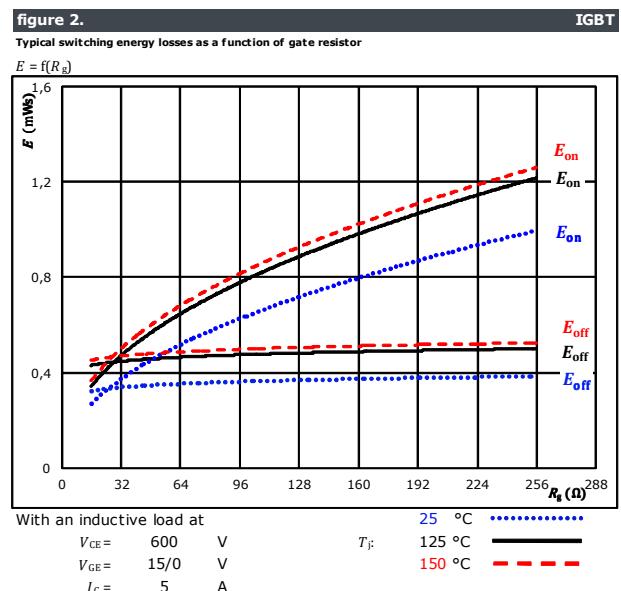
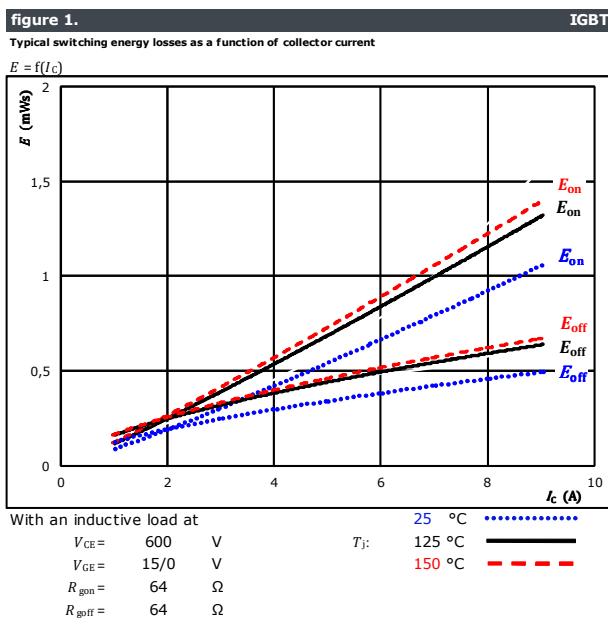




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Brake Switching Characteristics





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Brake Switching Characteristics

figure 5. Typical switching times as a function of collector current

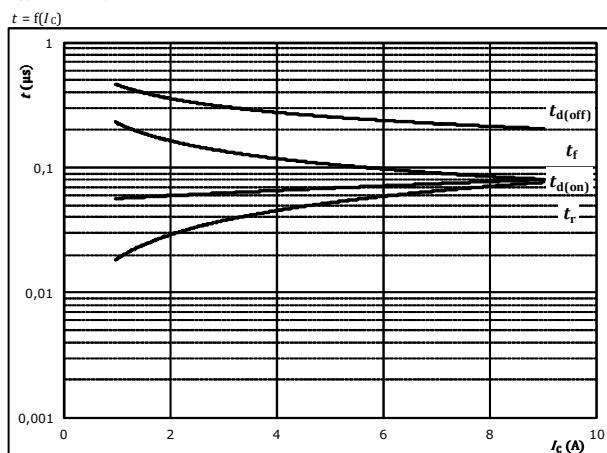


figure 6. Typical switching times as a function of gate resistor

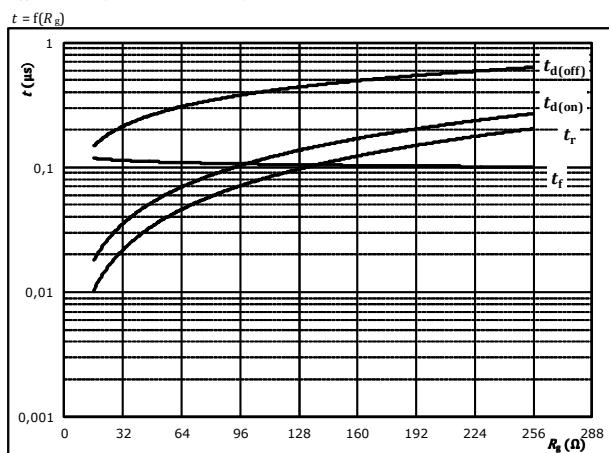


figure 7. Typical reverse recovery time as a function of collector current

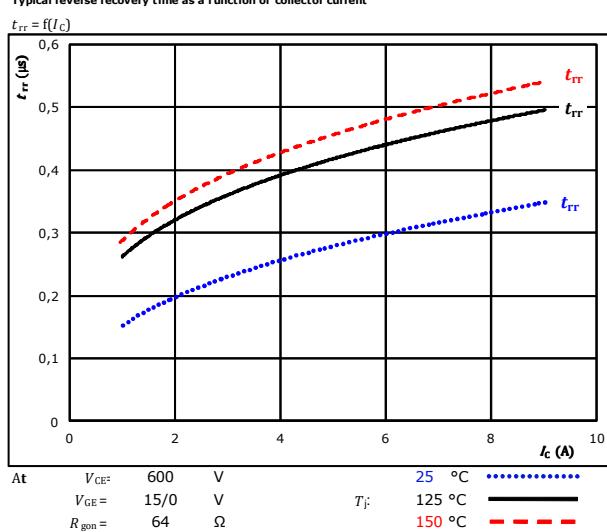
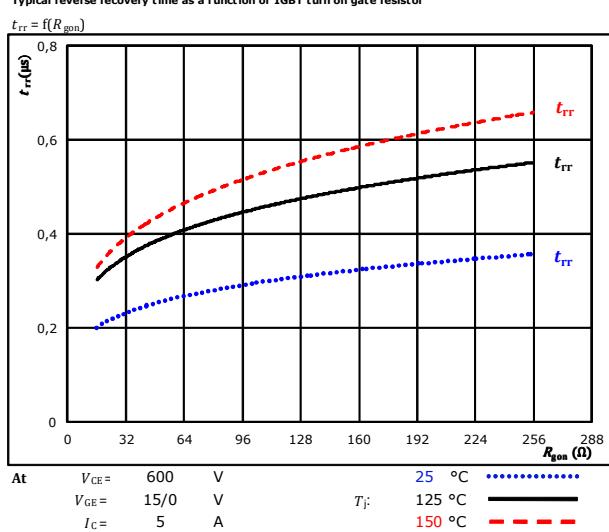


figure 8. Typical reverse recovery time as a function of IGBT turn on gate resistor

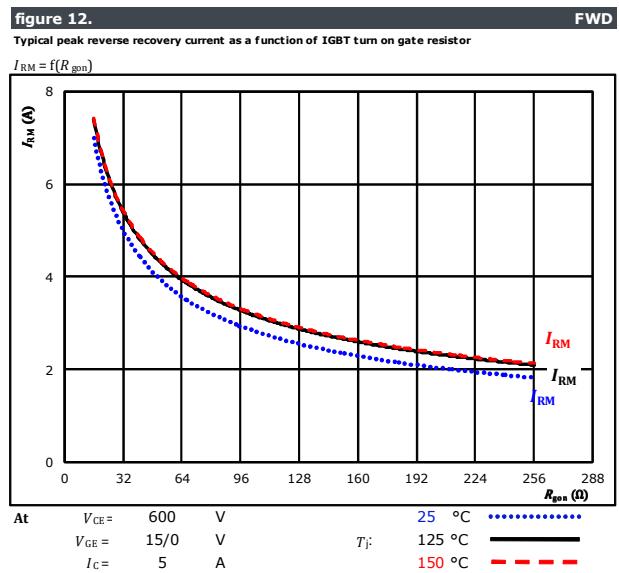
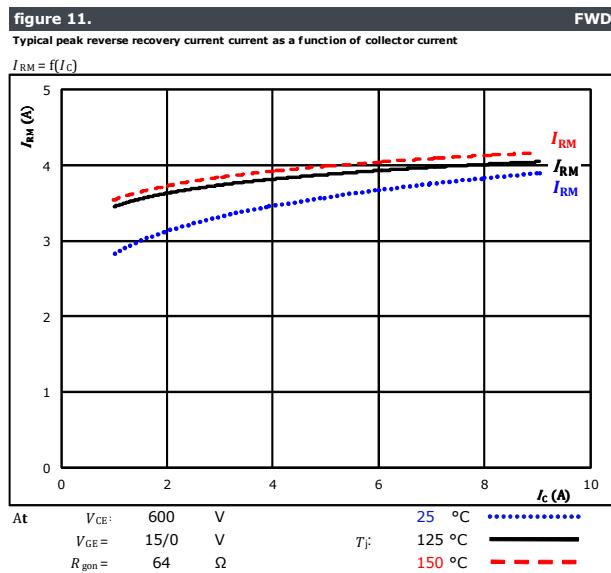
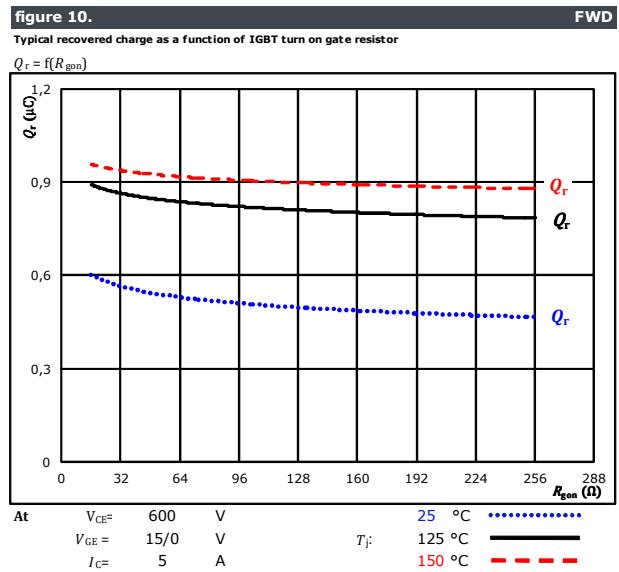
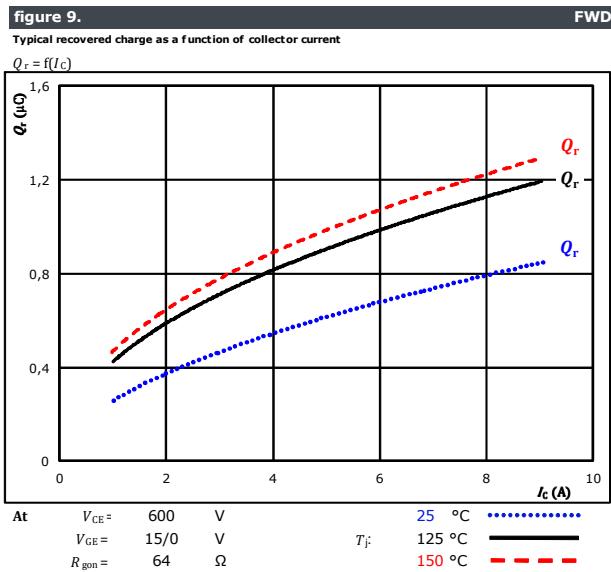




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Brake Switching Characteristics





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Brake Switching Characteristics

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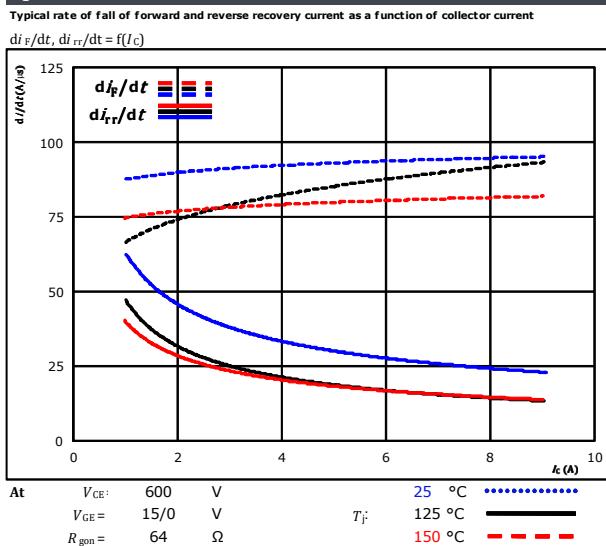


figure 14.

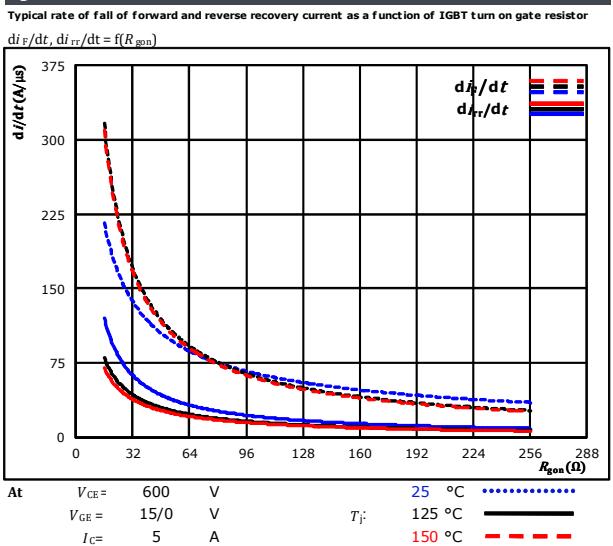
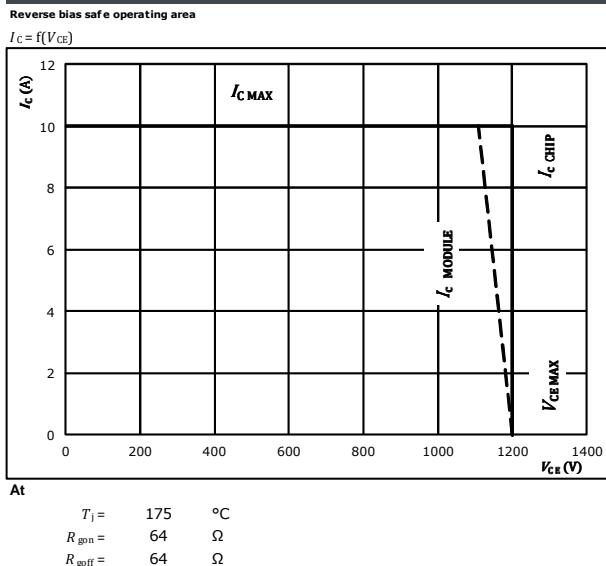


figure 15.





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datasheet

Brake Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	64 Ω
R_{goff}	=	64 Ω

figure 1.

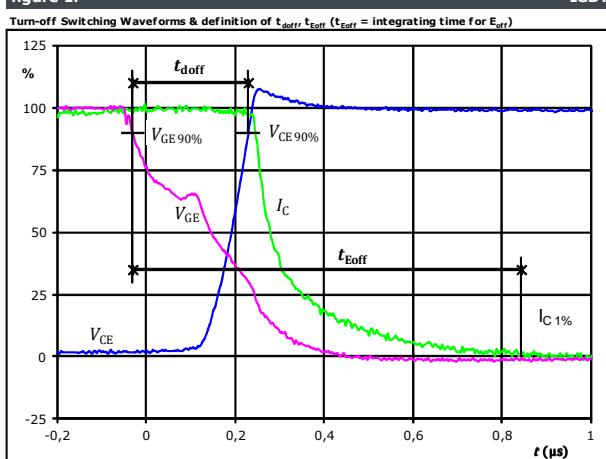


figure 3.

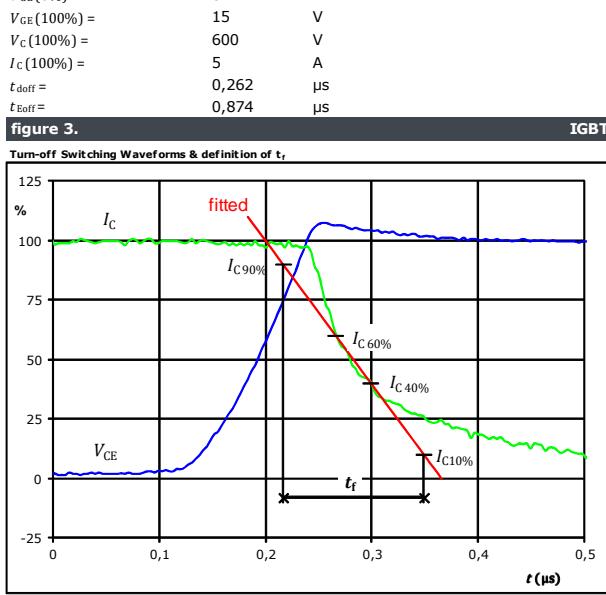


figure 2.

figure 2.

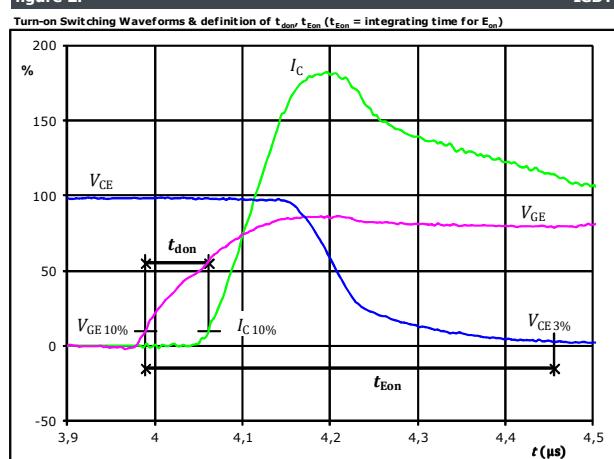
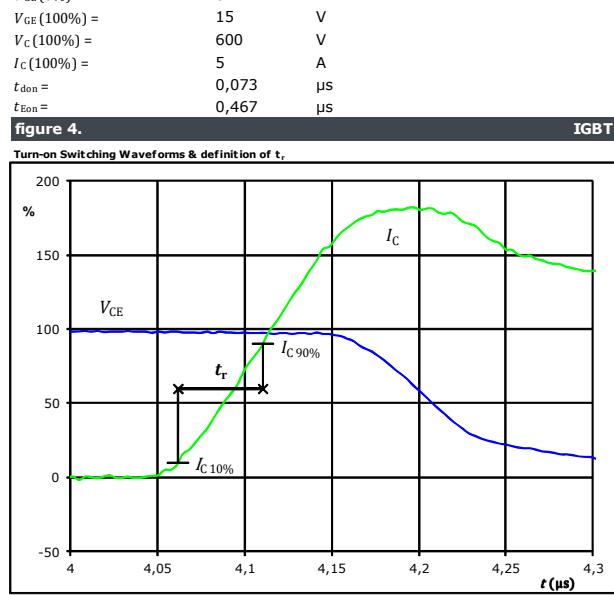


figure 4.

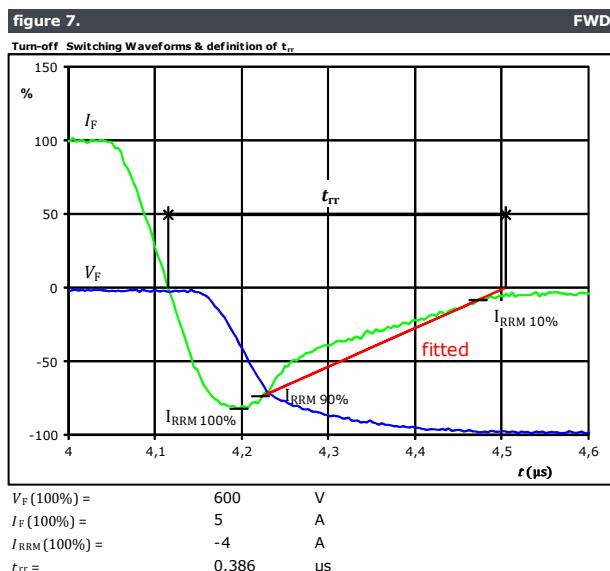
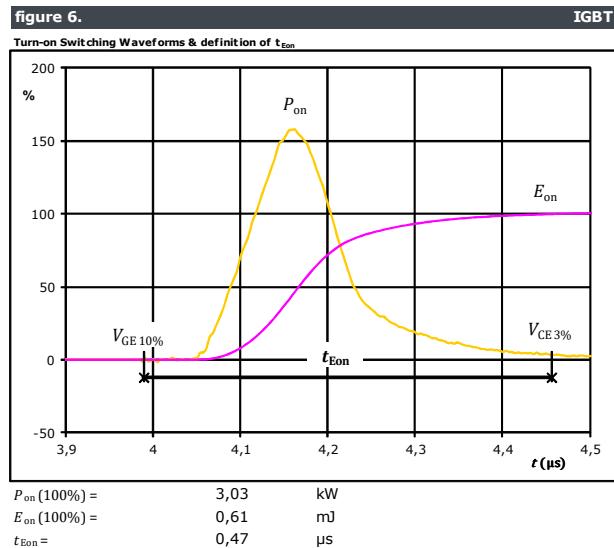
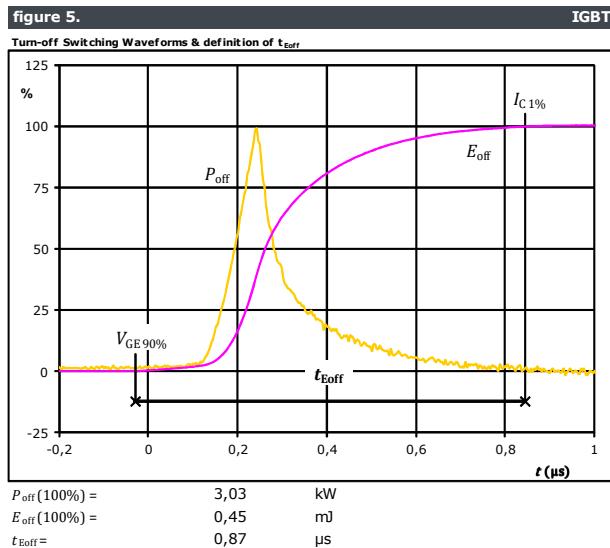




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Brake Switching Characteristics





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Brake Switching Characteristics

figure 8.

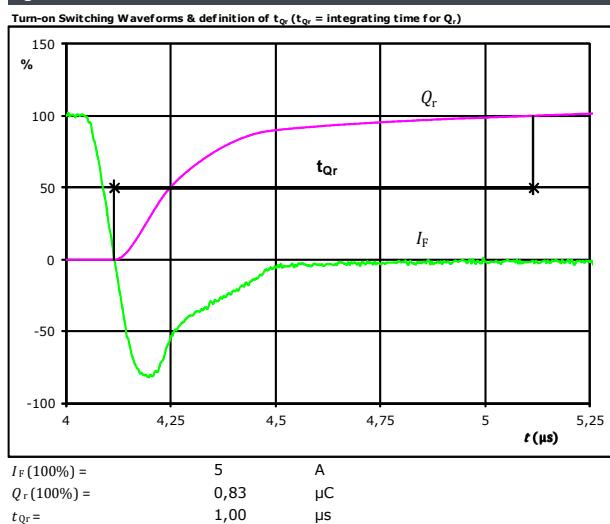
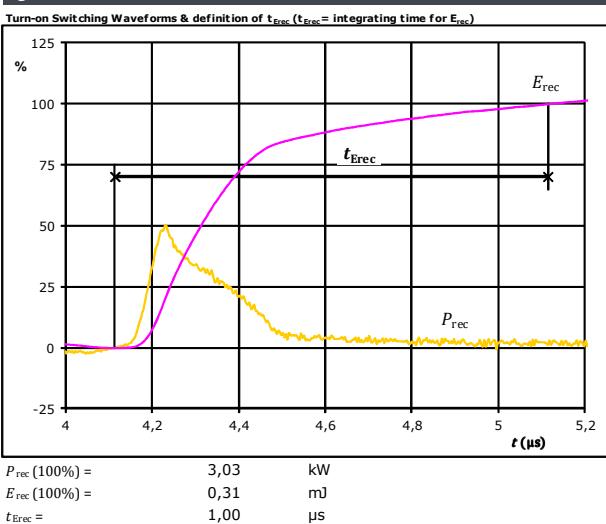


figure 9.





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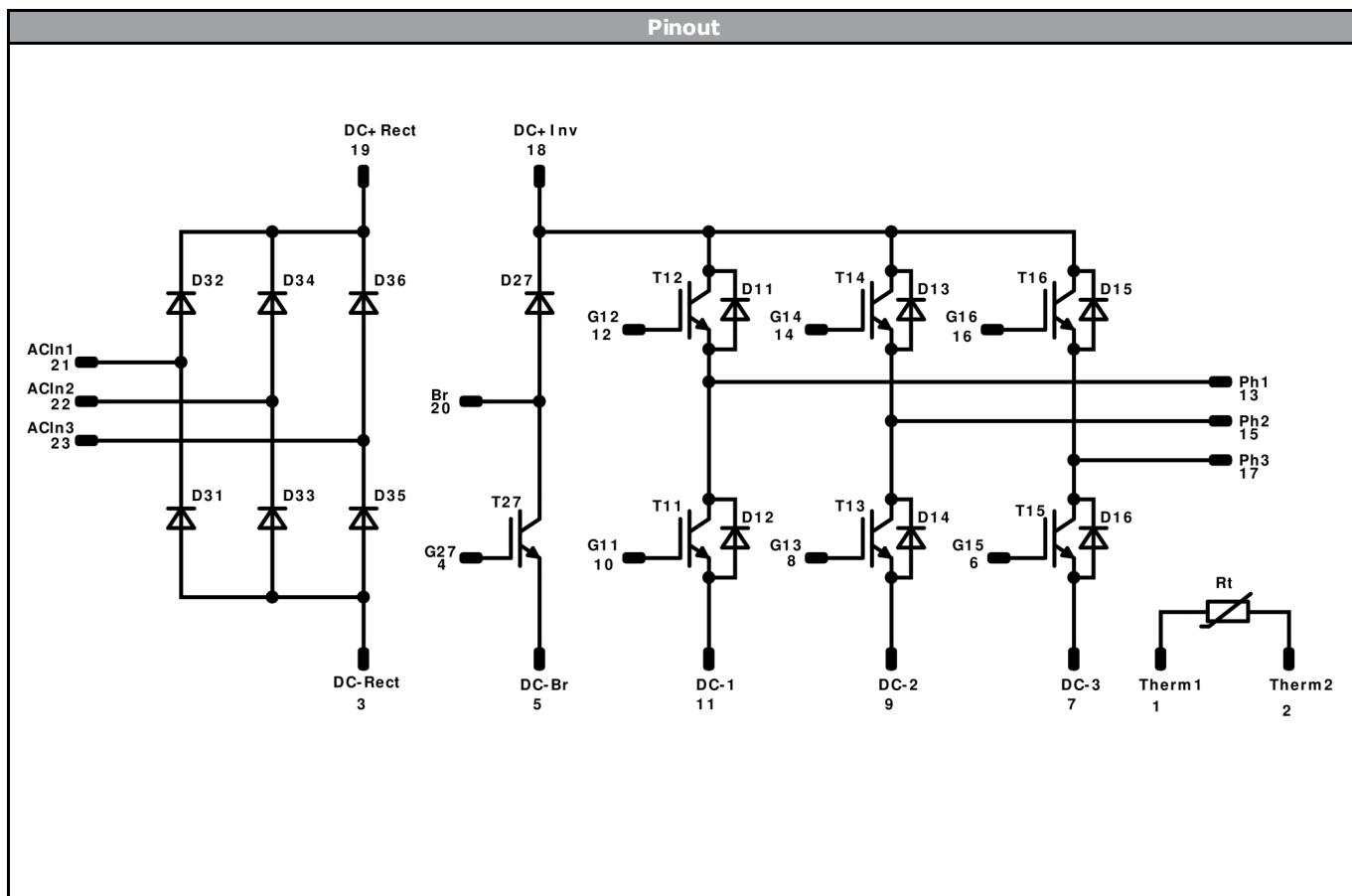
**10-FZ12PMA010M7-P849A28
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datasheet

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without thermal paste 17 mm housing with solder pins				10-F012PMA010M7-P849A29																																																																																																		
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Outline																																																																																																						
Pin table																																																																																																						
<table border="1"><thead><tr><th>Pin</th><th>X</th><th>Y</th><th>Function</th></tr></thead><tbody><tr><td>1</td><td>25,5</td><td>2,7</td><td>Therm1</td></tr><tr><td>2</td><td>25,5</td><td>0</td><td>Therm2</td></tr><tr><td>3</td><td>22,8</td><td>0</td><td>DC-Rect</td></tr><tr><td>4</td><td>20,1</td><td>0</td><td>G27</td></tr><tr><td>5</td><td>16,2</td><td>0</td><td>DC-Br</td></tr><tr><td>6</td><td>13,5</td><td>0</td><td>G15</td></tr><tr><td>7</td><td>10,8</td><td>0</td><td>DC-3</td></tr><tr><td>8</td><td>8,1</td><td>0</td><td>G13</td></tr><tr><td>9</td><td>5,4</td><td>0</td><td>DC-2</td></tr><tr><td>10</td><td>2,7</td><td>0</td><td>G11</td></tr><tr><td>11</td><td>0</td><td>0</td><td>DC-1</td></tr><tr><td>12</td><td>0</td><td>19,8</td><td>G12</td></tr><tr><td>13</td><td>0</td><td>22,5</td><td>Ph1</td></tr><tr><td>14</td><td>7,5</td><td>19,8</td><td>G14</td></tr><tr><td>15</td><td>7,5</td><td>22,5</td><td>Ph2</td></tr><tr><td>16</td><td>15</td><td>19,8</td><td>G16</td></tr><tr><td>17</td><td>15</td><td>22,5</td><td>Ph3</td></tr><tr><td>18</td><td>22,8</td><td>22,5</td><td>DC+Inv</td></tr><tr><td>19</td><td>25,5</td><td>22,5</td><td>DC+Rect</td></tr><tr><td>20</td><td>33,5</td><td>22,5</td><td>Br</td></tr><tr><td>21</td><td>33,5</td><td>15</td><td>ACIn1</td></tr><tr><td>22</td><td>33,5</td><td>7,5</td><td>ACIn2</td></tr><tr><td>23</td><td>33,5</td><td>0</td><td>ACIn3</td></tr></tbody></table>							Pin	X	Y	Function	1	25,5	2,7	Therm1	2	25,5	0	Therm2	3	22,8	0	DC-Rect	4	20,1	0	G27	5	16,2	0	DC-Br	6	13,5	0	G15	7	10,8	0	DC-3	8	8,1	0	G13	9	5,4	0	DC-2	10	2,7	0	G11	11	0	0	DC-1	12	0	19,8	G12	13	0	22,5	Ph1	14	7,5	19,8	G14	15	7,5	22,5	Ph2	16	15	19,8	G16	17	15	22,5	Ph3	18	22,8	22,5	DC+Inv	19	25,5	22,5	DC+Rect	20	33,5	22,5	Br	21	33,5	15	ACIn1	22	33,5	7,5	ACIn2	23	33,5	0	ACIn3
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Tolerance of pinpositions ±0.5mm at the end of pins Dimension of coordinate axis is only offset without tolerance																																																																																																						



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Identification					
ID	Component	Voltage	Current	Function	Comment
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	25 A	Rectifier Diode	
T11, T12, T13, T14, T15, T16	IGBT	1200 V	10 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	10 A	Inverter Diode	
T27	IGBT	1200 V	5 A	Brake Switch	
D27	FWD	1200 V	5 A	Brake Diode	
Rt	NTC			Thermistor	



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Packaging instruction			
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ Sample

Handling instruction			
Handling instructions for flow 0 packages see vincotech.com website.			

Package data			
Package data for flow 0 packages see vincotech.com website.			

UL recognition and file number			
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.			

Document No.:	Date:	Modification:	Pages
10-Fx12PMA010M7-P849A2x-D1-14	17 Nov. 2017		

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.