

flowPHASE0
1200V/75A
Features

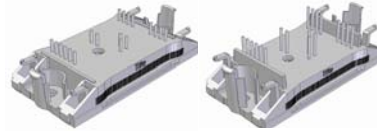
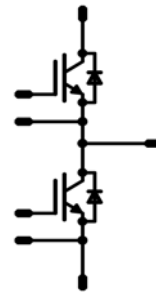
- Trench Fieldstop IGBT⁴ technology
- 2-clip housing in 12mm and 17mm height
- Compact and low inductance design

Target Applications

- Motor Drive
- UPS

Types

- FZ122PA075SC
- F0122PA075SC

flow0 housing

Schematic


Maximum Ratings

 $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Transistor				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	59 75	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by T_{jmax}	225	A
Power dissipation per IGBT	P_{tot}	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	126 191	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE} = 15V$	10 800	μs V
Maximum Junction Temperature	T_{jmax}		175	$^\circ\text{C}$
Inverter Diode				
Peak Repetitive Reverse Voltage	V_{RRM}	$T_j = 25^\circ\text{C}$	1200	V
DC forward current	I_F	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	55 74	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	150	A
Power dissipation per Diode	P_{tot}	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	85 128	W
Maximum Junction Temperature	T_{jmax}		175	$^\circ\text{C}$

Maximum Ratings

 $T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
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Thermal Properties

Storage temperature	T_{stg}		-40...+125	$^{\circ}\text{C}$
Operation temperature under switching condition	T_{op}		-40...+($T_{j\text{max}}$ - 25)	$^{\circ}\text{C}$

Insulation Properties

Insulation voltage	V_{is}	t=2s DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
			V _{GE} [V] or V _{GS} [V]	V _r [V] or V _{CE} [V] or V _{DS} [V]	I _c [A] or I _F [A] or I _D [A]	T _j	Min	Typ	Max	
Inverter Transistor										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,003	T _j =25°C T _j =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		75	T _j =25°C T _j =150°C	1,5	1,94 2,38	2,3	V
Collector-emitter cut-off current incl. Diode	I _{CES}		0	1200		T _j =25°C T _j =150°C			0,03	mA
Gate-emitter leakage current	I _{GES}		20	0		T _j =25°C T _j =150°C			700	nA
Integrated Gate resistor	R _{gint}							10		Ω
Turn-on delay time	t _{d(on)}	Rgoff=4 Ω Rgon=4 Ω	±15	600	75	T _j =25°C T _j =150°C		178 196		ns
Rise time	t _r					T _j =25°C T _j =150°C		34 36		
Turn-off delay time	t _{d(off)}					T _j =25°C T _j =150°C		284 373		
Fall time	t _f					T _j =25°C T _j =150°C		63 124		
Turn-on energy loss per pulse	E _{on}					T _j =25°C T _j =150°C		6,17 9,39		mWs
Turn-off energy loss per pulse	E _{off}					T _j =25°C T _j =150°C		4,01 6,99		
Input capacitance	C _{ies}	f=1MHz	0	25		T _j =25°C		4400		pF
Output capacitance	C _{oss}							290		
Reverse transfer capacitance	C _{rss}							235		
Gate charge	Q _{Gate}		±15			T _j =25°C		290		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						0,75		K/W
Thermal resistance chip to case per chip	R _{thJC}									
Inverter Diode										
Diode forward voltage	V _F				75	T _j =25°C T _j =150°C	1	1,78 1,72	2,3	V
Peak reverse recovery current	I _{RRM}	Rgon=4 Ω	±15	600	75	T _j =25°C T _j =150°C		69,44 86,2		A
Reverse recovery time	t _{rr}					T _j =25°C T _j =150°C		275,1 457		ns
Reverse recovered charge	Q _{rr}					T _j =25°C T _j =150°C		6,62 14,08		μC
Peak rate of fall of recovery current	di(rec)max /dt					T _j =25°C T _j =150°C		1859 724		A/μs
Reverse recovered energy	E _{rec}					T _j =25°C T _j =150°C		2,29 5,22		mWs
Thermal resistance chip to heatsink per chip	R _{thJH}					Thermal grease thickness≤50um λ = 1 W/mK				
Thermal resistance chip to case per chip	R _{thJC}									

Output Inverter

Figure 1 Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

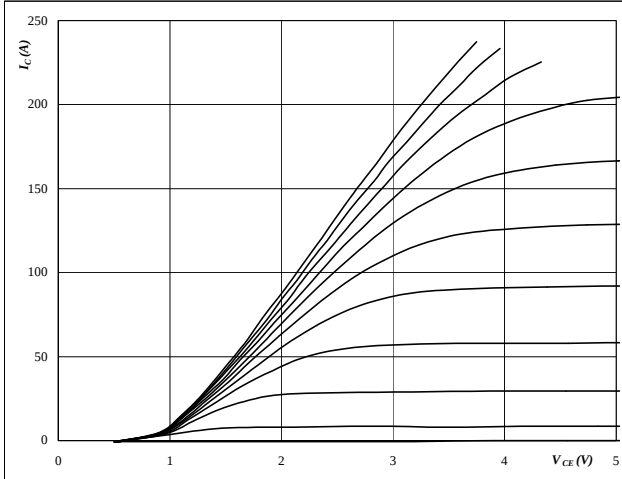

At
 $t_p = 350 \mu s$
 $T_J = 25^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2 Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

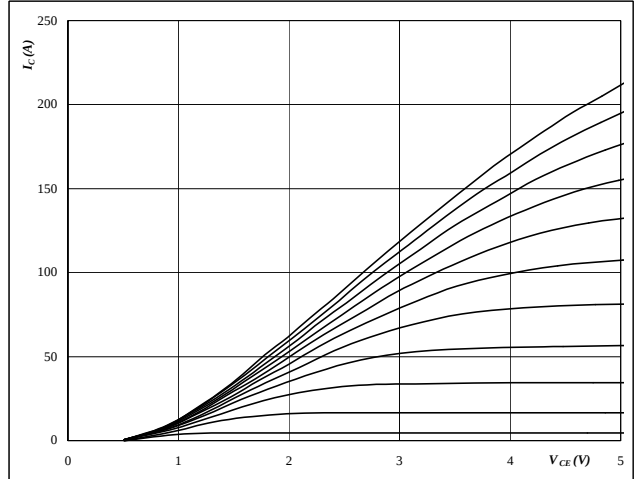
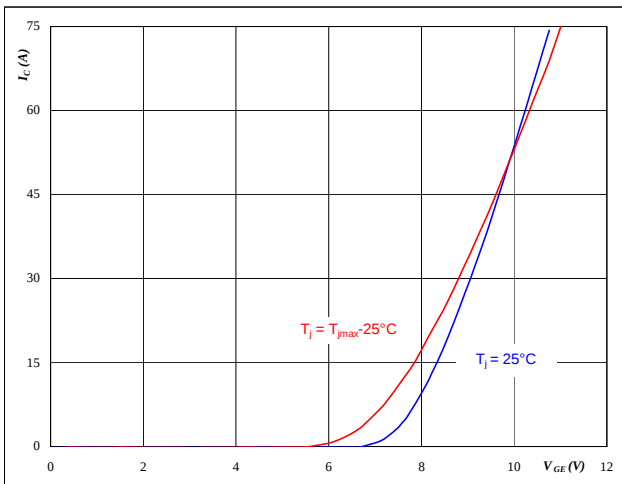

At
 $t_p = 350 \mu s$
 $T_J = 150^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3 Output inverter IGBT

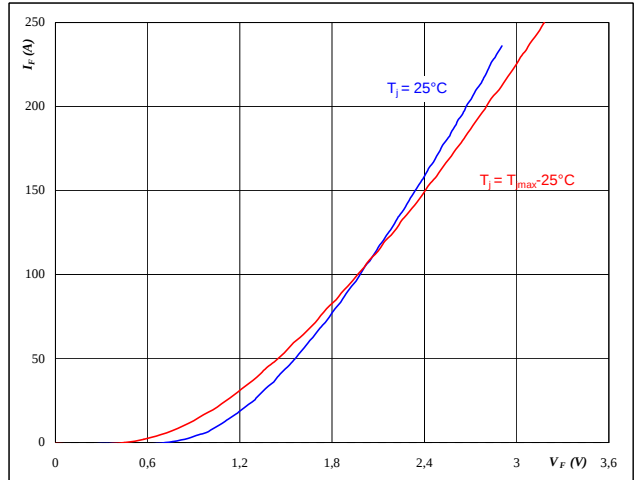
Typical transfer characteristics

$$I_C = f(V_{GE})$$


At
 $t_p = 350 \mu s$
 $V_{CE} = 10 V$
Figure 4 Output inverter FRED

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

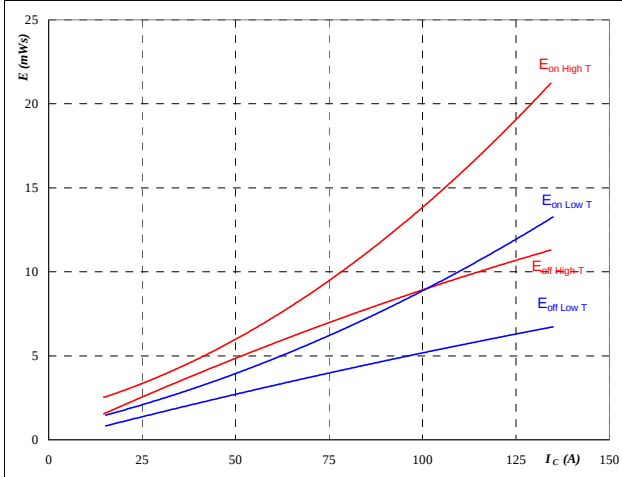

At
 $t_p = 350 \mu s$

Output Inverter

Figure 5 Output inverter IGBT

Typical switching energy losses
as a function of collector current

$$E = f(I_C)$$



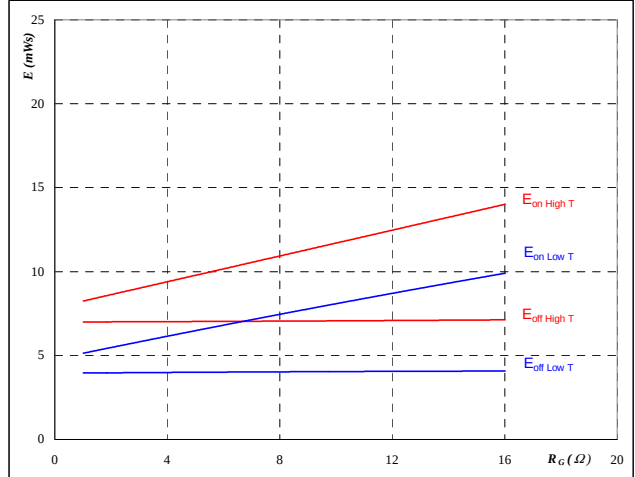
With an inductive load at

$T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω
 $R_{goff} = 4$ Ω

Figure 6 Output inverter IGBT

Typical switching energy losses
as a function of gate resistor

$$E = f(R_G)$$



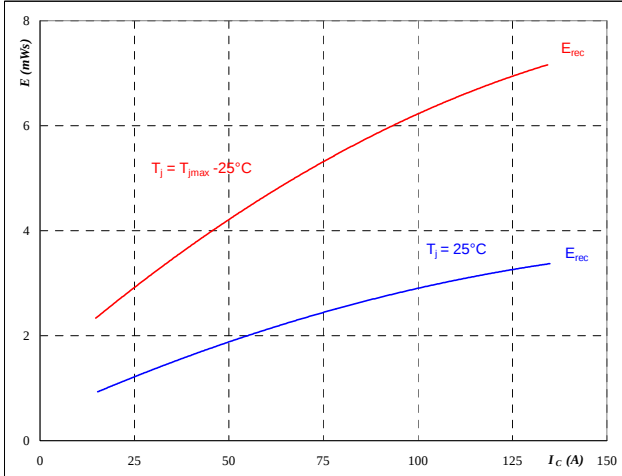
With an inductive load at

$T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 75$ A

Figure 7 Output inverter IGBT

Typical reverse recovery energy loss
as a function of collector current

$$E_{rec} = f(I_C)$$



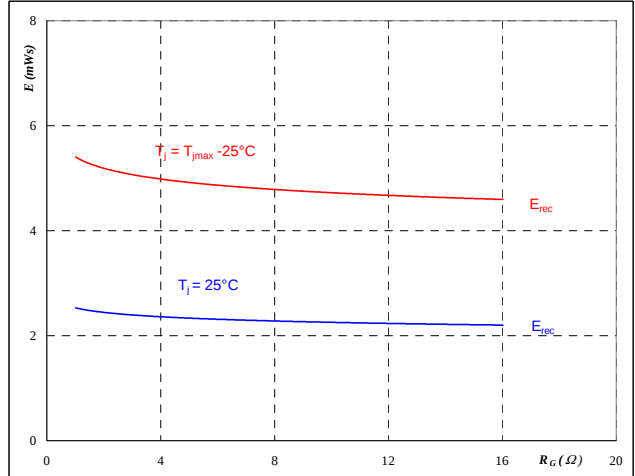
With an inductive load at

$T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω

Figure 8 Output inverter IGBT

Typical reverse recovery energy loss
as a function of gate resistor

$$E_{rec} = f(R_G)$$



With an inductive load at

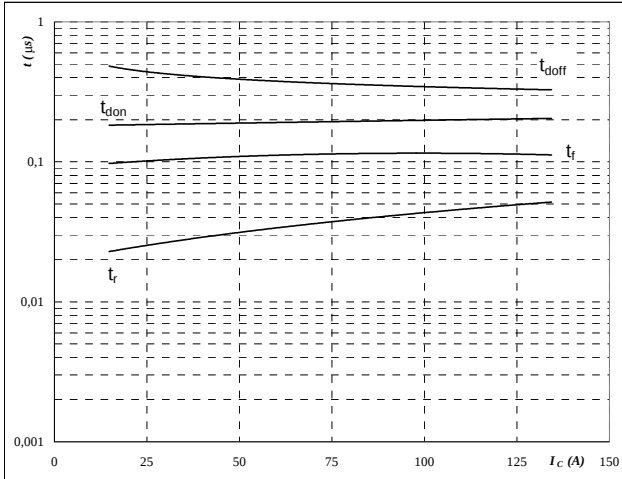
$T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 75$ A

Output Inverter

Figure 9 Output inverter IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



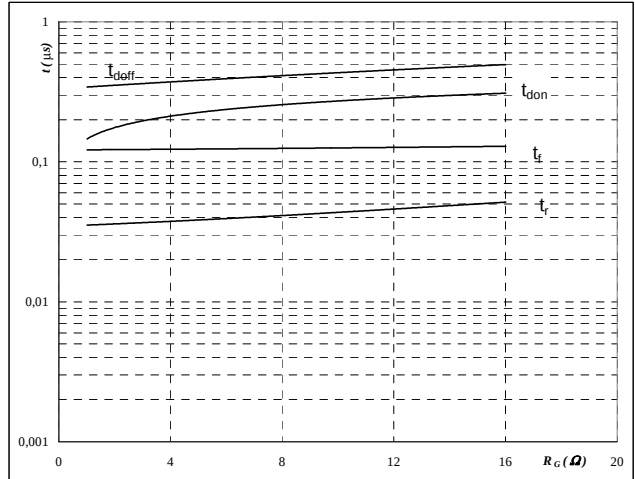
With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω
$R_{goff} =$	4	Ω

Figure 10 Output inverter IGBT

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



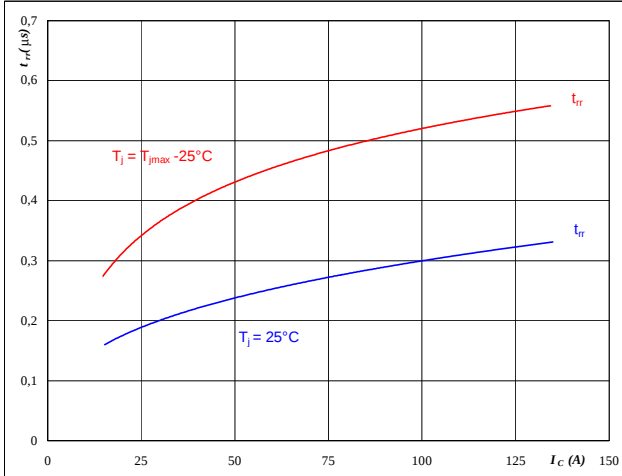
With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	75	A

Figure 11 Output inverter FRED

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



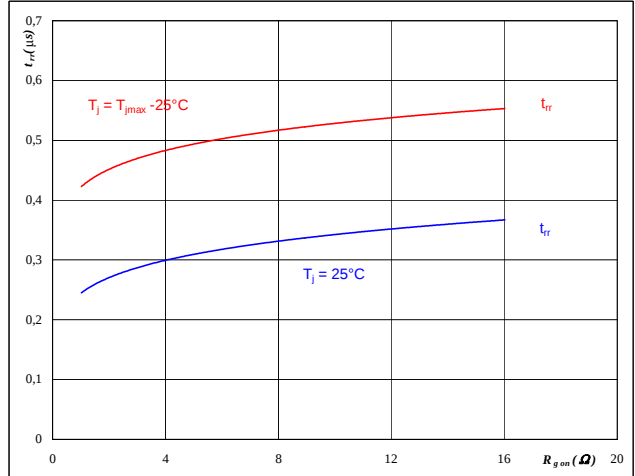
At

$T_J =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω

Figure 12 Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

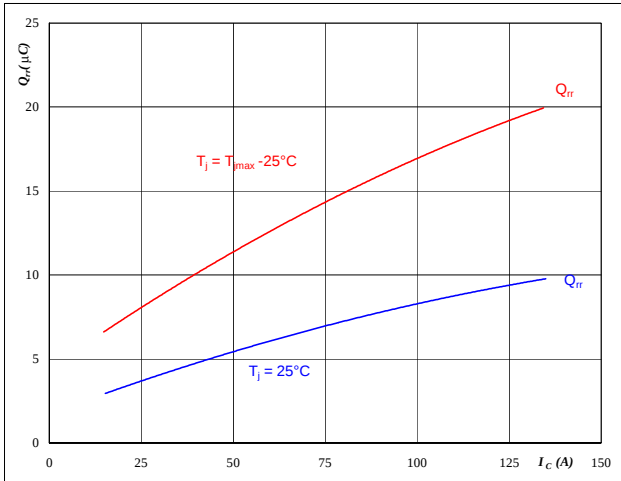
$T_J =$	25/150	°C
$V_R =$	600	V
$I_F =$	75	A
$V_{GE} =$	±15	V

Output Inverter

Figure 13 Output inverter FRED

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

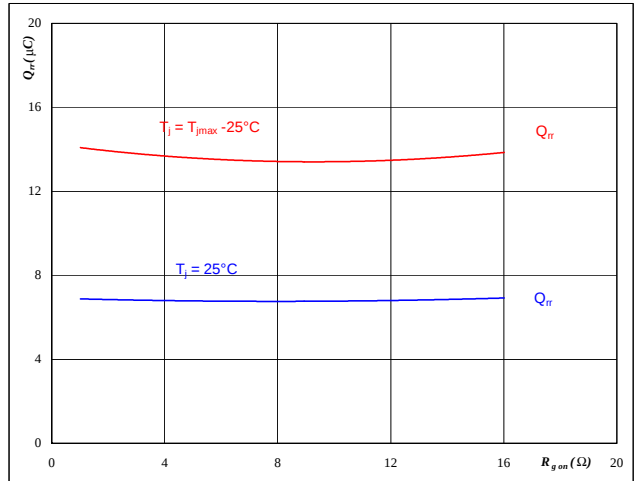


At
 $T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω

Figure 14 Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

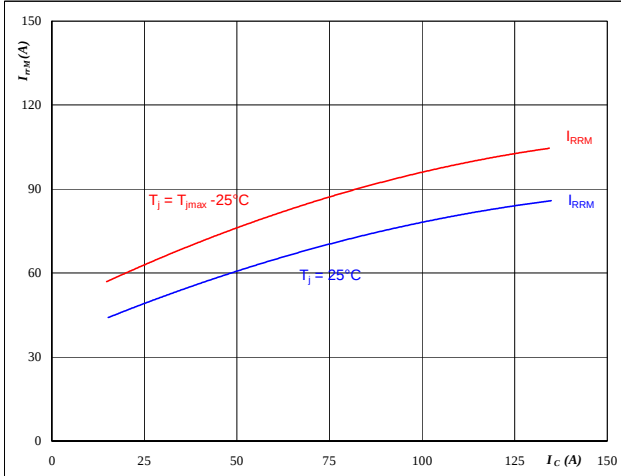


At
 $T_J = 25/150$ °C
 $V_R = 600$ V
 $I_F = 75$ A
 $V_{GE} = \pm 15$ V

Figure 15 Output inverter FRED

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

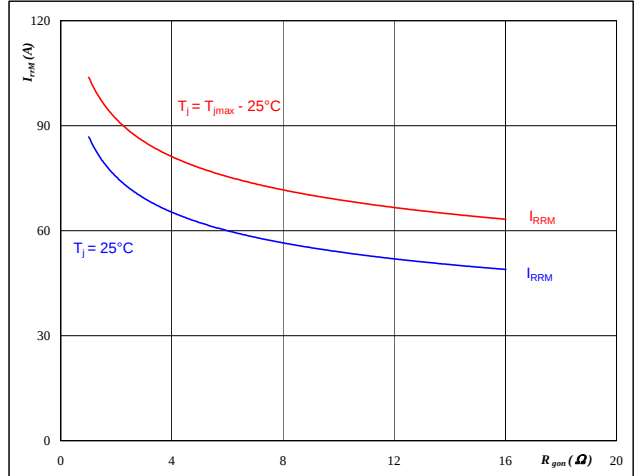


At
 $T_J = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω

Figure 16 Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$



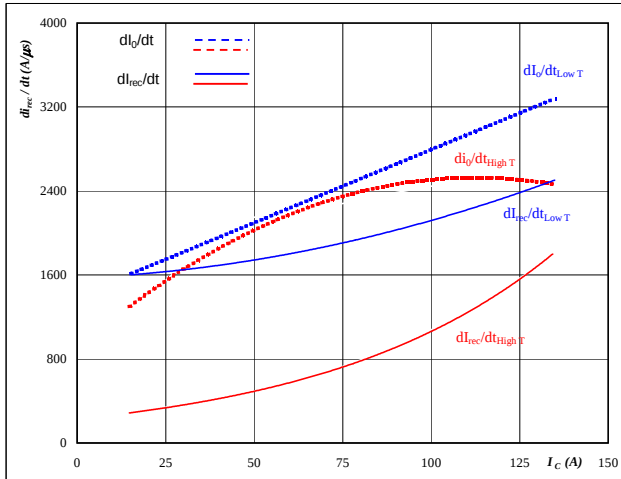
At
 $T_J = 25/150$ °C
 $V_R = 600$ V
 $I_F = 75$ A
 $V_{GE} = \pm 15$ V

Output Inverter

Figure 17 Output inverter FRED

Typical rate of fall of forward
and reverse recovery current as a
function of collector current

$$dI_o/dt, dI_{rec}/dt = f(I_C)$$



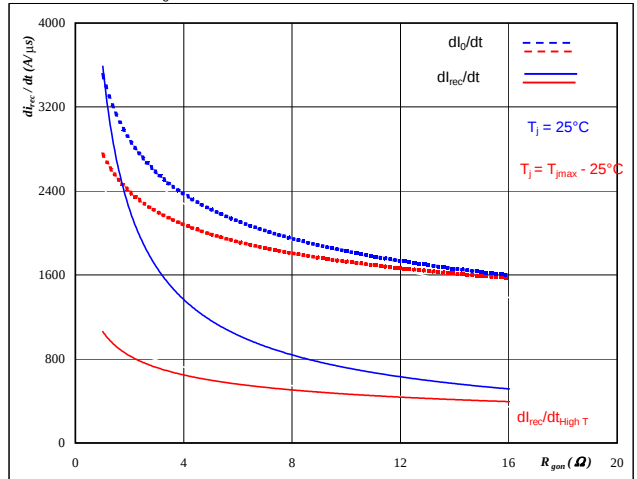
At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω

Figure 18 Output inverter FRED

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor

$$dI_o/dt, dI_{rec}/dt = f(R_{gon})$$



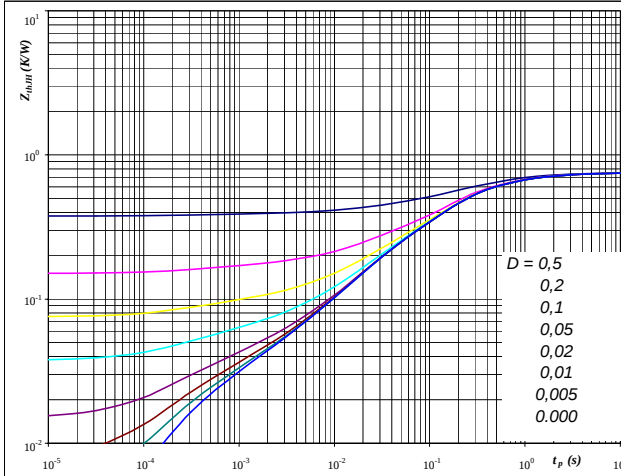
At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	75	A
$V_{GE} =$	±15	V

Figure 19 Output inverter IGBT

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$D =$	t_p / T	
$R_{thJH} =$	0,75	K/W

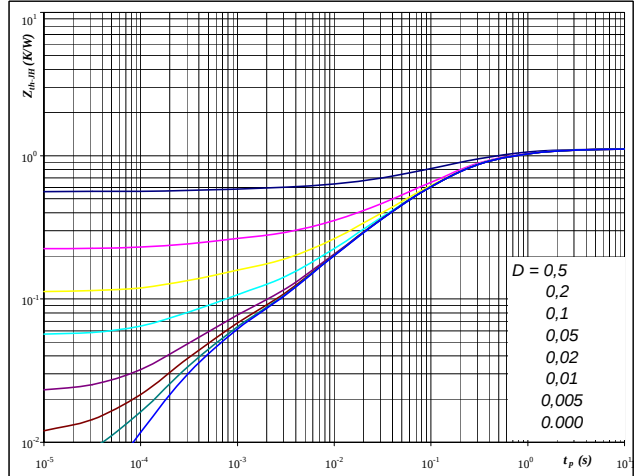
IGBT thermal model values

R (C/W)	Tau (s)
0,05	4,2E+00
0,17	7,6E-01
0,39	1,7E-01
0,11	2,2E-02
0,02	2,2E-03
0,02	2,9E-04

Figure 20 Output inverter FRED

FRED transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$D =$	t_p / T	
$R_{thJH} =$	1,12	K/W

FRED thermal model values

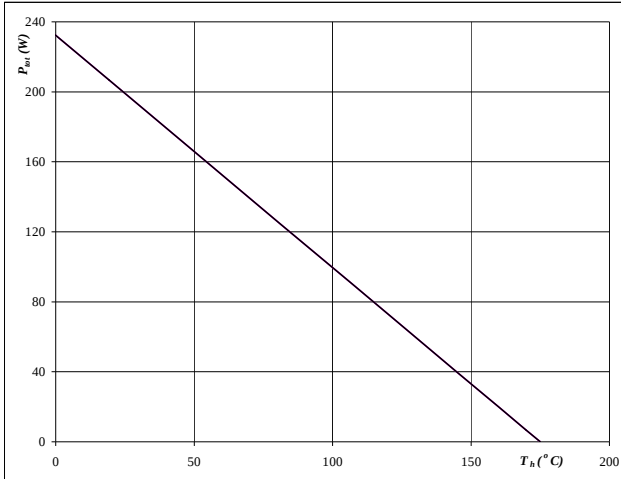
R (C/W)	Tau (s)
0,03	9,3E+00
0,17	1,1E+00
0,57	1,8E-01
0,24	3,1E-02
0,07	5,5E-03
0,04	4,0E-04

Output Inverter

Figure 21 Output inverter IGBT

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

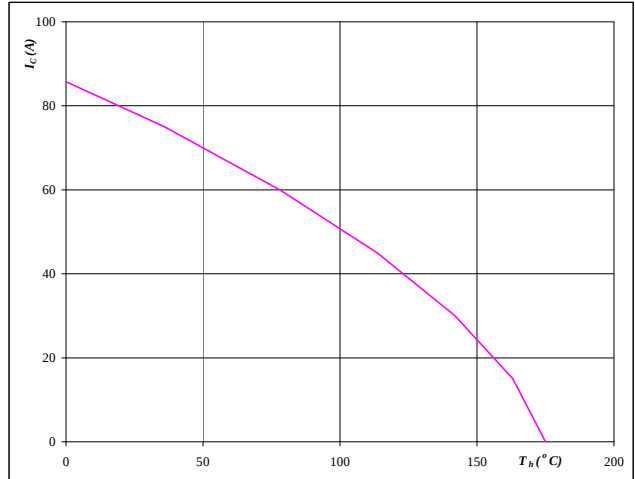


At $T_j = 175$ °C
 — single heating
 — overall heating

Figure 22 Output inverter IGBT

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$

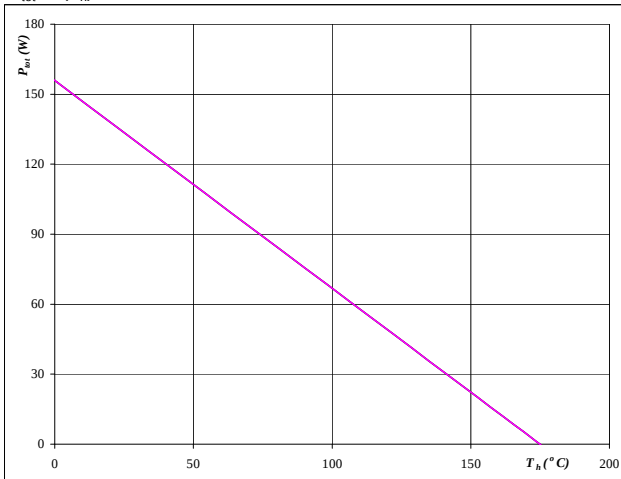


At $T_j = 175$ °C
 $V_{GE} = 15$ V

Figure 23 Output inverter FRED

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

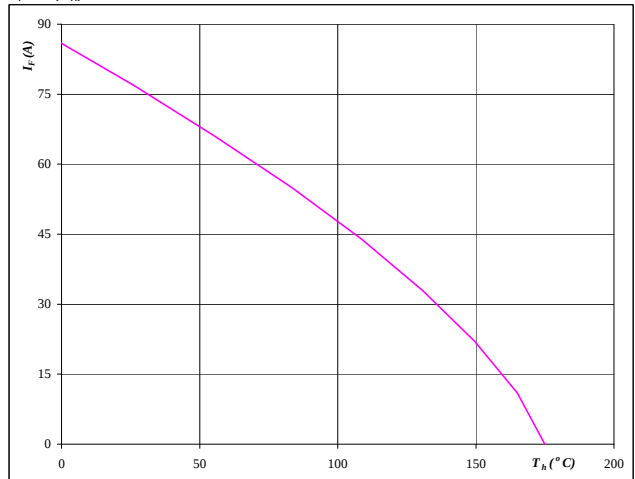


At $T_j = 175$ °C
 — single heating
 — overall heating

Figure 24 Output inverter FRED

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$



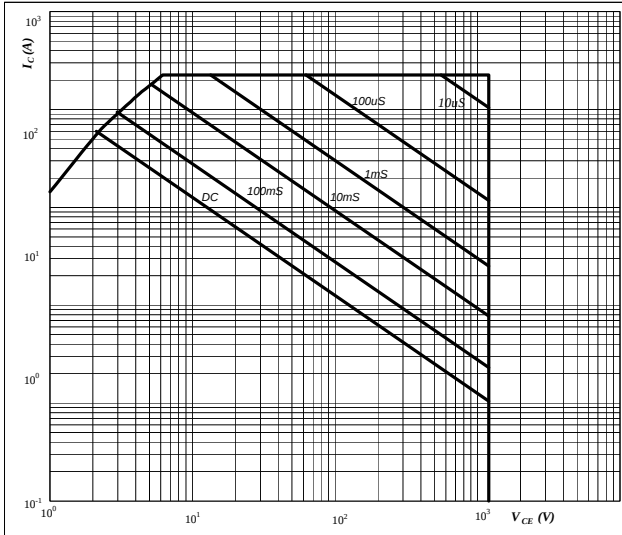
At $T_j = 175$ °C

Output Inverter

Figure 25 Output inverter IGBT

Safe operating area as a function
of collector-emitter voltage

$$I_C = f(V_{CE})$$



At

D = single pulse

$T_h = 80$ °C

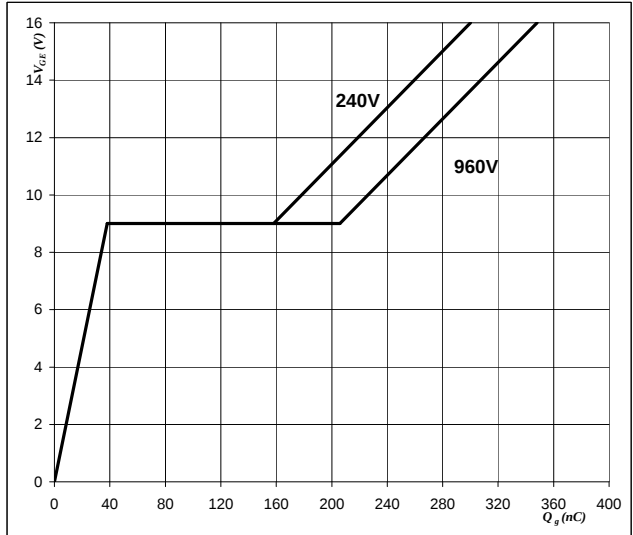
$V_{GE} = \pm 15$ V

$T_j = T_{jmax}$ °C

Figure 26 Output inverter IGBT

Gate voltage vs Gate charge

$$V_{GE} = f(Q_{GE})$$



At

$I_C = 75$ A

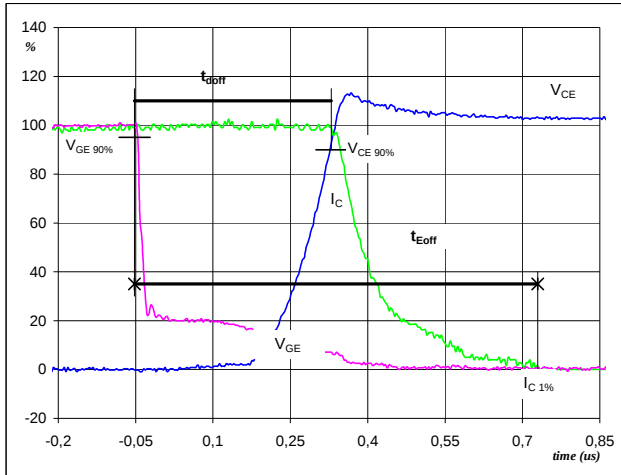
Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	4 Ω
R_{goff}	=	4 Ω

Figure 1 Output inverter IGBT

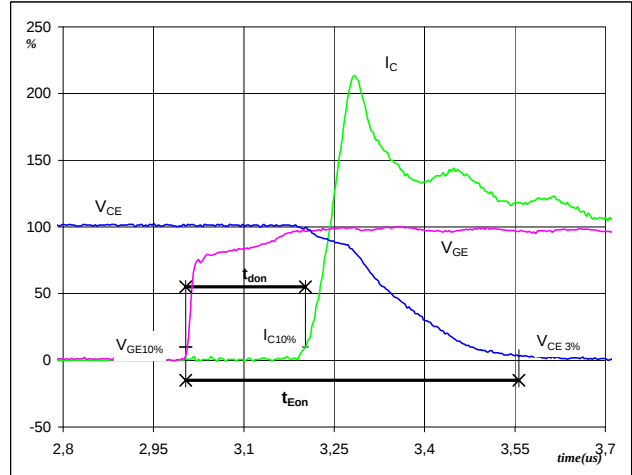
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 (t_{Eoff} = integrating time for E_{off})



$V_{GE}(0\%)$	=	-15	V
$V_{GE}(100\%)$	=	15	V
$V_C(100\%)$	=	600	V
$I_C(100\%)$	=	75	A
t_{doff}	=	0,37	μs
t_{Eoff}	=	0,78	μs

Figure 2 Output inverter IGBT

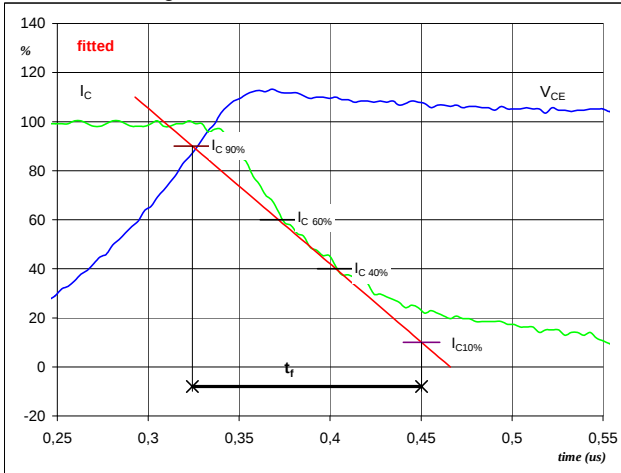
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 (t_{Eon} = integrating time for E_{on})



$V_{GE}(0\%)$	=	-15	V
$V_{GE}(100\%)$	=	15	V
$V_C(100\%)$	=	600	V
$I_C(100\%)$	=	75	A
t_{don}	=	0,20	μs
t_{Eon}	=	0,55	μs

Figure 3 Output inverter IGBT

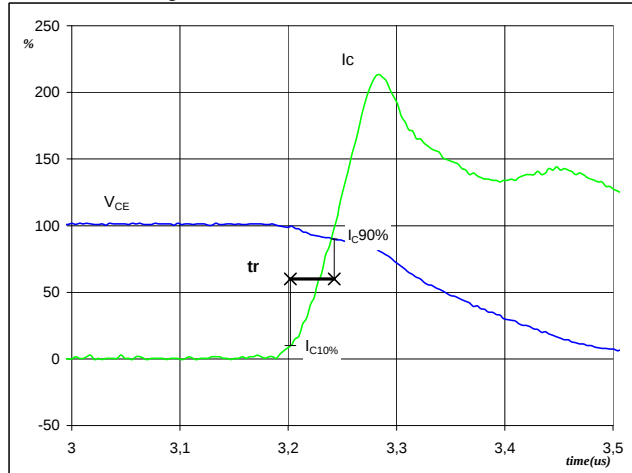
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%)$	=	600	V
$I_C(100\%)$	=	75	A
t_f	=	0,12	μs

Figure 4 Output inverter IGBT

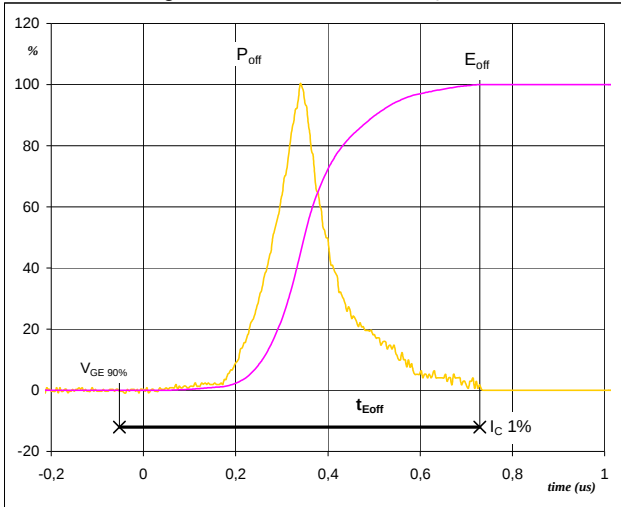
Turn-on Switching Waveforms & definition of t_r



$V_C(100\%)$	=	600	V
$I_C(100\%)$	=	75	A
t_r	=	0,04	μs

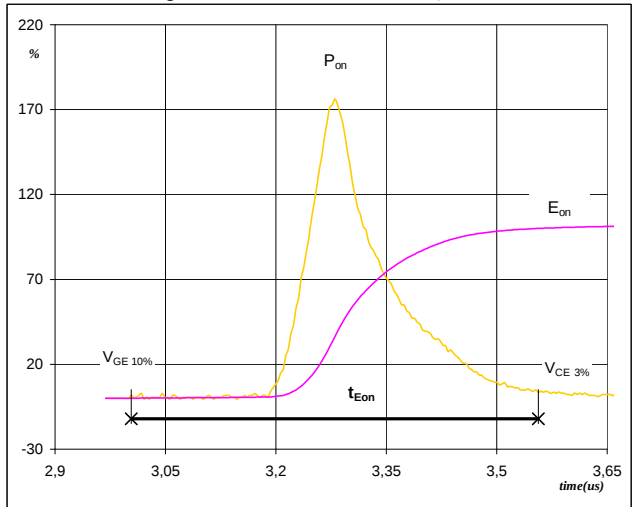
Switching Definitions Output Inverter

Figure 5 Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


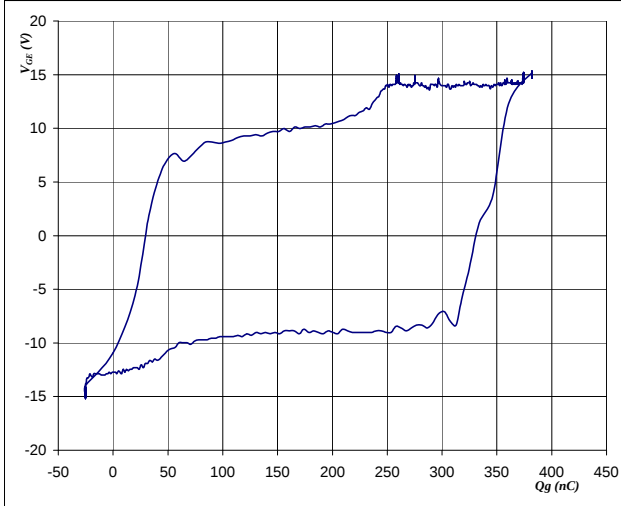
$P_{off}(100\%) = 44,97$ kW
 $E_{off}(100\%) = 7,03$ mJ
 $t_{Eoff} = 0,78$ μ s

Figure 6 Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{Eon}


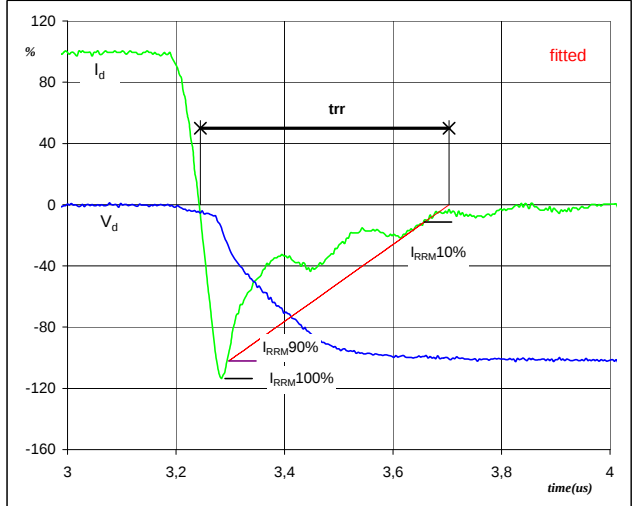
$P_{on}(100\%) = 44,97$ kW
 $E_{on}(100\%) = 9,36$ mJ
 $t_{Eon} = 0,55$ μ s

Figure 7 Output inverter FRED

Gate voltage vs Gate charge (measured)


$V_{GEoff} = -15$ V
 $V_{GEon} = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 75$ A
 $Q_g = 6601,20$ nC

Figure 8 Output inverter IGBT

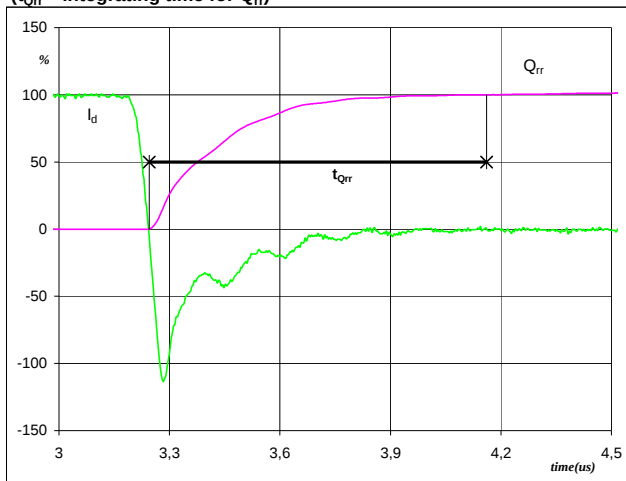
Turn-off Switching Waveforms & definition of t_{tr}


$V_d(100\%) = 600$ V
 $I_d(100\%) = 75$ A
 $I_{RRM}(100\%) = -85$ A
 $t_{tr} = 0,46$ μ s

Switching Definitions Output Inverter

Figure 9 Output inverter FRED

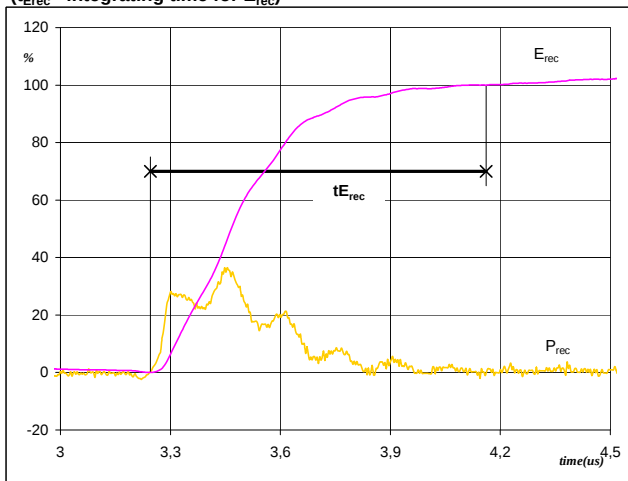
Turn-on Switching Waveforms & definition of t_{Qrr}
 (t_{Qrr} = integrating time for Q_{rr})



I_d (100%) = 75 A
 Q_{rr} (100%) = 13,41 μ C
 t_{Qrr} = 0,92 μ s

Figure 10 Output inverter FRED

Turn-on Switching Waveforms & definition of t_{Erec}
 (t_{Erec} = integrating time for E_{rec})

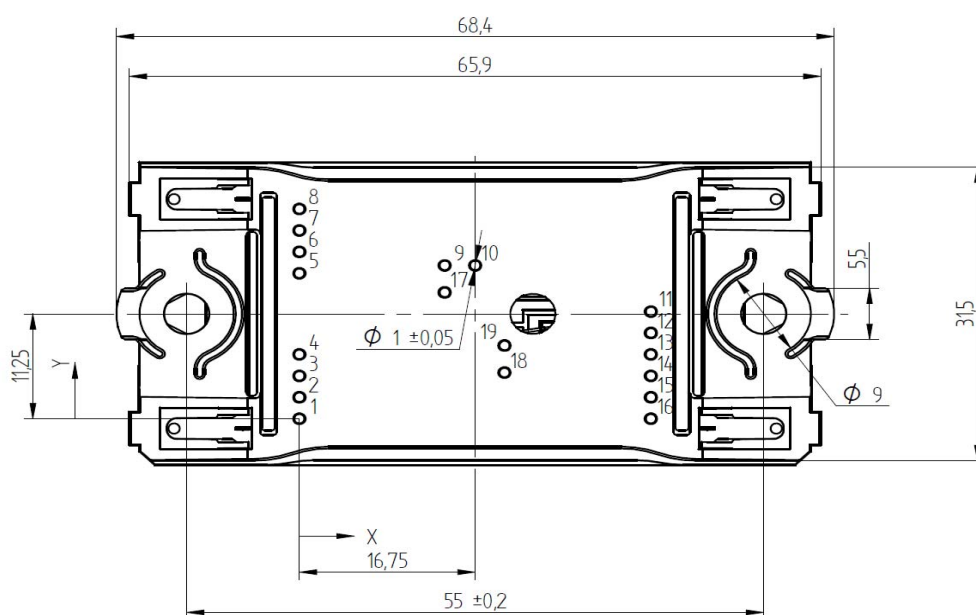


P_{rec} (100%) = 44,97 kW
 E_{rec} (100%) = 4,88 mJ
 t_{Erec} = 0,92 μ s

Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	10-FZ122PA075SC-P998F08	P998F08	P998F08
without thermal paste 17mm housing	10-F0122PA075SC-P998F09	P998F09	P998F09

Pin table		
Pin	X	Y
1	0	0
2	0	2,3
3	0	4,6
4	0	6,9
5	0	15,6
6	0	17,9
7	0	20,2
8	0	22,5
9	13,85	16,45
10	16,75	16,45
11	33,5	11,5
12	33,5	9,2
13	33,5	6,9
14	33,5	4,6
15	33,5	2,3
16	33,5	0
17	13,85	13,55
18	19,55	4,95
19	19,55	7,85



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.

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