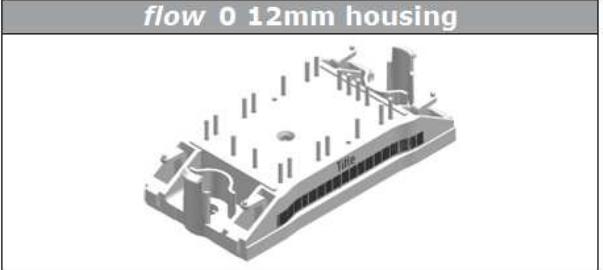
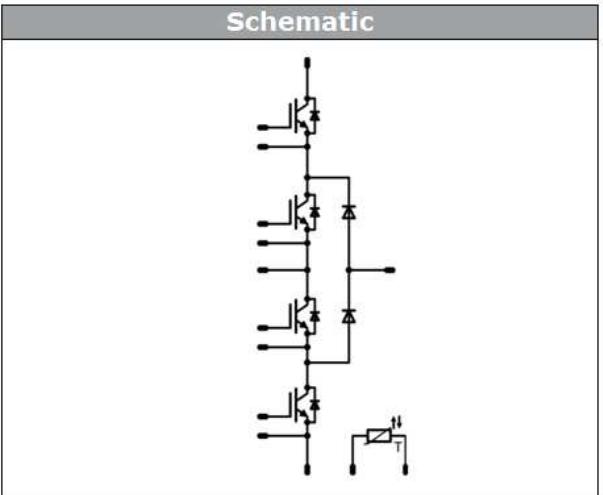




Vincotech

flow NPC 0		650 V / 60 A
Features		
	<ul style="list-style-type: none">• High Efficiency three-level half-bridge• High efficiency IGBT• Neutral point-Clamped inverter• Clip-In PCB mounting• Low Inductance Layout	
Target applications		Schematic
	<ul style="list-style-type: none">• Solar• UPS	
Types		
	<ul style="list-style-type: none">• 10-FZ07NIA060SM-P926F43	

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Buck Switch\ Out. Boost Switch				
Collector-emitter voltage	V_{CES}		650	V
Collector current	I_C	$T_j = T_{j,\max}$	53	A
Repetitive peak collector current	I_{CRM}	t_p limited by $T_{j,\max}$	180	A
Total power dissipation	P_{tot}	$T_j = T_{j,\max}$	97	W
Gate-emitter voltage	V_{GES}		± 20	V
Maximum Junction Temperature	$T_{j,\max}$		175	$^\circ\text{C}$



10-FZ07NIA060SM-P926F43

datasheet

Vincotech

Parameter	Symbol	Conditions	Value	Unit
Buck Diode\ Out. Boost Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		650	V
Continuous (direct) forward current	I_F	$T_j=T_{jmax}$	45	A
Surge (non-repetitive) forward current	I_{FSM}	50Hz Single Half Sine Wave	500	A
Total power dissipation	P_{tot}	$T_j=T_{jmax}$	76	W
Maximum Junction Temperature	T_{jmax}		175	°C

Parameter	Symbol	Conditions	Value	Unit
Out. Boost Inv. Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		650	V
Continuous (direct) forward current	I_F	$T_j=T_{jmax}$	65	A
Repetitive peak forward current	I_{FRM}		150	A
Total power dissipation	P_{tot}	$T_j=T_{jmax}$	85	W
Maximum Junction Temperature	T_{jmax}		175	°C

Module Properties

Parameter	Symbol	Conditions	Value	Unit
Thermal Properties				
Storage temperature	T_{stg}		-40...+125	°C
Operation Junction Temperature	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC voltage	$t_p=2s$	4000	V
Creepage distance				min 12,7	mm
Clearance				min 9,75	mm
Comparative Tracking Index	CTI			>200	



Vincotech

Characteristic Values

Buck Switch

Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max	

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}$			0,0006	25 125	3,3	4	4,7	V
Collector-emitter saturation voltage	V_{CESat}		15		60	25 125 150	1,67 1,80 1,84		2,22	V
Collector-emitter cut-off current	I_{CES}		0	650		25 125			40	µA
Gate-emitter leakage current	I_{GES}		20	0		25 125			120	nA
Internal gate resistance	r_g						none			Ω
Input capacitance	C_{ies}	$f=1$ MHz	0	25	25		2100			pF
Reverse transfer capacitance	C_{res}									

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	Phase-Change Material $\lambda=3,4W/mK$						0,98		K/W
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IGBT Switching

Turn-on delay time	$t_{d(on)}$	$R_{goff} = 16 \Omega$ $R_{gon} = 16 \Omega$	± 15	350	60	25 125 150		115 117 117		ns
Rise time	t_r					25 125 150		18 19 20		
Turn-off delay time	$t_{d(off)}$					25 125 150		95 108 111		
Fall time	t_f					25 125 150		5 8 9		
Turn-on energy (per pulse)	E_{on}					25 125 150		1,128 1,485 1,571		
Turn-off energy (per pulse)	E_{off}					25 125 150		0,220 0,415 0,465		



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Buck Diode

Parameter	Symbol	Conditions				Value			Unit
				V_r [V]	I_F [A]	T_j [°C]	Min	Typ	
Static									
Forward voltage	V_F				50	25 125 150		2,30 1,94 1,86	2,6
Reverse leakage current	I_r			665		25 150			10 -
Thermal									
Thermal resistance junction to sink	$R_{th(j-s)}$	Phase-Change Material $\lambda=3,4\text{W/mK}$						1,25	K/W
FWD Switching									
Peak recovery current	I_{RRM}	$di/dt = 2943 \text{ A}/\mu\text{s}$ $di/dt = 2875 \text{ A}/\mu\text{s}$ $di/dt = 2730 \text{ A}/\mu\text{s}$	± 15	350	60	25 125 150		23 32 36	A
Reverse recovery time	t_{rr}					25 125 150		30 124 140	ns
Recovered charge	Q_r					25 125 150		0,692 2,065 2,511	μC
Reverse recovered energy	E_{rec}					25 125 150		0,097 0,340 0,422	mWs
Peak rate of fall of recovery current	$(di_R/dt)_{max}$					25 125 150		2675 480 691	A/ μs



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Out. Boost Switch

Parameter	Symbol	Conditions				Value			Unit
		V_{GE} [V]	V_{CE} [V]	I_c [A]	T_j [°C]	Min	Typ	Max	

Static

Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}$		0,0006	25 125	3,3	4	4,7	V
Collector-emitter saturation voltage	V_{CEsat}		15	60	25 125 150	1,67 1,80 1,84	2,22		V
Collector-emitter cut-off current	I_{CES}		0	650	25 125			40	μA
Gate-emitter leakage current	I_{GES}		20	0	25 125			120	nA
Internal gate resistance	r_g						none		Ω
Input capacitance	C_{ies}	f=1 MHz	0	25	25	2100			pF
Reverse transfer capacitance	C_{res}								

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	Phase-Change Material $\lambda=3,4\text{W/mK}$						0,98		K/W
-------------------------------------	---------------	---	--	--	--	--	--	------	--	-----

IGBT Switching

Turn-on delay time	$t_{d(on)}$	$R_{goff} = 16 \Omega$ $R_{gon} = 16 \Omega$	± 15	350	60	25 125 150		119 118 119		ns
Rise time	t_r					25 125 150		17 20 21		
Turn-off delay time	$t_{d(off)}$					25 125 150		99 112 115		
Fall time	t_f					25 125 150		5 11 14		
Turn-on energy (per pulse)	E_{on}					25 125 150		1,133 1,453 1,543		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		0,271 0,490 0,549		



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Out. Boost Diode

Parameter	Symbol	Conditions				Value			Unit
				V_r [V]	I_F [A]	T_j [°C]	Min	Typ	
Static									
Forward voltage	V_F				50	25 125 150		2,30 1,94 1,86	2,6
Reverse leakage current	I_r			665		25 150			10 -
Thermal									
Thermal resistance junction to sink	$R_{th(j-s)}$	Phase-Change Material $\lambda=3,4\text{W/mK}$						1,25	K/W
FWD Switching									
Peak recovery current	I_{RRM}	$di/dt = 3120 \text{ A}/\mu\text{s}$ $di/dt = 2136 \text{ A}/\mu\text{s}$ $di/dt = 2000 \text{ A}/\mu\text{s}$	± 15	350	60	25 125 150		24 34 37	A
Reverse recovery time	t_{rr}					25 125 150		87 119 130	ns
Recovered charge	Q_r					25 125 150		0,701 2,072 2,592	μC
Reverse recovered energy	E_{rec}					25 125 150		0,107 0,371 0,488	mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		2742 398 488	$\text{A}/\mu\text{s}$



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Out. Boost Inv. Diode

Parameter	Symbol	Conditions				Value			Unit
		V_r [V]	I_F [A]	T_j [°C]	Min	Typ	Max		
Static									
Forward voltage	V_F			75	25 125 150		1,46 1,42 1,40	1,9	V
Reverse leakage current	I_r		650		25 150			27 -	µA
Thermal									
Thermal resistance junction to sink	$R_{th(j-s)}$	Phase-Change Material $\lambda=3,4\text{W/mK}$					1,12		K/W

Thermistor

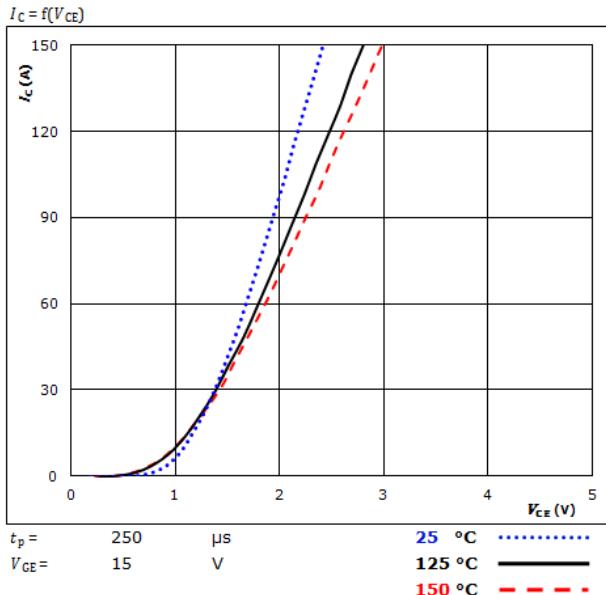
Parameter	Symbol	Conditions				Value			Unit
		V_{GE} [V]	V_{CE} [V]	I_c [A]	T_j [°C]	Min	Typ	Max	
Rated resistance	R				25		21,5		kΩ
Deviation of R100	$\Delta_{R/R}$	R100=1486 Ω			100	-4,5		+4,5	%
Power dissipation	P				25		210		mW
Power dissipation constant					25		3,5		mW/K
B-value	$B_{(25/50)}$				25		3884		K
B-value	$B_{(25/100)}$				25		3964		K
Vincotech NTC Reference								F	



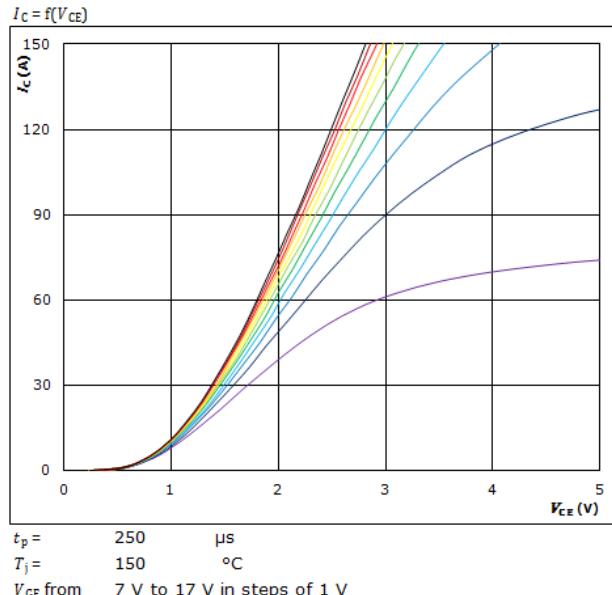
Vincotech

Buck Switch\Out. Boost Switch Characteristics

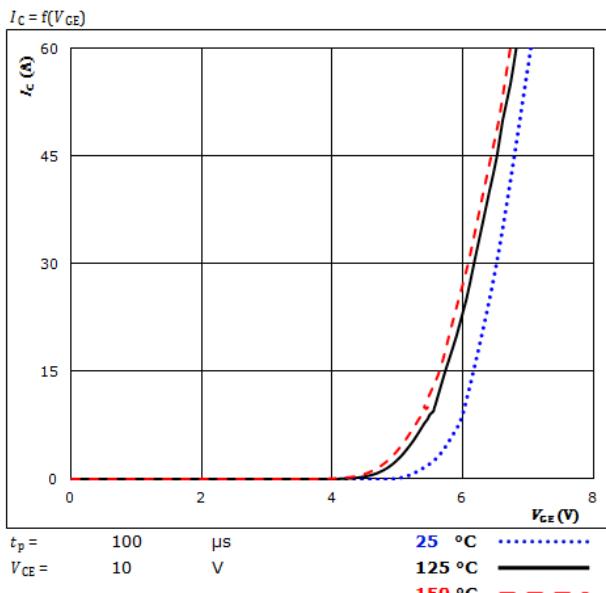
Typical output characteristics IGBT



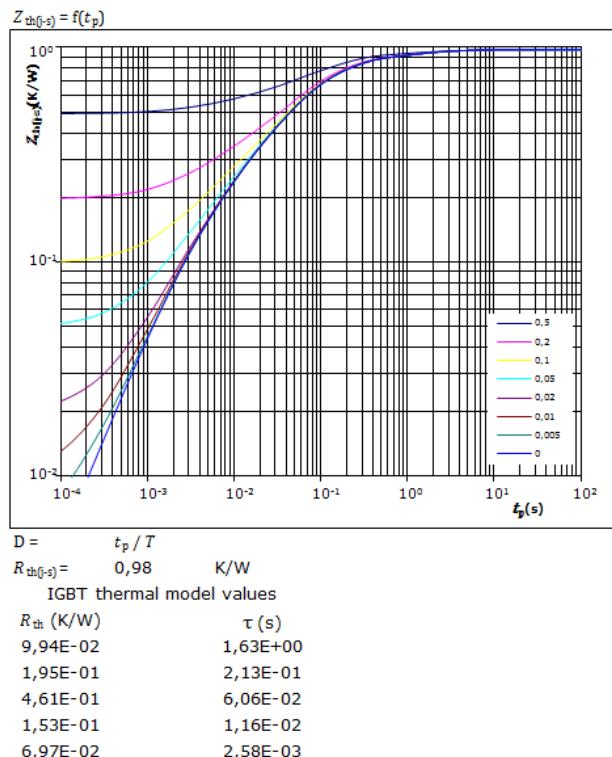
Typical output characteristics IGBT



Typical transfer characteristics IGBT



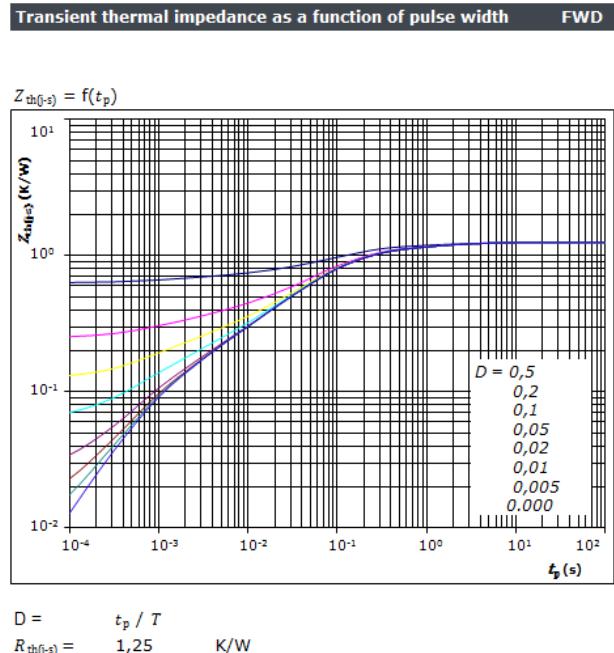
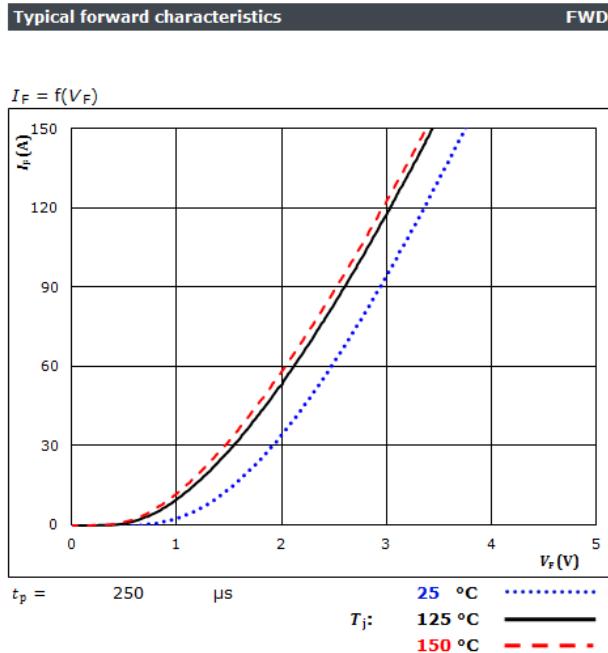
Transient Thermal Impedance as function of Pulse duration IGBT





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Buck Diode\Out. Boost Diode Characteristics



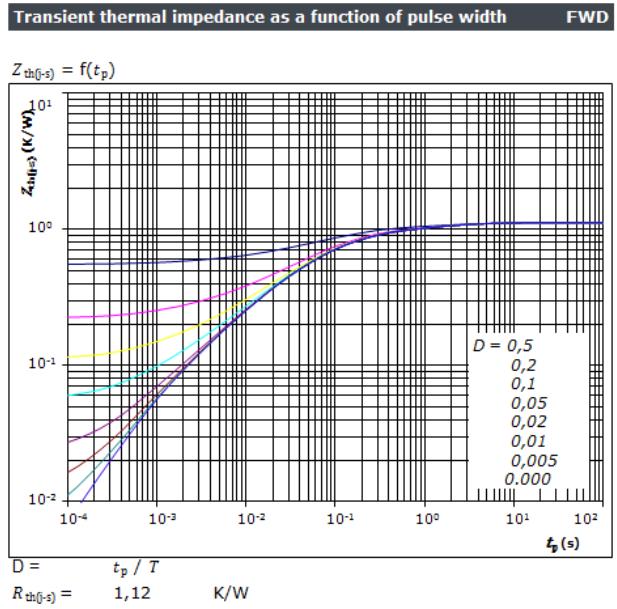
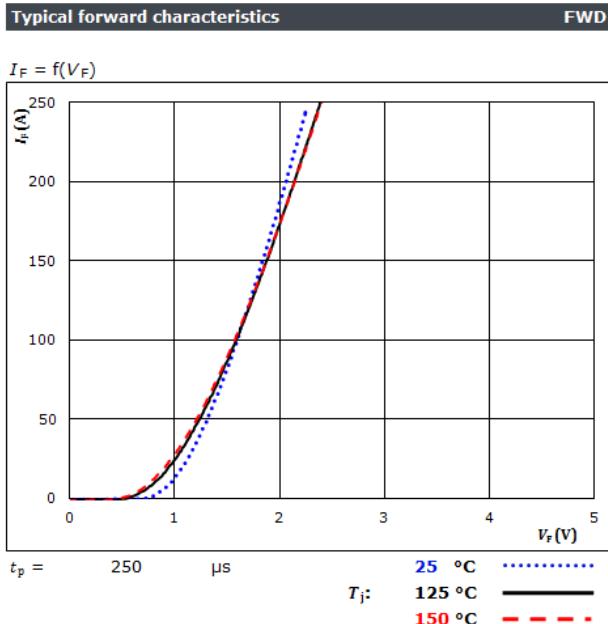
FWD thermal model values

R (K/W)	τ (s)
8,28E-02	3,17E+00
1,76E-01	6,05E-01
5,29E-01	1,07E-01
2,56E-01	3,44E-02
1,19E-01	6,15E-03
8,99E-02	8,86E-04



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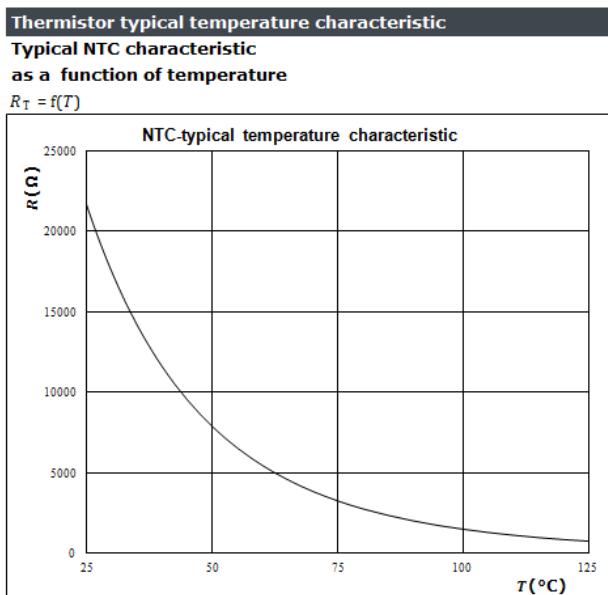
Out. Boost Inv. Diode Characteristics



FWD thermal model values

$R (\text{K/W})$	$\tau (\text{s})$
6,88E-02	4,09E+00
1,60E-01	8,43E-01
3,68E-01	1,30E-01
3,14E-01	4,09E-02
1,57E-01	8,78E-03
4,73E-02	1,11E-03

Thermistor Characteristics





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Buck Switching Characteristics

Figure 1. IGBT

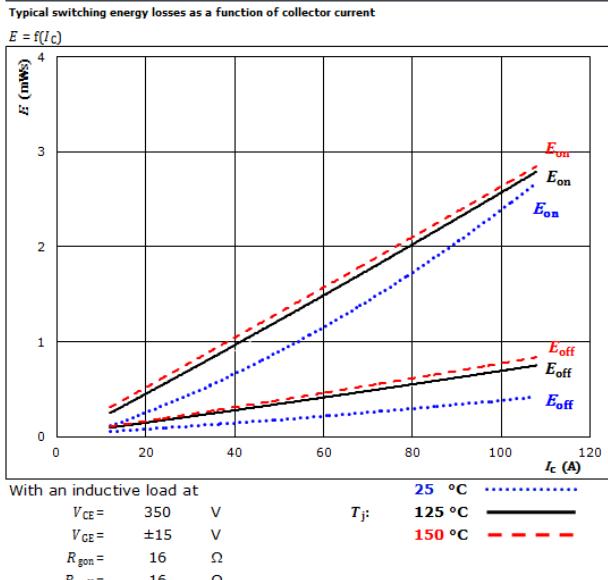


Figure 2. IGBT

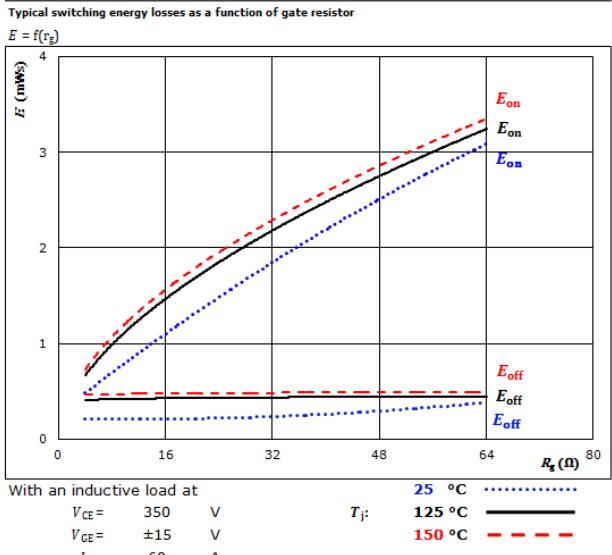


Figure 3. FWD

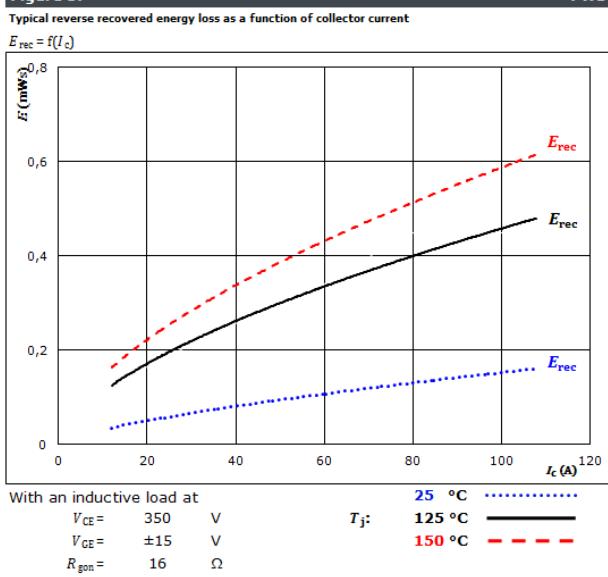
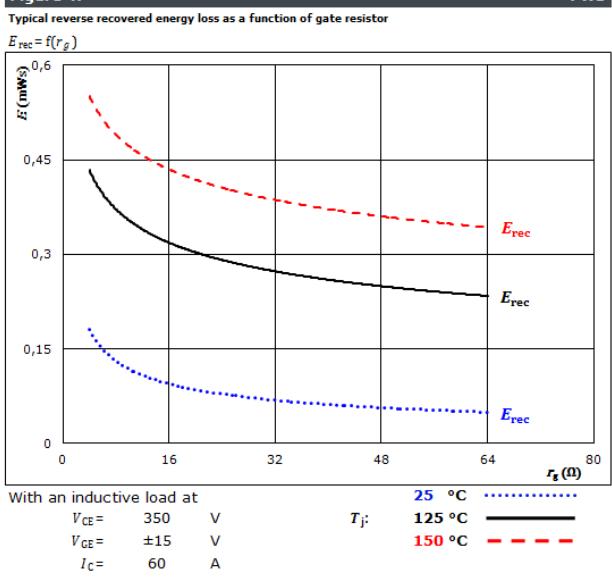


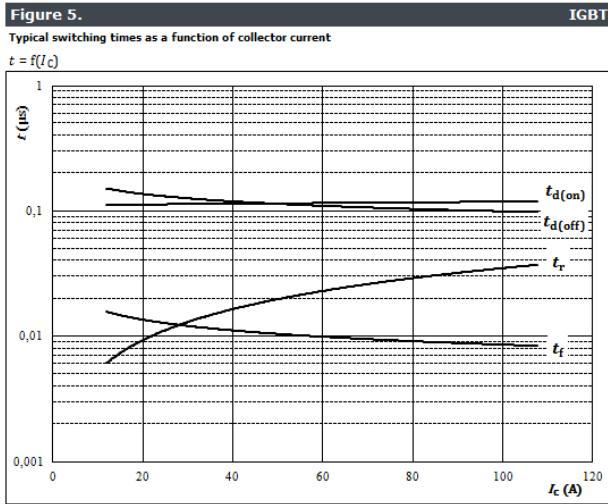
Figure 4. FWD





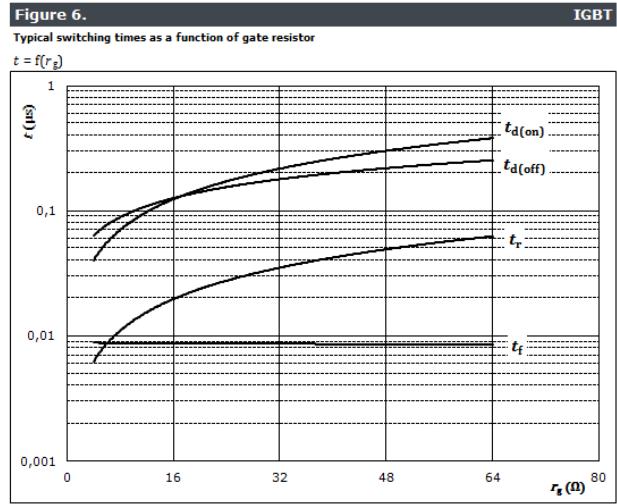
Vincotech

Buck Switching Characteristics



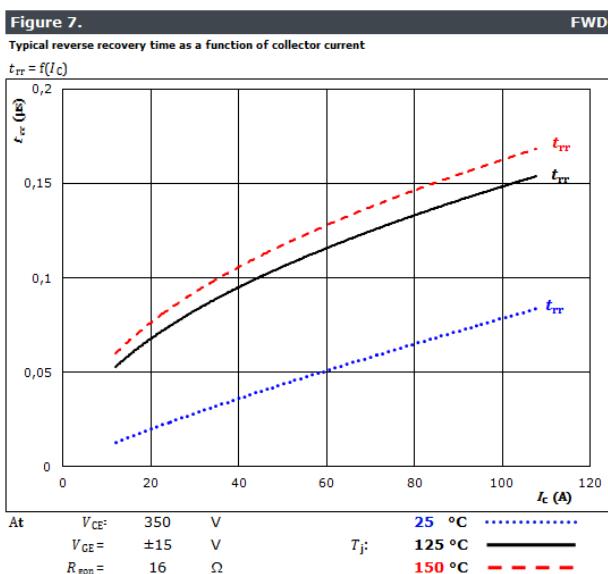
With an inductive load at

$T_j = 150^\circ\text{C}$
 $V_{CE} = 350\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $R_{gon} = 16\Omega$
 $R_{goff} = 16\Omega$



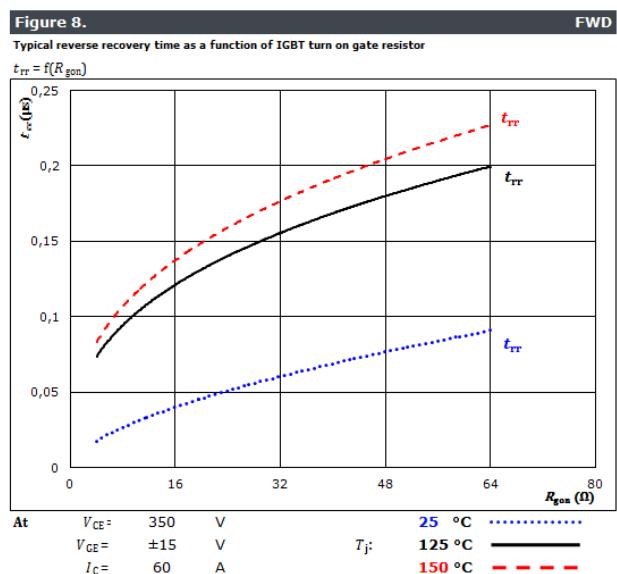
With an inductive load at

$T_j = 150^\circ\text{C}$
 $V_{CE} = 350\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $I_C = 60\text{ A}$



At

V_{CE} = 350 V	T_j : 25 °C
V_{GE} = ±15 V	T_j : 125 °C
R_{gon} = 16 Ω	T_j : 150 °C



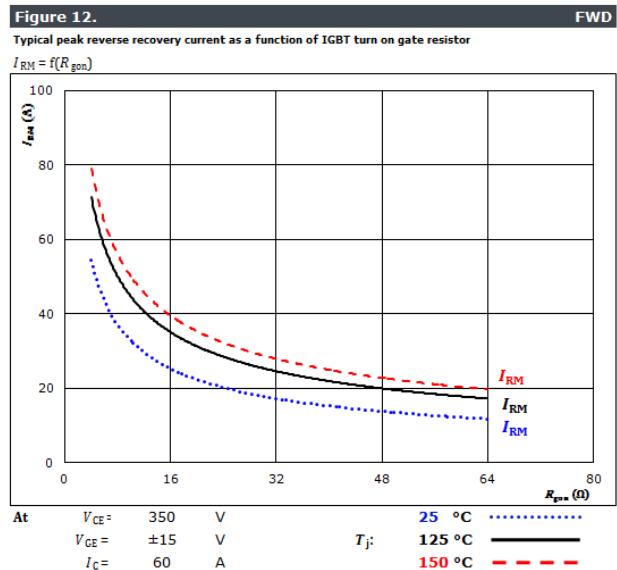
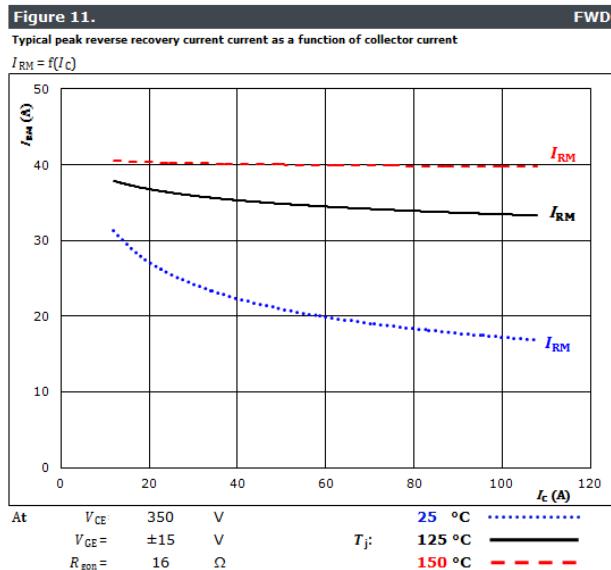
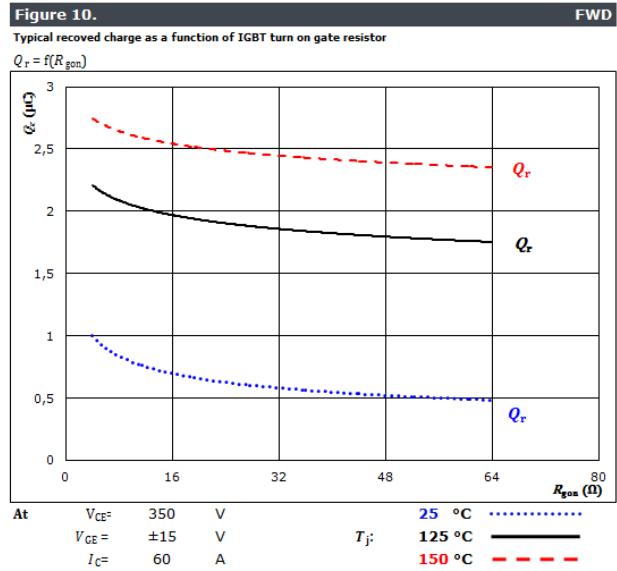
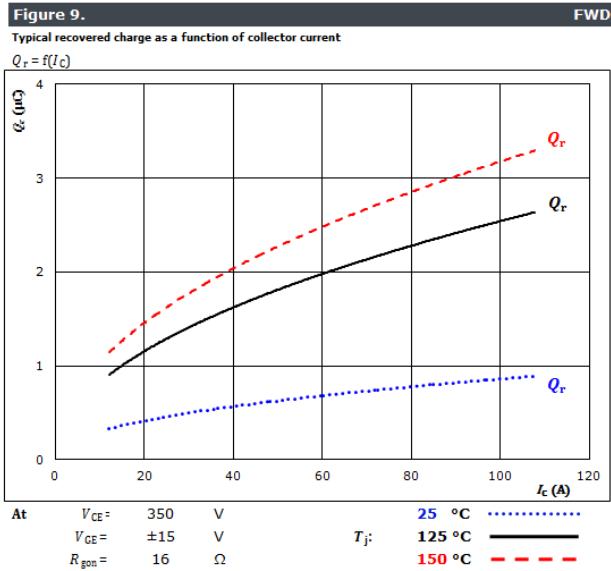
At

V_{CE} = 350 V	T_j : 25 °C
V_{GE} = ±15 V	T_j : 125 °C
I_C = 60 A	T_j : 150 °C



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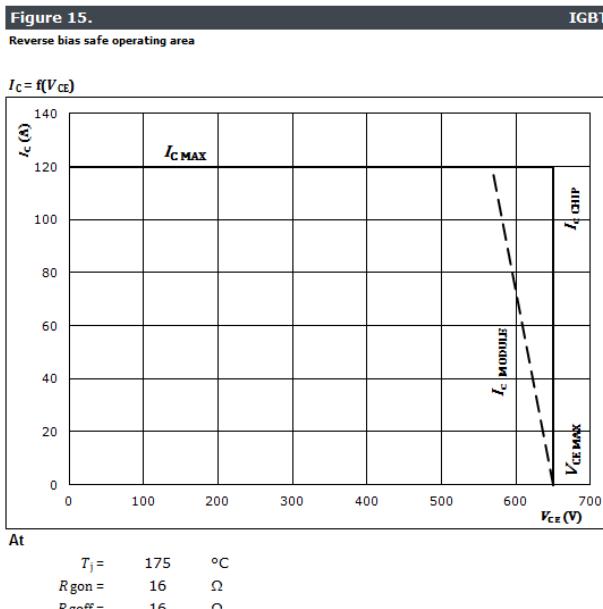
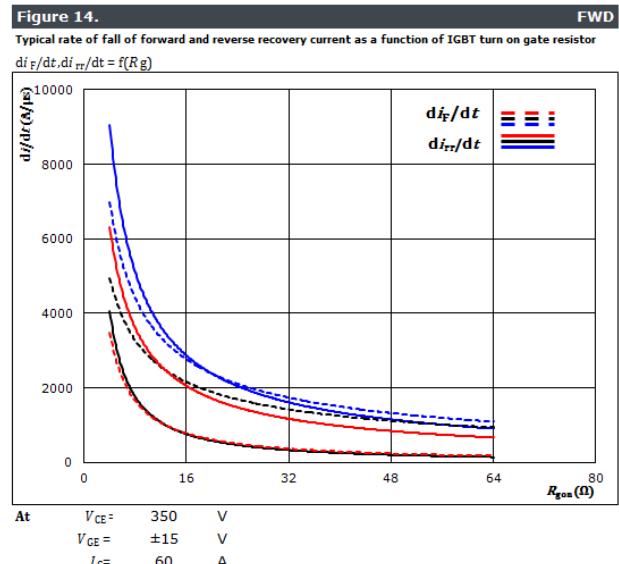
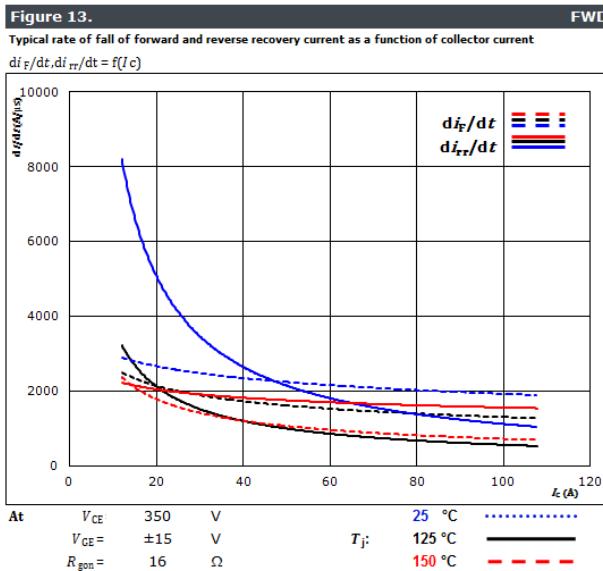
Buck Switching Characteristics





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Buck Switching Characteristics





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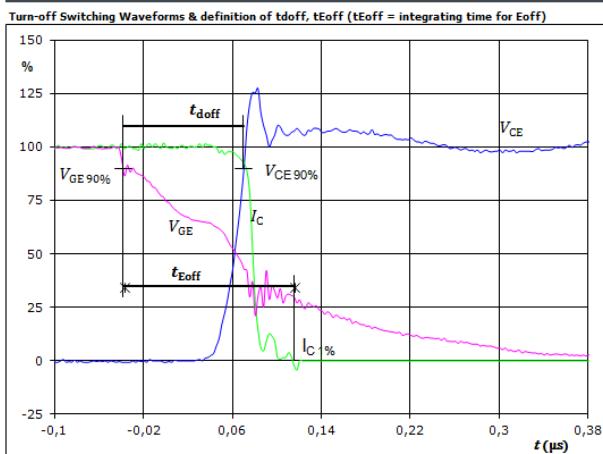
Buck Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

Figure 1.

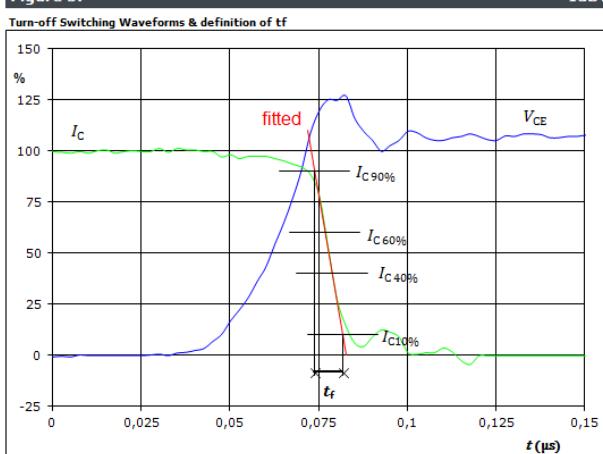
IGBT



$V_{CE}(0\%) = -15$ V
 $V_{CE}(100\%) = 15$ V
 $V_G(100\%) = 350$ V
 $I_C(100\%) = 60$ A
 $t_{doff} = 0,108$ μs
 $t_{Eoff} = 0,153$ μs

Figure 3.

IGBT

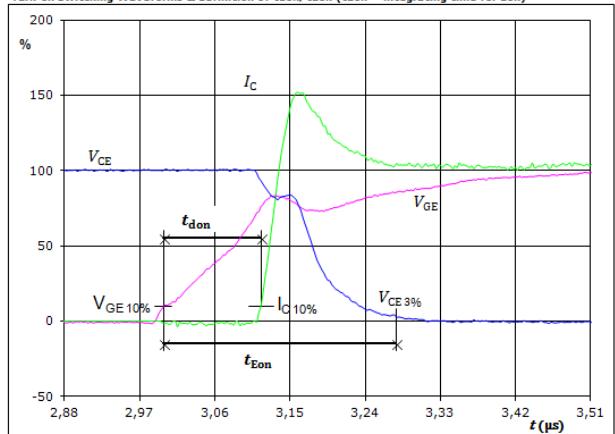


$V_C(100\%) = 350$ V
 $I_C(100\%) = 60$ A
 $t_f = 0,008$ μs

Figure 2.

IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

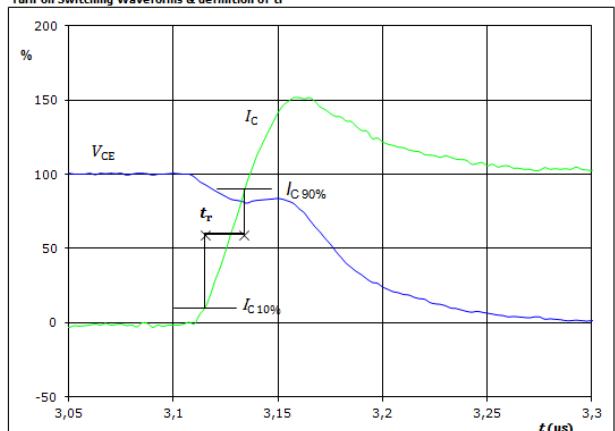


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 350$ V
 $I_C(100\%) = 60$ A
 $t_{don} = 0,117$ μs
 $t_{Eon} = 0,279$ μs

Figure 4.

IGBT

Turn-on Switching Waveforms & definition of t_r

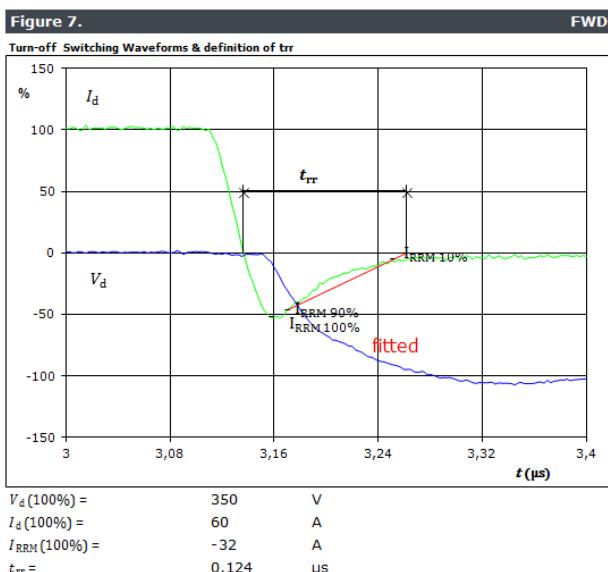
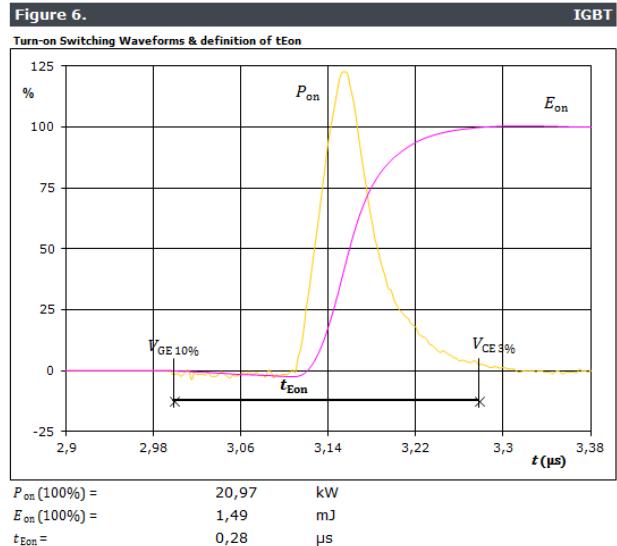
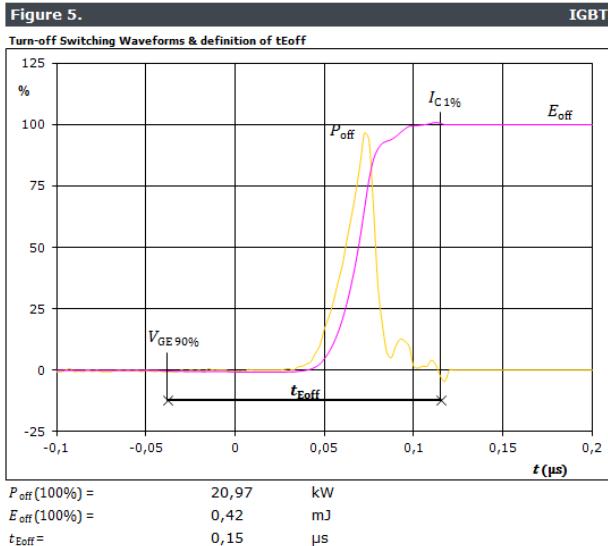


$V_C(100\%) = 350$ V
 $I_C(100\%) = 60$ A
 $t_r = 0,019$ μs



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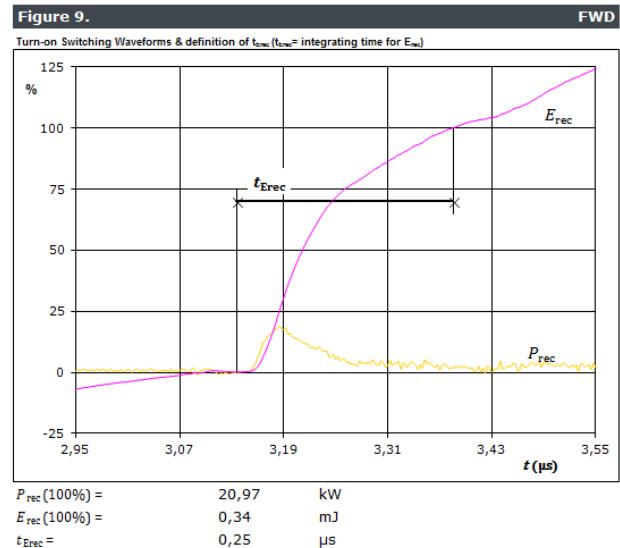
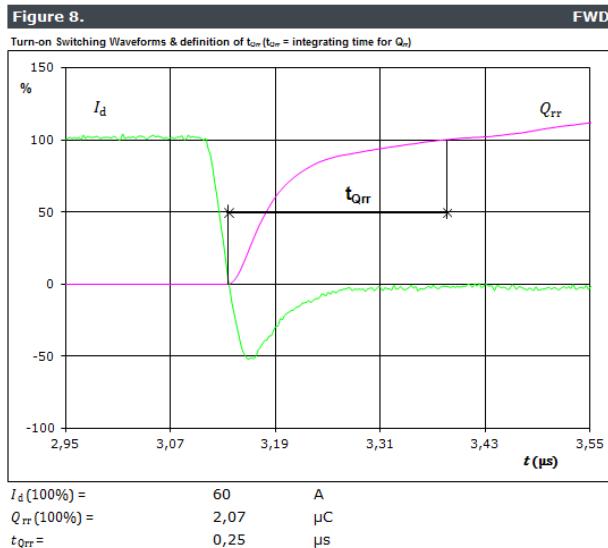
Buck Switching Definitions





Vincotech

Buck Switching Definitions





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Out. Boost Switching Characteristics

Figure 1. IGBT

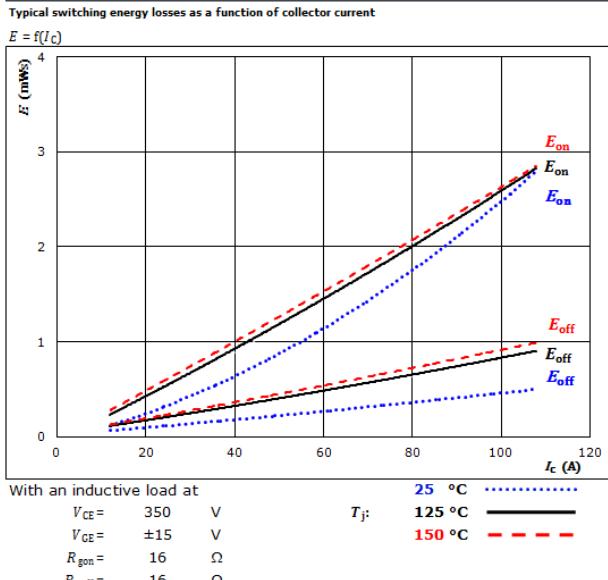


Figure 2. IGBT

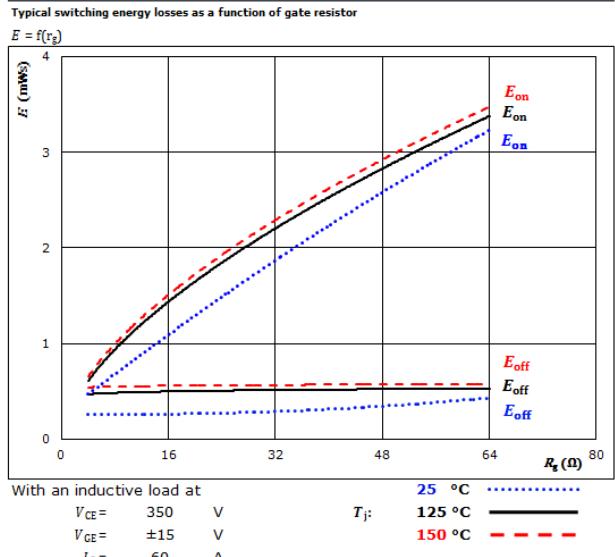


Figure 3. FWD

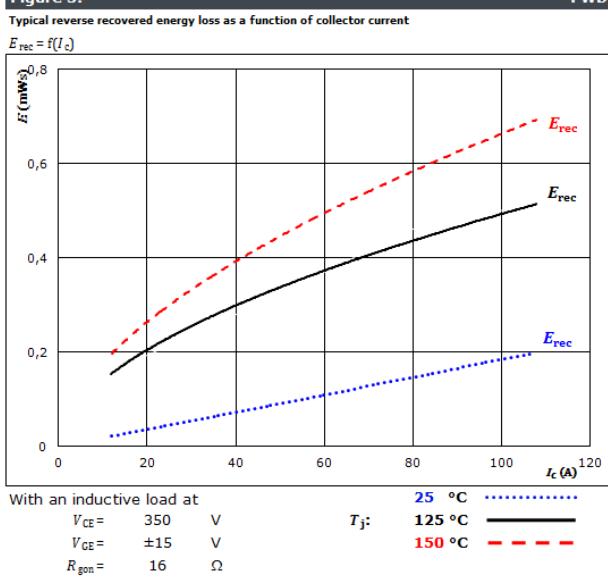
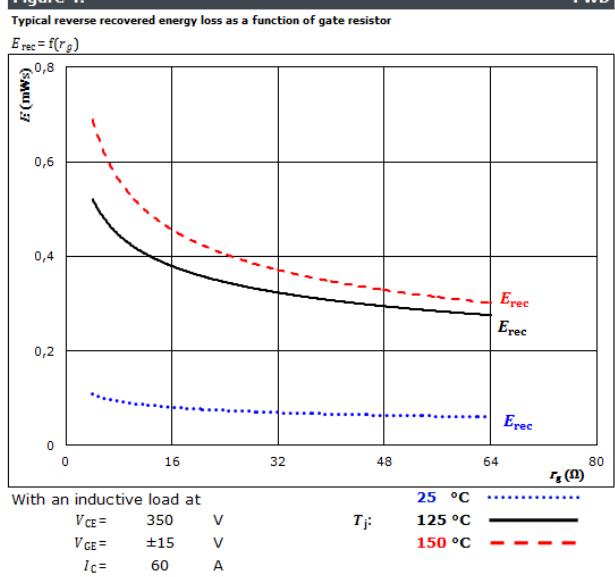


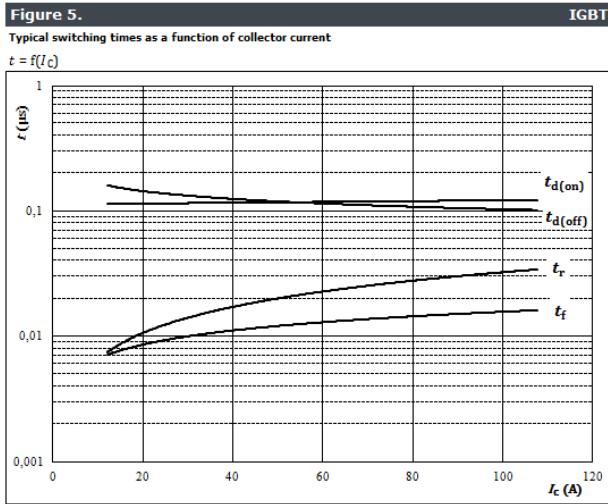
Figure 4. FWD





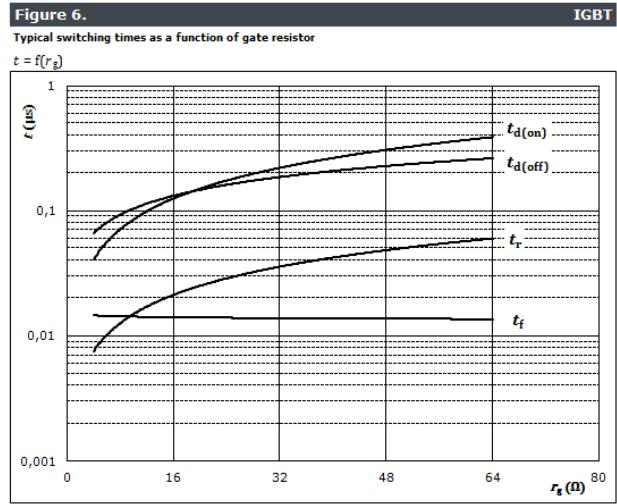
Vincotech

Out. Boost Switching Characteristics



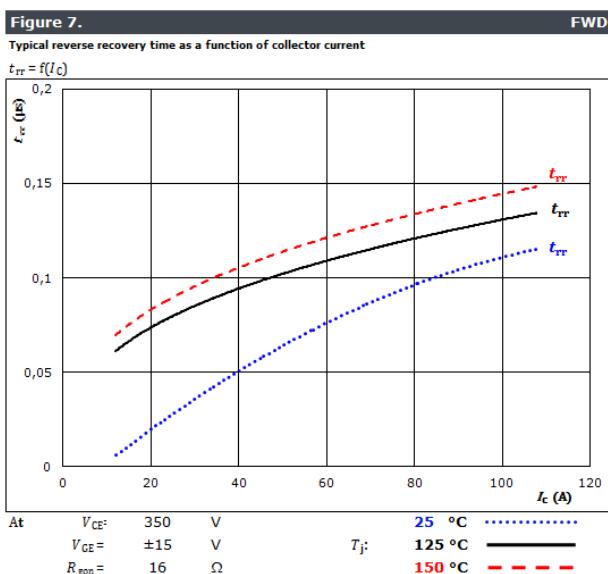
With an inductive load at

$T_j = 150^\circ\text{C}$
 $V_{CE} = 350\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $R_{gon} = 16\Omega$
 $R_{goff} = 16\Omega$

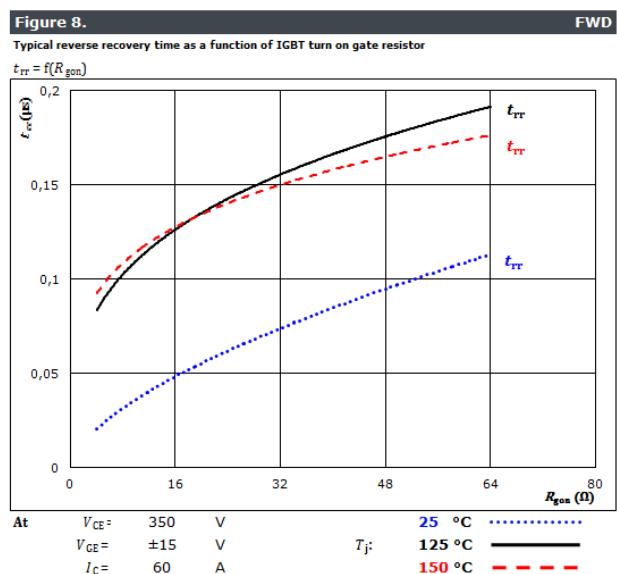


With an inductive load at

$T_j = 150^\circ\text{C}$
 $V_{CE} = 350\text{ V}$
 $V_{GE} = \pm 15\text{ V}$
 $I_C = 60\text{ A}$



At $V_{CE} = 350\text{ V}$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$ $T_j = 150^\circ\text{C}$
 $V_{GE} = \pm 15\text{ V}$ $R_{gon} = 16\Omega$

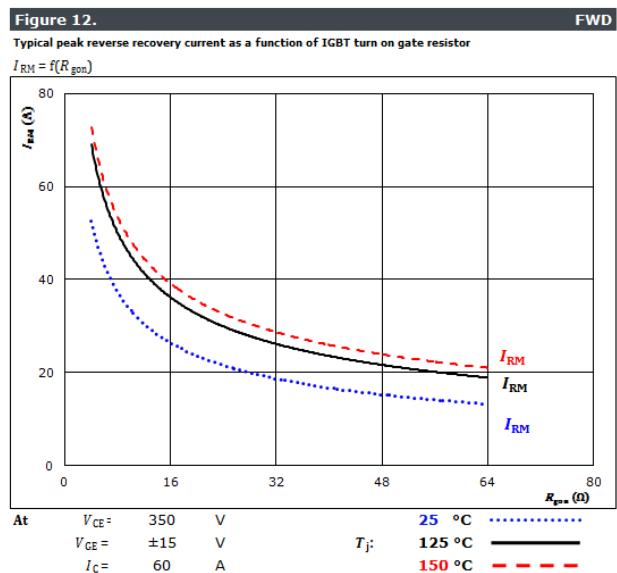
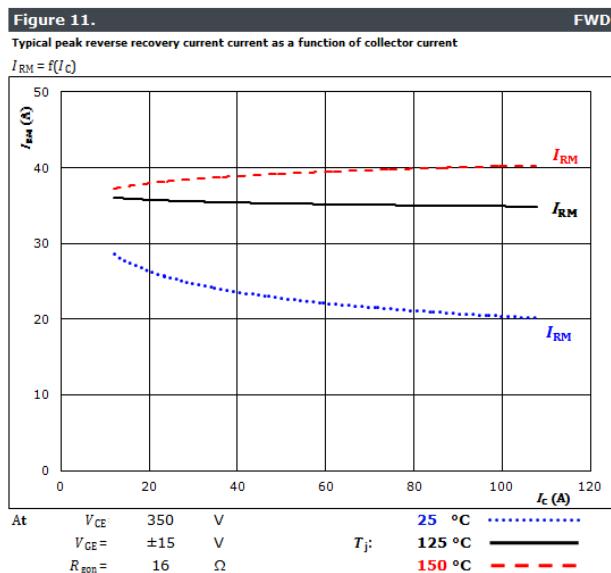
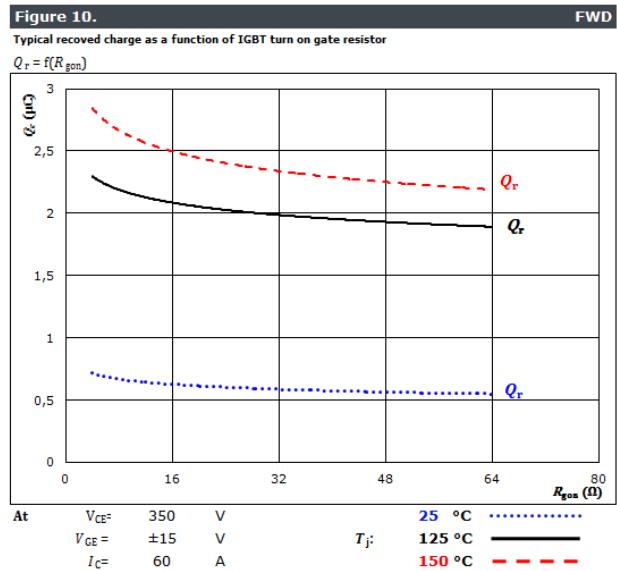
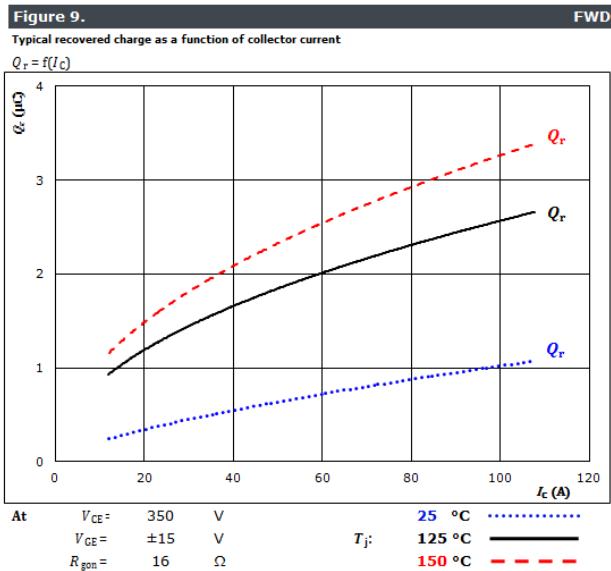


At $V_{CE} = 350\text{ V}$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$ $T_j = 150^\circ\text{C}$
 $V_{GE} = \pm 15\text{ V}$ $I_C = 60\text{ A}$



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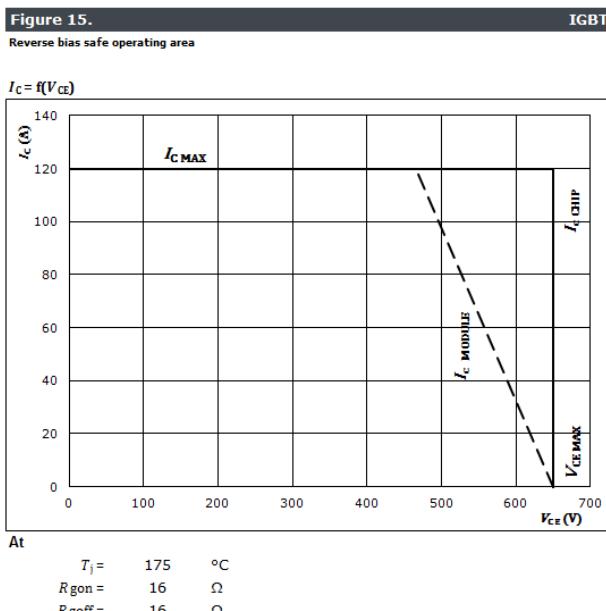
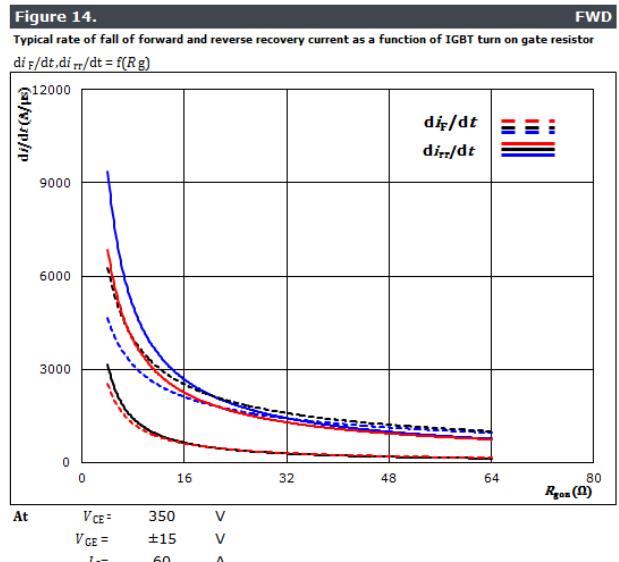
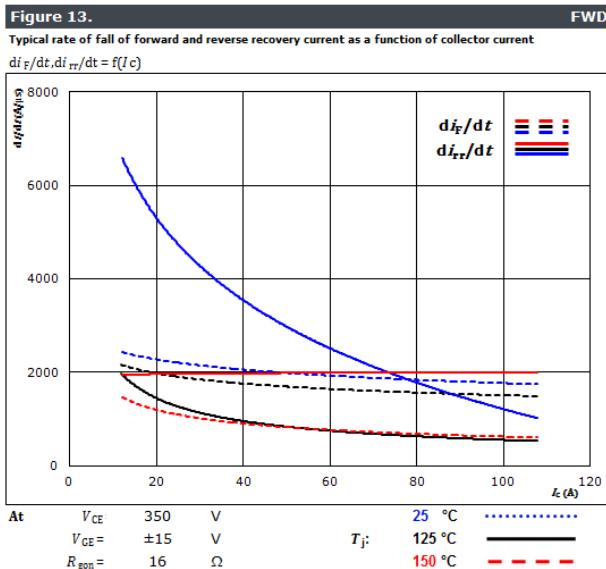
Out. Boost Switching Characteristics





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Out. Boost Switching Characteristics





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Out. Boost Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

Figure 1.

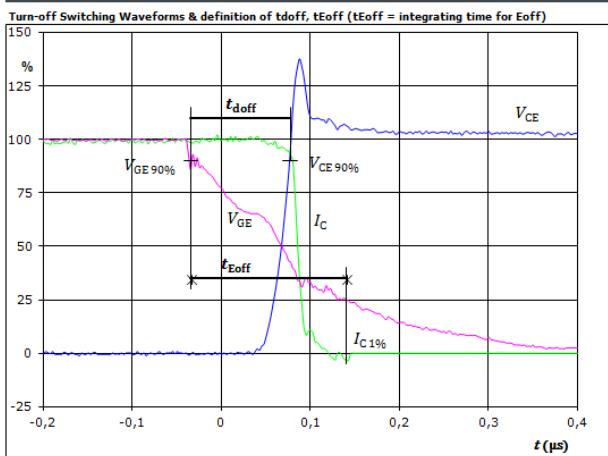


Figure 2.

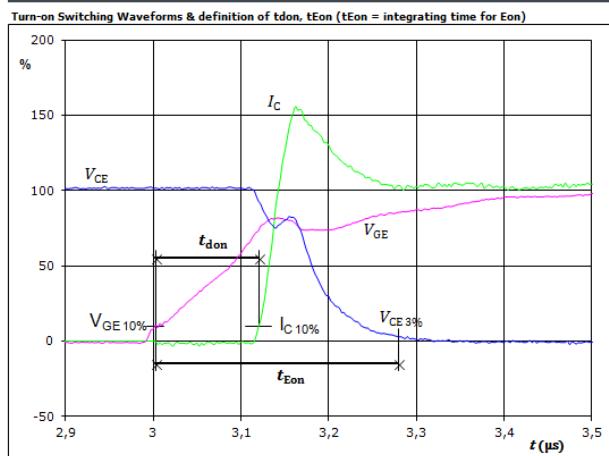


Figure 3.

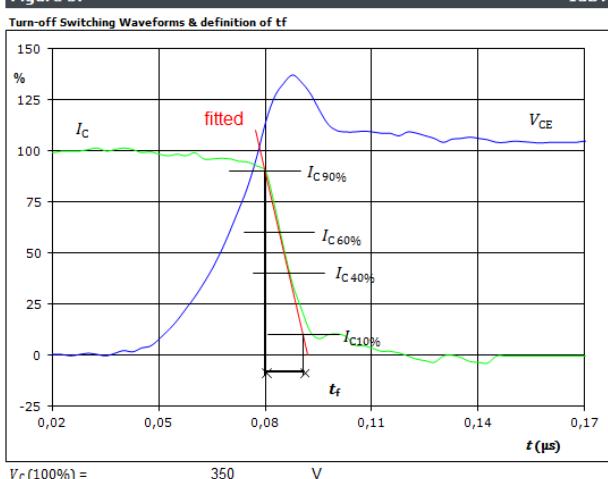
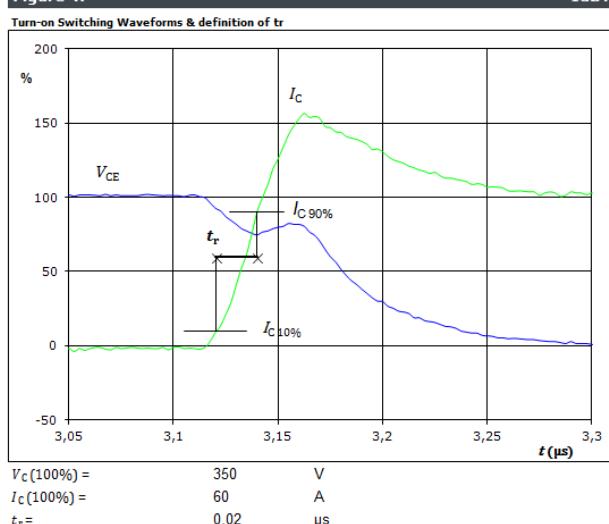


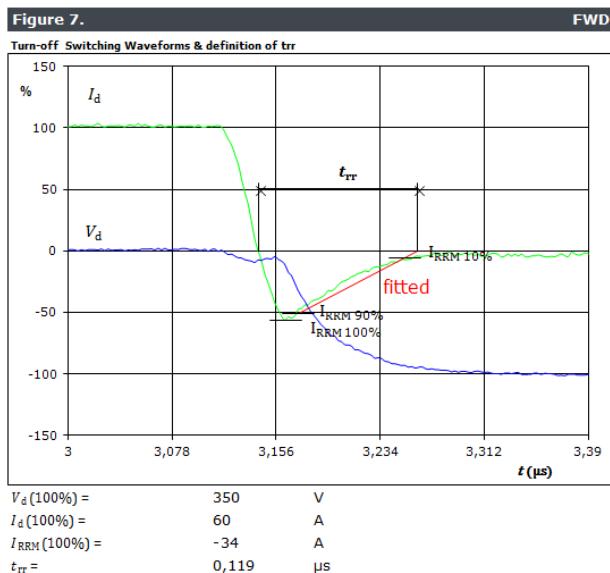
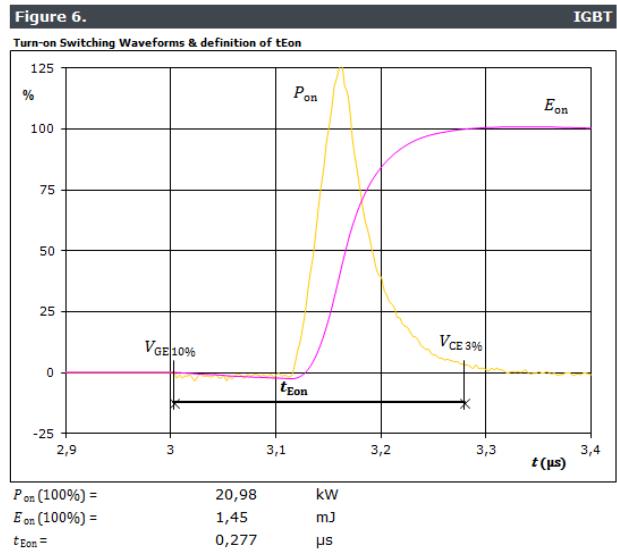
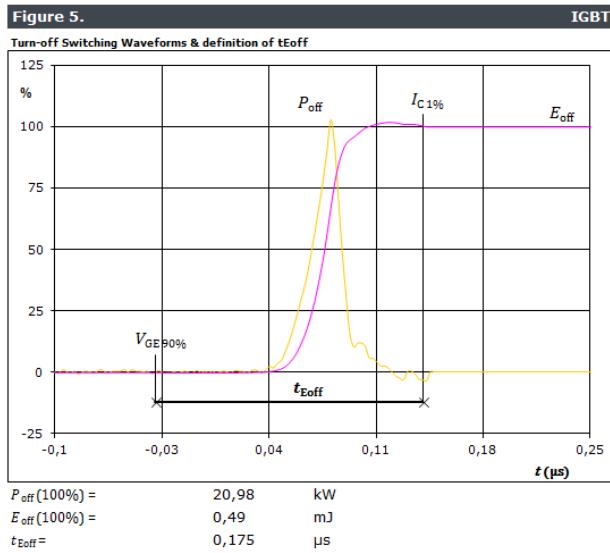
Figure 4.





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Out. Boost Switching Definitions





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Out. Boost Switching Definitions

Figure 8.

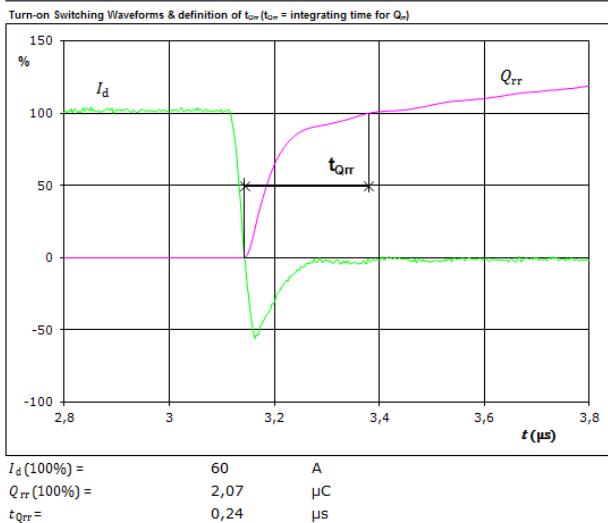
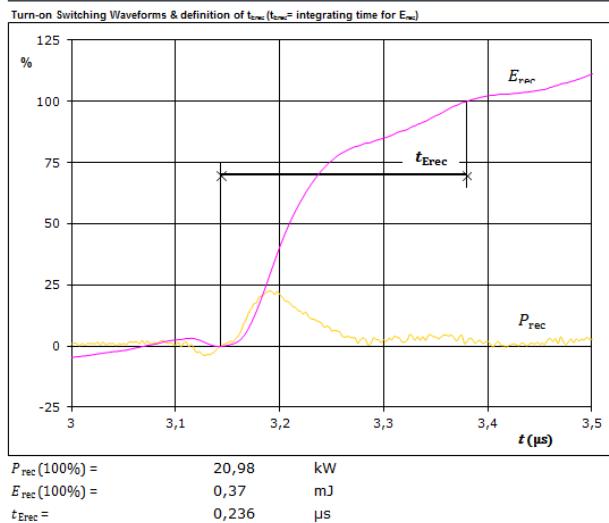


Figure 9.



**10-FZ07NIA060SM-P926F43**

datasheet

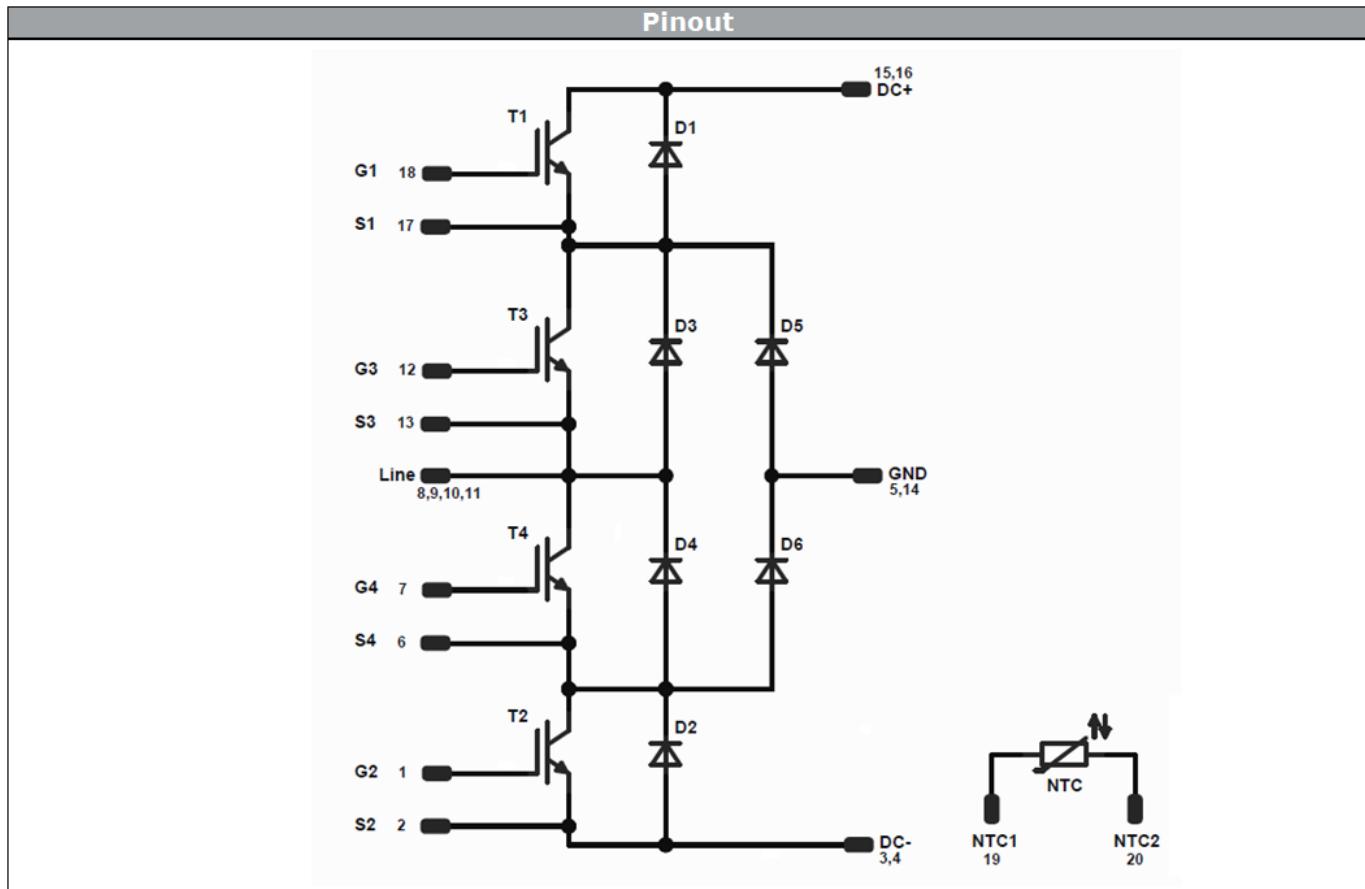
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Ordering Code & Marking						
Version	Ordering Code			in DataMatrix as		in packaging barcode as
without thermal paste 12 mm housing	10-FZ07NIA060SM-P926F43			P926F43		P926F43
NN-NNNNNNNNNNNN NNNNNNNN WWYY UL Vinc VINCO SSSS		Text	Name NN-NNNNNNNNNNNNNN-NNNNNNNN	Date code WWYY	UL & Vinc VINCO	Lot LLLLL
		Datamatrix	Type TTTT-TTT	Lot number LLLLL	Serial SSSS	Date code WWYY

Outline						
Pin table [mm]				Dimensions [mm]		
Pin	X	Y	Function			
1	33,6	0	G2	68,4		
2	30,8	0	S2	65,9		
3	22	0	DC-	64,2 ±0,2		
4	19,2	0	DC-	38,8		
5	10,1	0	GND			
6	2,8	0	S4			
7	0	0	G4			
8	0	7,1	Line			
9	0	9,9	Line			
10	0	12,7	Line			
11	0	15,5	Line			
12	0	22,6	G3			
13	2,8	22,6	S3			
14	10,1	22,6	GND			
15	19,2	22,6	DC+			
16	22	22,6	DC+			
17	30,8	22,6	S1			
18	33,6	22,6	G1			
19	33,6	14,8	NTC1			
20	33,6	8,2	NTC2			



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Identification					
ID	Component	Voltage	Current	Function	Comment
T1,T2	IGBT	650V	2x30A	Buck Switch	
D5,D6	FWD	650V	50A	Buck Diode	
T3,T4	IGBT	650V	2x30A	Out. Boost Switch	
D1,D2	FWD	650V	50A	Out. Boost Diode	
D3,D4	FWD	650V	75A	Out. Boost Inv. Diode	
NTC	NTC	-	-	Thermistor	



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Packaging instruction			
Standard packaging quantity (SPQ) 135	>SPQ	Standard	<SPQ Sample
Handling instruction			
Handling instructions for <i>flow</i> 0 packages see vincotech.com website.			

Document No.:	Date:	Modification:	Pages
10-FZ07NIA060SM-P926F43-D1-14	13 Apr. 2015		

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.