



flowPIM 1

1200 V / 35 A

Topology features

- Kelvin Emitter for improved switching performance
- Open Emitter configuration
- Temperature sensor
- Converter+Brake+Inverter
- Tandem diode

Component features

- Easy paralleling
- Low turn-off losses
- Low collector emitter saturation voltage
- Positive temperature coefficient
- Short tail current
- Switching optimized for EMC

Housing features

- Base isolation: Al₂O₃
- Convex shaped substrate for superior thermal contact
- Thermo-mechanical push-and-pull force relief
- Solder pin

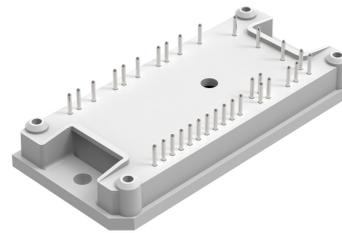
Target applications

- Embedded Drives
- Industrial Drives

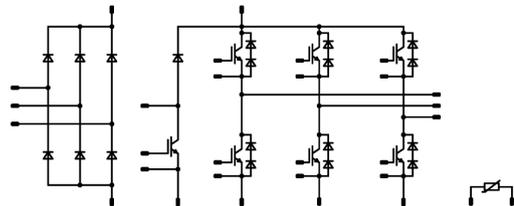
Types

- 10-FY12PMA035M703-P589A73

flow 1 12 mm housing



Schematic





Vincotech

10-FY12PMA035M703-P589A73
datasheet

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	48	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	70	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	99	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	9,5	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Inverter Diode

Peak repetitive reverse voltage	V_{RRM}		1300	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	31	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	89	W
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Brake Switch

Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	32	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	50	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	72	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	9,5	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$



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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Brake Diode				
Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	21	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	42	W
Maximum junction temperature	T_{jmax}		175	°C

Rectifier Diode

Peak repetitive reverse voltage	V_{RRM}		1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	47	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 150\text{ °C}$	350	A
Surge current capability	I^2t		610	A ² s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	53	W
Maximum junction temperature	T_{jmax}		150	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			7,91	mm
Comparative Tracking Index	CTI		≥ 200	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$		10	0,0035	25	5,4	6	6,6	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	15		35	25 125 150		1,47 1,64 1,68	1,85 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}	0	1200		25			80	μA
Gate-emitter leakage current	I_{GES}	20	0		25			200	nA
Internal gate resistance	r_g						None		Ω
Input capacitance	C_{ies}						7900		pF
Output capacitance	C_{oes}	0	10		25		270		pF
Reverse transfer capacitance	C_{res}						97		pF
Gate charge	Q_g	$V_{CC} = 600$ V	0/15		35	25		260	nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)					0,96		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$					25 125 150		73,42 73,71 73,27	ns
Rise time	t_r	$R_{gon} = 4$ Ω $R_{goff} = 4$ Ω				25 125 150		10,01 10,85 11,72	ns
Turn-off delay time	$t_{d(off)}$		±15	600	35	25 125 150		151,83 172,63 181,28	ns
Fall time	t_f					25 125 150		97,02 120,23 126,56	ns
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD} = 0,578$ μC $Q_{tFWD} = 1,8$ μC $Q_{tFWD} = 2,03$ μC				25 125 150		0,646 1,06 1,17	mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		2,49 3,32 3,47	mWs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Inverter Diode										
Static										
Forward voltage	V_F				30	25 125 150		3,19 3,01 2,93	3,84 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1300$ V				25			1,6	μA
Thermal										
Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)						1,07		K/W
Dynamic										
Peak recovery current	I_{RM}	$di/dt=5362$ A/μs $di/dt=4088$ A/μs $di/dt=4424$ A/μs	±15	600	35	25		53,4		A
Reverse recovery time	t_{rr}					125		57,55		ns
						150		58,7		
						25		19,21		
Recovered charge	Q_r					125		134,98		μC
						150		149,98		
		25		0,578						
Reverse recovered energy	E_{rec}	125		1,8		mWs				
		150		2,03						
		25		0,154						
Peak rate of fall of recovery current	$(di_r/dt)_{max}$	125		0,695		A/μs				
		150		0,789						
		25		7177,28						
								4480,86		
								3819,39		



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Brake Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0025	25	5,4	6	6,6	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		25	25 125 150		1,64 1,89 1,95	2,1 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			70	μA
Gate-emitter leakage current	I_{GES}		20	0		25			200	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}							4800		pF
Output capacitance	C_{oes}		0	10		25		170		pF
Reverse transfer capacitance	C_{res}							57		pF
Gate charge	Q_g	$V_{CC} = 600$ V	0/15		25	25		180		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)						1,31		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 16$ Ω $R_{goff} = 16$ Ω	0/15	700	25	25		70,6		ns			
						125		66,6					
						150		65					
Rise time	t_r								25		47,6		ns
									125		50,4		
									150		51,4		
Turn-off delay time	$t_{d(off)}$								25		262,2		ns
					125		290						
					150		296						
Fall time	t_f				25		101,1		ns				
					125		117,29						
					150		119,12						
Turn-on energy (per pulse)	E_{on}	$Q_{tFWD} = 1,92$ μC $Q_{tFWD} = 2,9$ μC $Q_{tFWD} = 3,15$ μC				25		2,6		mWs			
						125		3,11					
						150		3,24					
Turn-off energy (per pulse)	E_{off}					25		2,03		mWs			
						125		2,65					
						150		2,81					



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datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Brake Diode										
Static										
Forward voltage	V_F			15	25 125 150		1,63 1,74 1,73	1,9 ⁽¹⁾		V
Reverse leakage current	I_R	$V_r = 1200$ V			25			30		μA
Thermal										
Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)					2,25			K/W
Dynamic										
Peak recovery current	I_{RM}	$di/dt=394$ A/μs $di/dt=319$ A/μs $di/dt=403$ A/μs	0/15	700	25	25		13,75		A
Reverse recovery time	t_{rr}					125		15,17		
						150		15,47		
						25		263,52		
Recovered charge	Q_r					125		374,58		
						150		413,14		
		25		1,92						
Reverse recovered energy	E_{rec}	125		2,9						
		150		3,15						
		25		0,778						
Peak rate of fall of recovery current	$(di_r/dt)_{max}$	125		1,28						
		150		1,41						
		25		111,02						
							85		A/μs	
							72,04			



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		

Rectifier Diode

Static

Forward voltage	V_F				18	25 125 150		0,99 0,912 0,908	1,21 ⁽¹⁾ 1,1 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1600$ V				25			50	μA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 5,2$ W/mK (PTM)						1,31		K/W
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Thermistor

Static

Rated resistance	R					25		22		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 1484$ Ω				100	-5		5	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,5		mW/K
B-value	$B_{(25/50)}$	Tol. ±1 %						3962		K
B-value	$B_{(25/100)}$	Tol. ±1 %						4000		K
Vincotech Thermistor Reference									I	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.

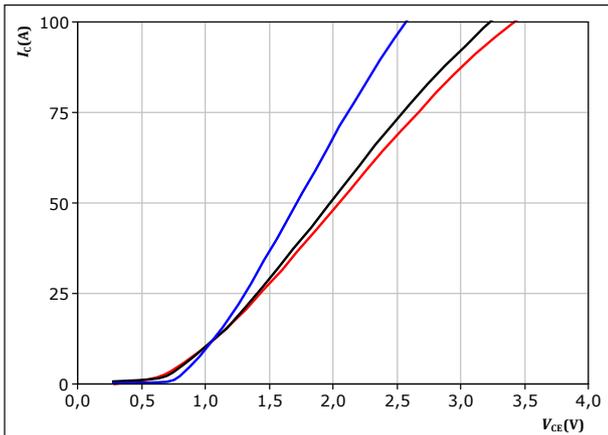


Inverter Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

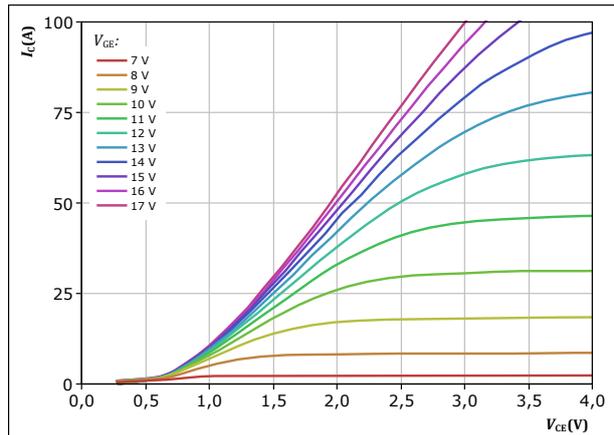


$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j:$ — 25 °C
 — 125 °C
 — 150 °C

figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

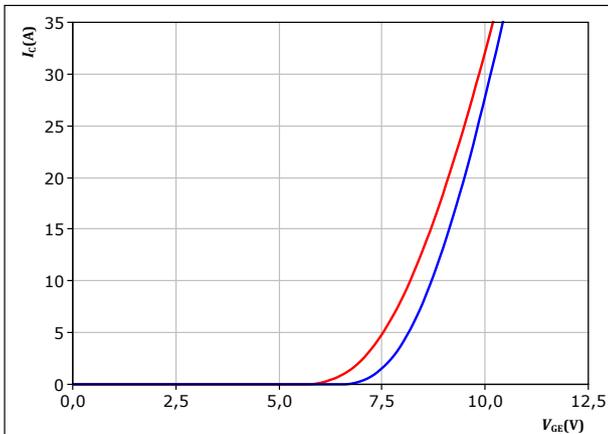


$t_p = 250 \mu s$
 $T_j = 150 \text{ °C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

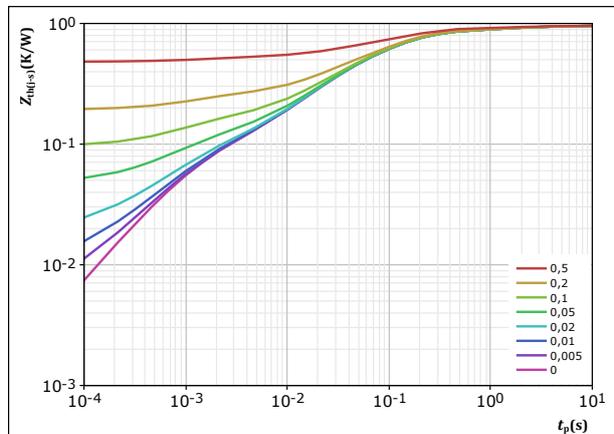


$t_p = 250 \mu s$
 $V_{CE} = 10 V$
 $T_j:$ — 25 °C
 — 125 °C

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 0,957 \text{ K/W}$
 IGBT thermal model values

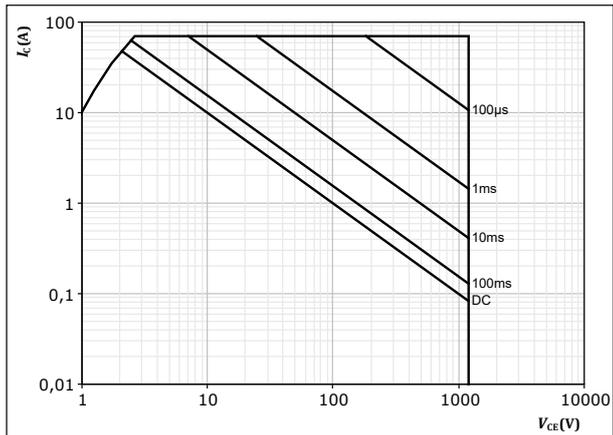
R (K/W)	τ (s)
3,36E-02	5,53E+00
1,00E-01	1,06E+00
5,28E-01	1,14E-01
2,33E-01	2,36E-02
6,69E-02	1,07E-03



Inverter Switch Characteristics

figure 5. IGBT

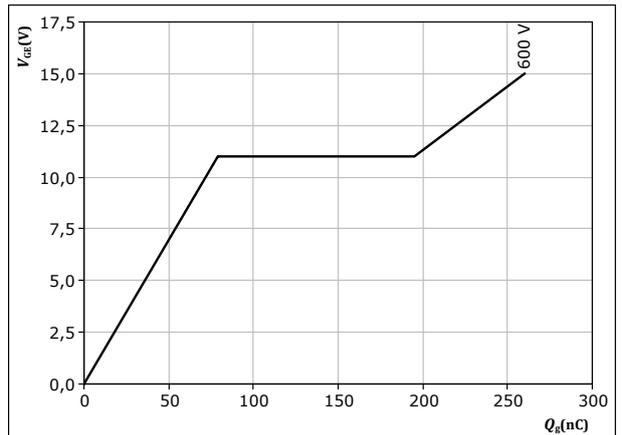
Safe operating area
 $I_C = f(V_{CE})$



$D =$ single pulse
 $T_s = 80 \text{ } ^\circ\text{C}$
 $V_{GE} = 15 \text{ V}$
 $T_j = T_{jmax}$

figure 6. IGBT

Gate voltage vs gate charge
 $V_{GE} = f(Q_g)$



$I_C = 35 \text{ A}$
 $T_j = 25 \text{ } ^\circ\text{C}$



Inverter Diode Characteristics

figure 7. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

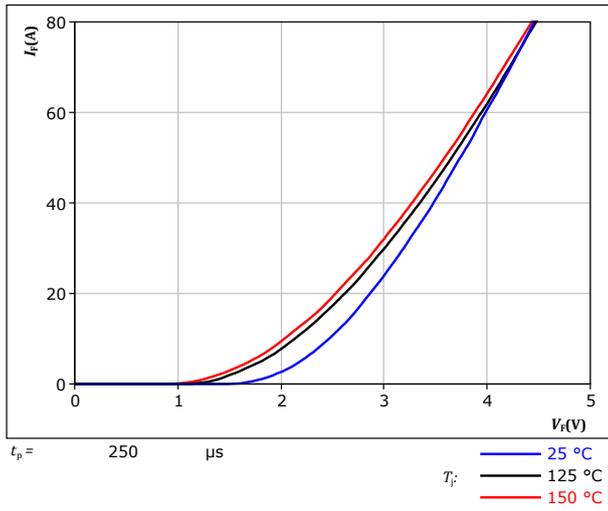
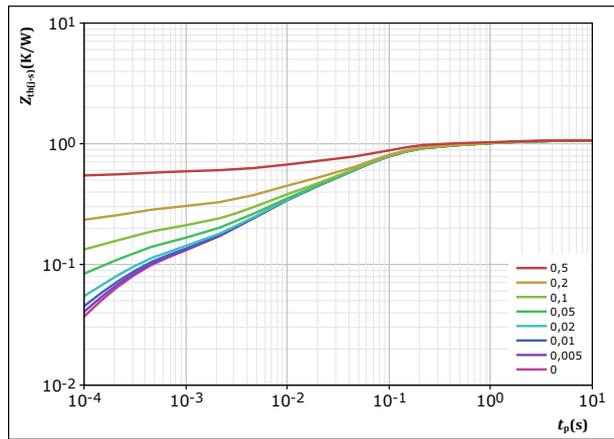


figure 8. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	
$R_{th(j-s)} =$	1,066	K/W
FWD thermal model values		
R (K/W)	τ (s)	
2,57E-02	6,85E+00	
1,35E-01	7,68E-01	
6,16E-01	6,78E-02	
2,02E-01	6,16E-03	
9,26E-02	2,29E-04	

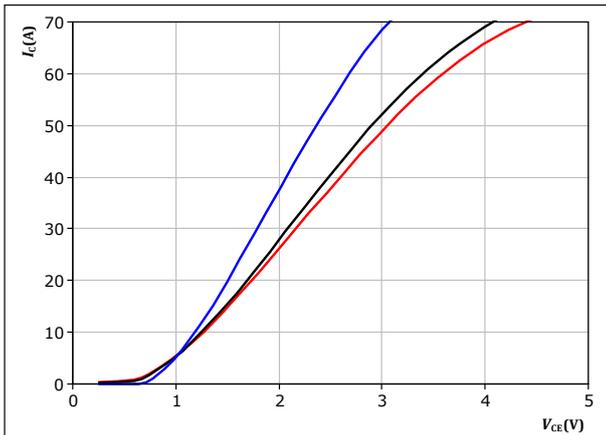


Brake Switch Characteristics

figure 9. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$



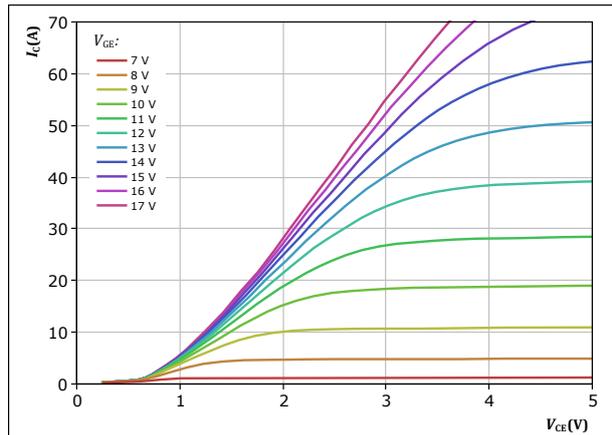
$t_p = 250\ \mu\text{s}$
 $V_{GE} = 15\ \text{V}$

$T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 10. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

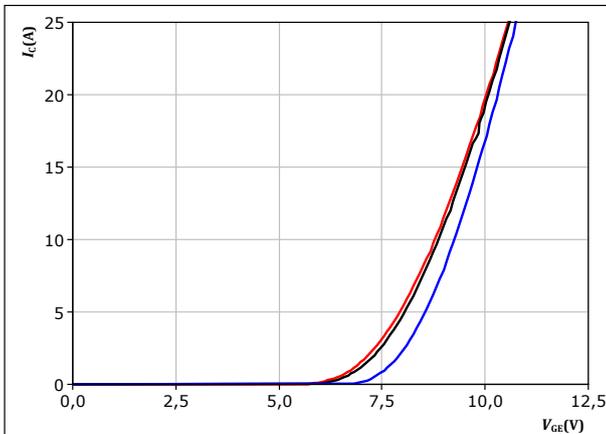


$t_p = 250\ \mu\text{s}$
 $T_j = 150\text{ °C}$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 11. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$



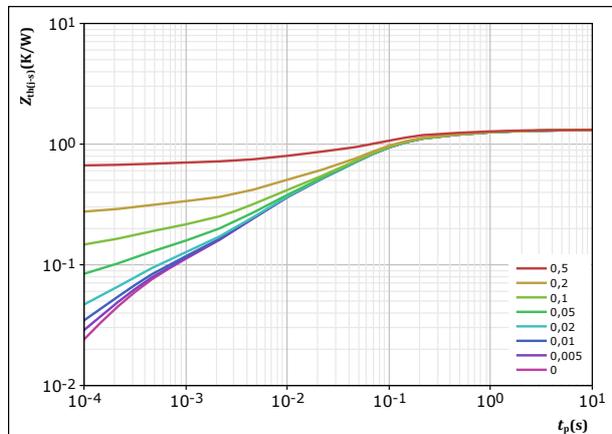
$t_p = 250\ \mu\text{s}$
 $V_{CE} = 10\ \text{V}$

$T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 12. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,314\ \text{K/W}$

IGBT thermal model values

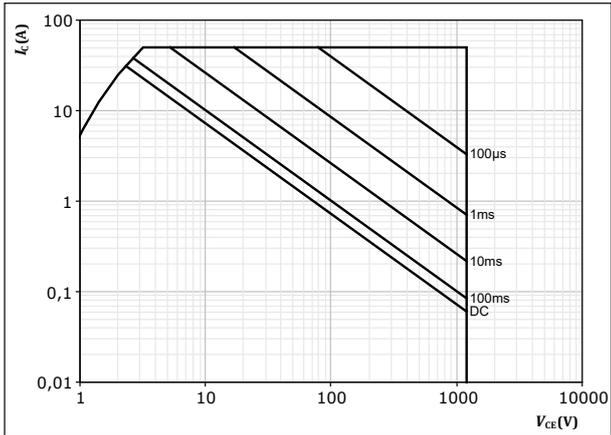
R (K/W)	τ (s)
4,80E-02	4,95E+00
2,14E-01	4,62E-01
7,66E-01	6,41E-02
2,24E-01	6,11E-03
6,85E-02	3,04E-04



Brake Switch Characteristics

figure 13. IGBT

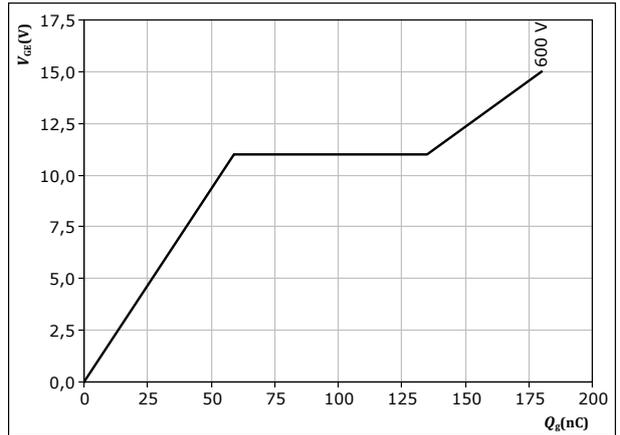
Safe operating area
 $I_C = f(V_{CE})$



$D =$ single pulse
 $T_s = 80$ °C
 $V_{GE} = 15$ V
 $T_j = T_{jmax}$

figure 14. IGBT

Gate voltage vs gate charge
 $V_{GE} = f(Q_g)$



$I_C = 25$ A
 $T_j = 25$ °C



Brake Diode Characteristics

figure 15. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

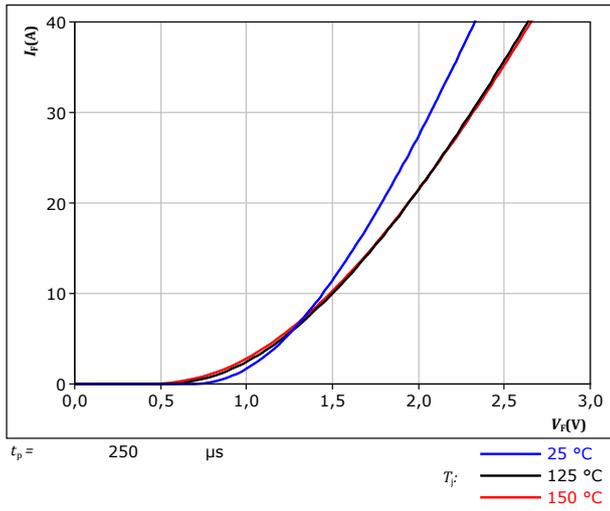
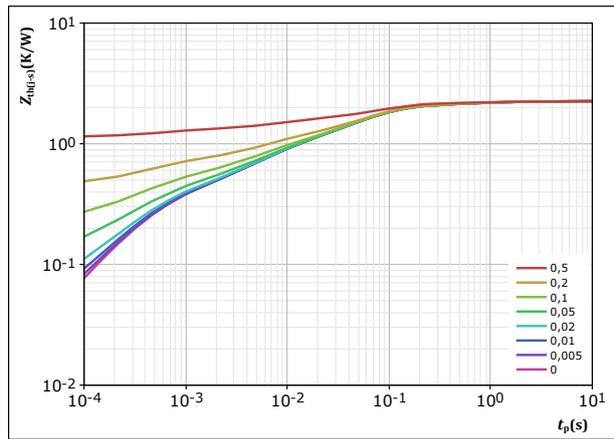


figure 16. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D =$	t_p / T	
$R_{th(j-s)} =$	2,253	K/W
FWD thermal model values		
R (K/W)	τ (s)	
4,18E-02	6,42E+00	
1,99E-01	5,83E-01	
1,22E+00	5,90E-02	
4,91E-01	5,62E-03	
3,08E-01	4,15E-04	



Rectifier Diode Characteristics

figure 17. Rectifier

Typical forward characteristics

$$I_F = f(V_F)$$

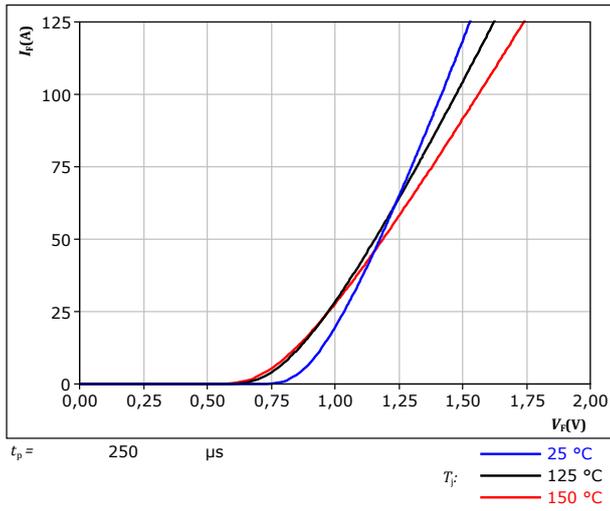
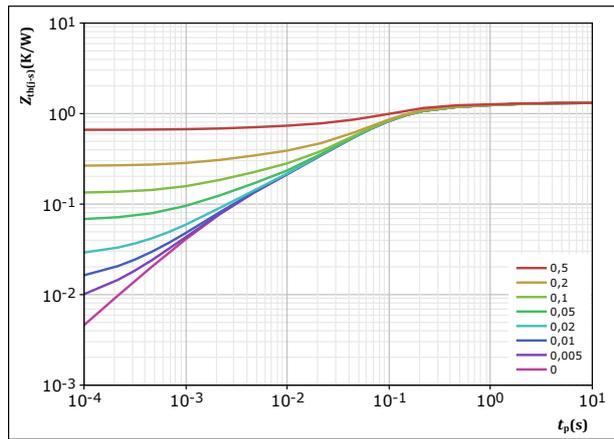


figure 18. Rectifier

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,313$ K/W
 Rectifier thermal model values

R (K/W)	τ (s)
3,05E-02	9,71E+00
1,83E-01	8,09E-01
9,17E-01	8,96E-02
1,17E-01	2,21E-02
7,38E-02	2,39E-03

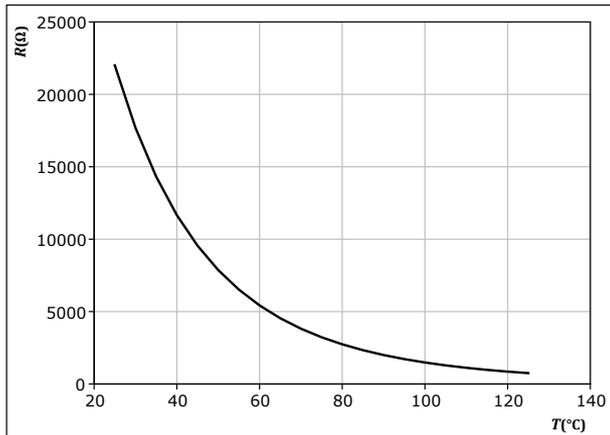


Thermistor Characteristics

figure 19. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$

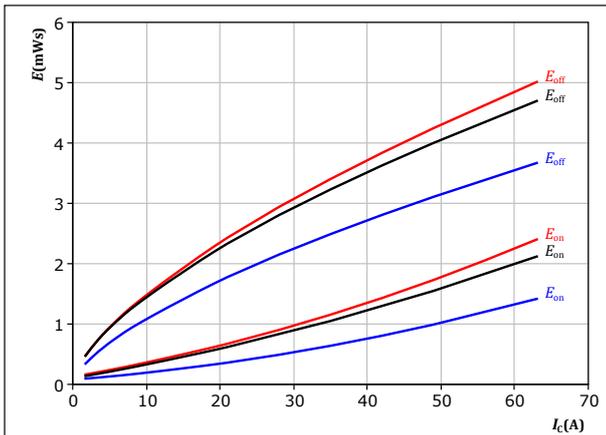




Inverter Switching Characteristics

figure 20. IGBT

Typical switching energy losses as a function of collector current
 $E = f(I_c)$

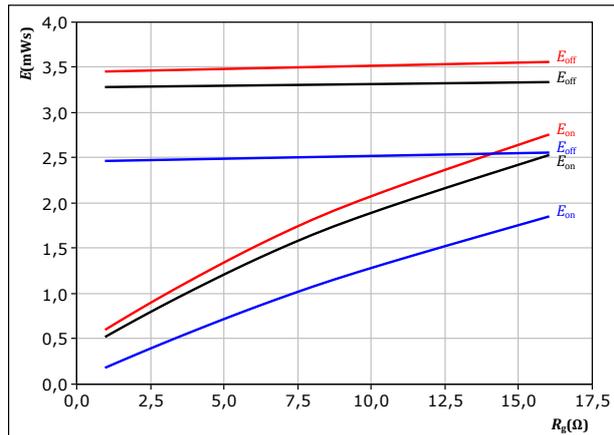


With an inductive load at

$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$R_{gon} = 4$ Ω	$T_j = 150$ °C
$R_{goff} = 4$ Ω	

figure 21. IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor
 $E = f(R_g)$

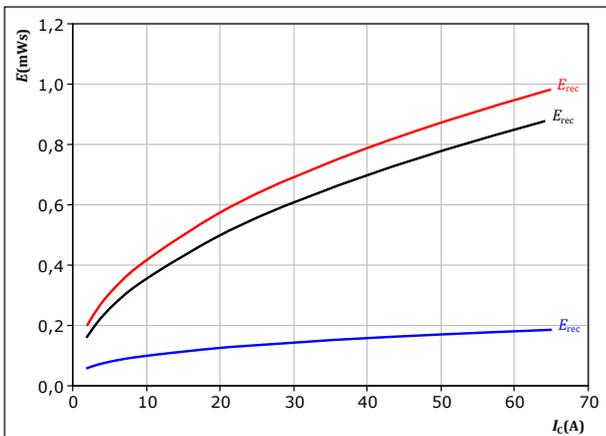


With an inductive load at

$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$I_c = 35$ A	$T_j = 150$ °C

figure 22. FWD

Typical reverse recovered energy loss as a function of collector current
 $E_{rec} = f(I_c)$

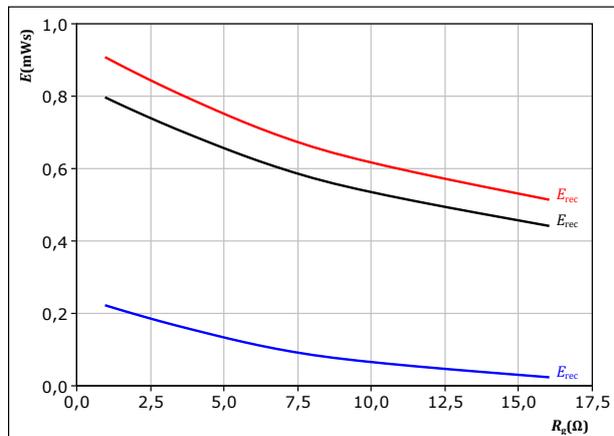


With an inductive load at

$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$R_{gon} = 4$ Ω	$T_j = 150$ °C

figure 23. FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at

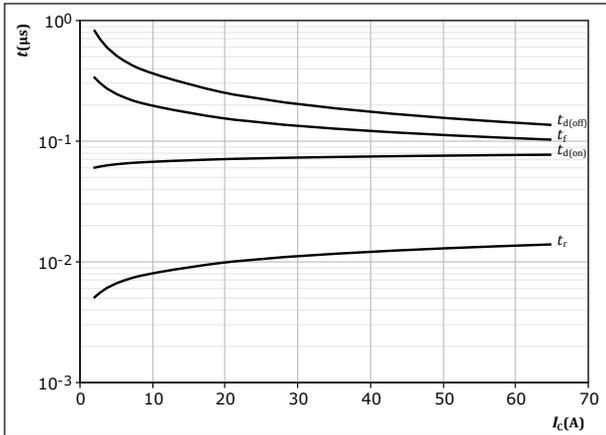
$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$I_c = 35$ A	$T_j = 150$ °C



Inverter Switching Characteristics

figure 24. IGBT

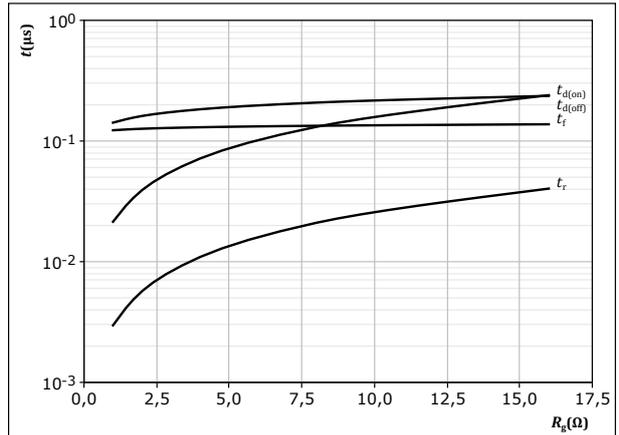
Typical switching times as a function of collector current
 $t = f(I_c)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 4 \text{ } \Omega$
 $R_{goff} = 4 \text{ } \Omega$

figure 25. IGBT

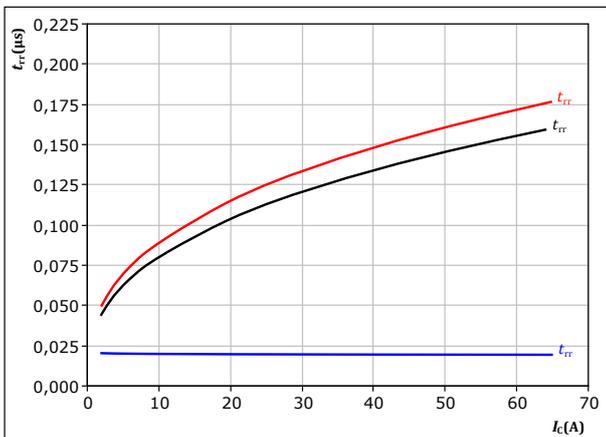
Typical switching times as a function of IGBT turn on gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 35 \text{ A}$

figure 26. FWD

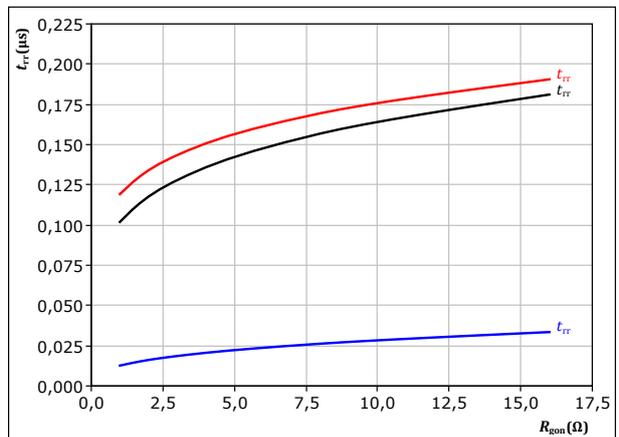
Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_c)$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 4 \text{ } \Omega$
 $T_j: \text{ — } 25 \text{ }^\circ\text{C}$
 $\text{ — } 125 \text{ }^\circ\text{C}$
 $\text{ — } 150 \text{ }^\circ\text{C}$

figure 27. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 35 \text{ A}$
 $T_j: \text{ — } 25 \text{ }^\circ\text{C}$
 $\text{ — } 125 \text{ }^\circ\text{C}$
 $\text{ — } 150 \text{ }^\circ\text{C}$

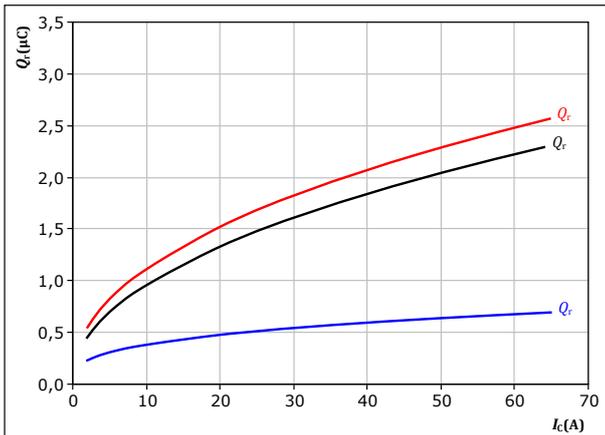


Inverter Switching Characteristics

figure 28. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

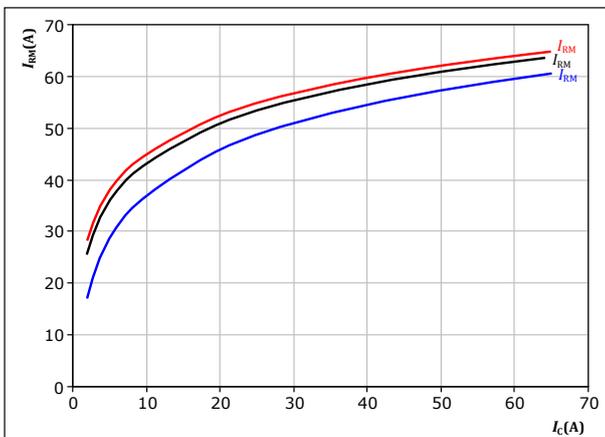
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 4 \ \Omega$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 30. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

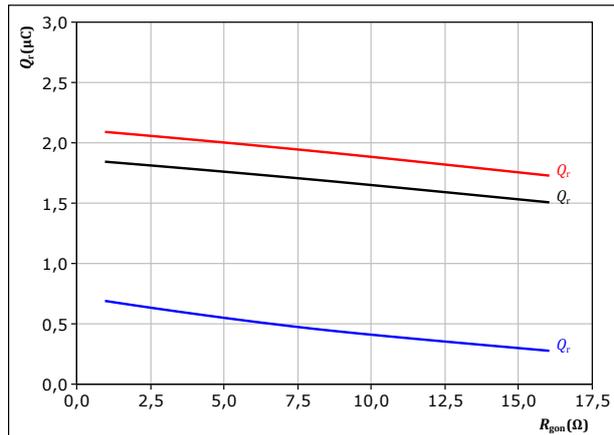
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 4 \ \Omega$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 29. FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

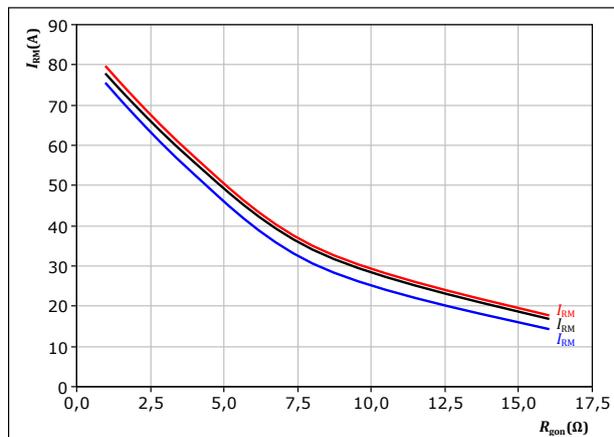
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 35 \text{ A}$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 31. FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 35 \text{ A}$

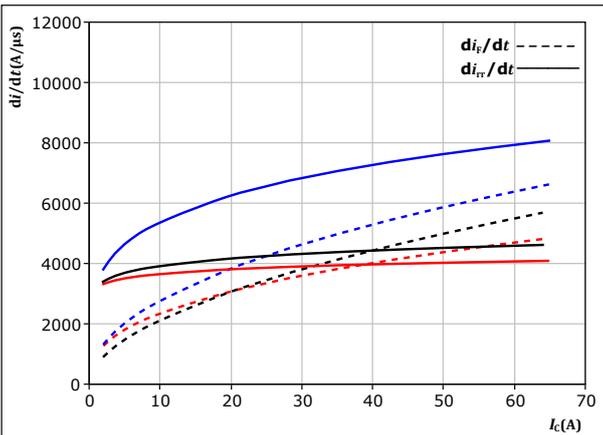
T_j : 25 °C (blue), 125 °C (black), 150 °C (red)



Inverter Switching Characteristics

figure 32. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_{rr}/dt = f(I_C)$

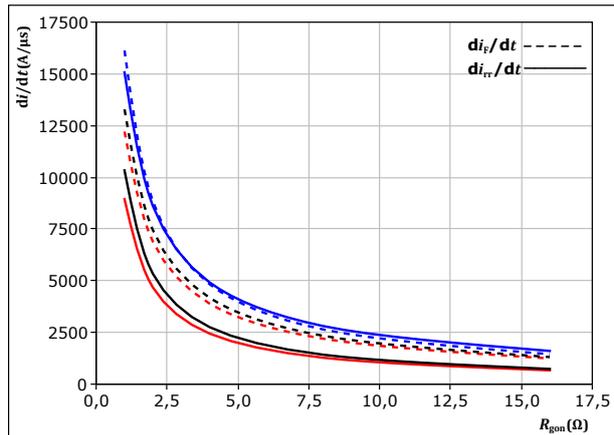


With an inductive load at

$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$R_{gon} = 4$ Ω	$T_j = 150$ °C

figure 33. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{gon})$

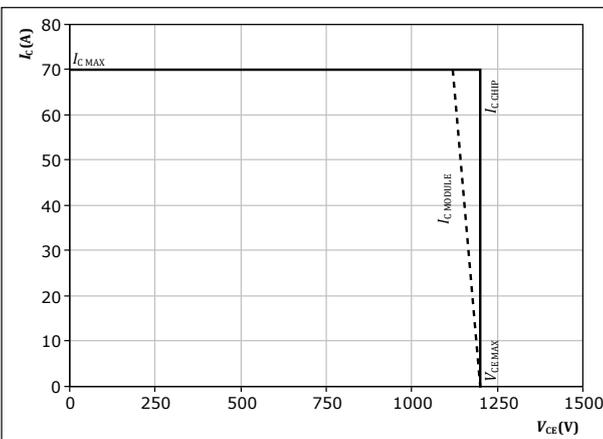


With an inductive load at

$V_{CE} = 600$ V	$T_j = 25$ °C
$V_{GE} = \pm 15$ V	$T_j = 125$ °C
$I_C = 35$ A	$T_j = 150$ °C

figure 34. IGBT

Reverse bias safe operating area
 $I_C = f(V_{CE})$



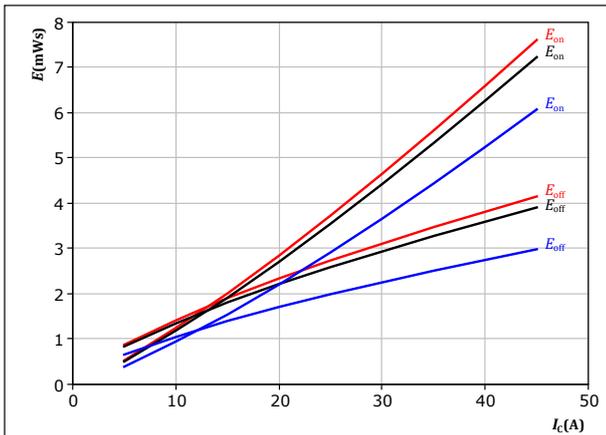
At $T_j = 150$ °C
 $R_{gon} = 4$ Ω
 $R_{goff} = 4$ Ω



Brake Switching Characteristics

figure 35. IGBT

Typical switching energy losses as a function of collector current
 $E = f(I_c)$

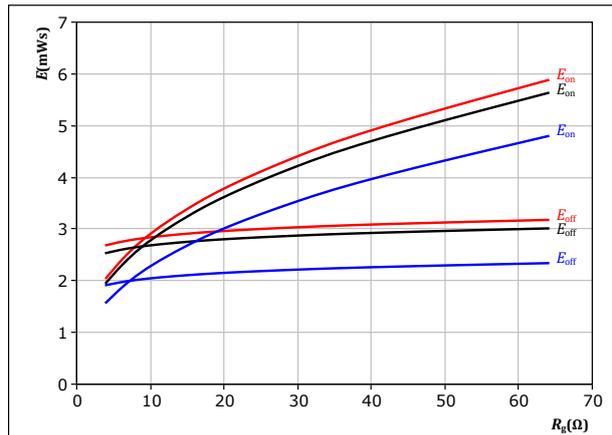


With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $R_{gon} = 16 \ \Omega$
 $R_{goff} = 16 \ \Omega$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 36. IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor
 $E = f(R_g)$

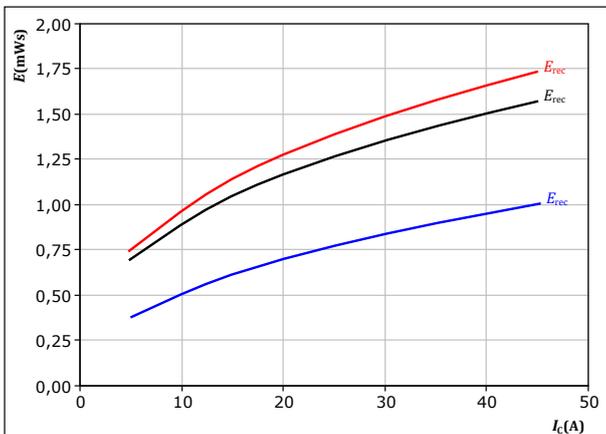


With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $I_c = 25 \text{ A}$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 37. FWD

Typical reverse recovered energy loss as a function of collector current
 $E_{rec} = f(I_c)$

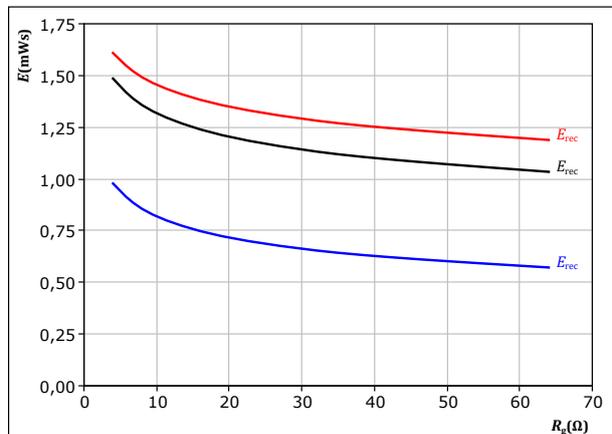


With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $R_{gon} = 16 \ \Omega$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 38. FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $I_c = 25 \text{ A}$

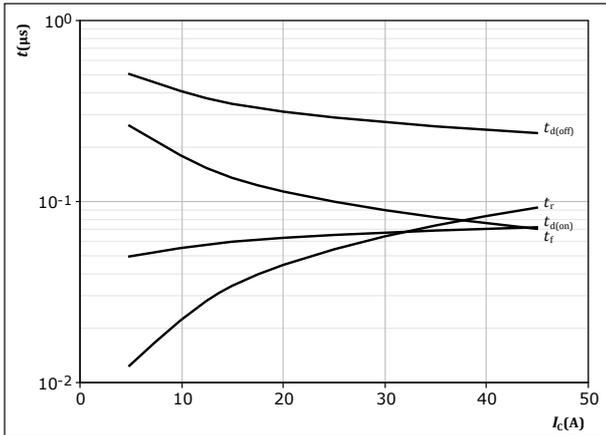
T_j :
— 25 °C
— 125 °C
— 150 °C



Brake Switching Characteristics

figure 39. IGBT

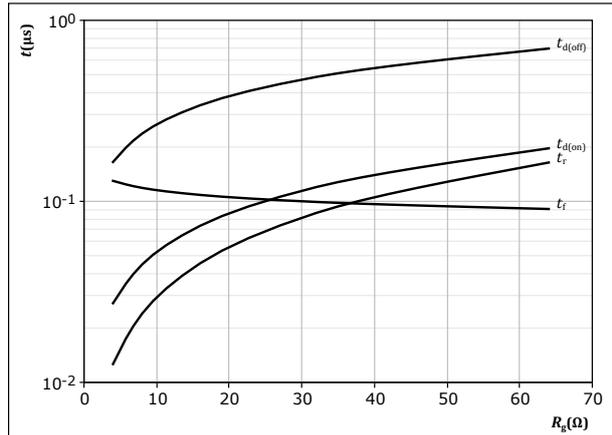
Typical switching times as a function of collector current
 $t = f(I_c)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $R_{gon} = 16 \text{ } \Omega$
 $R_{goff} = 16 \text{ } \Omega$

figure 40. IGBT

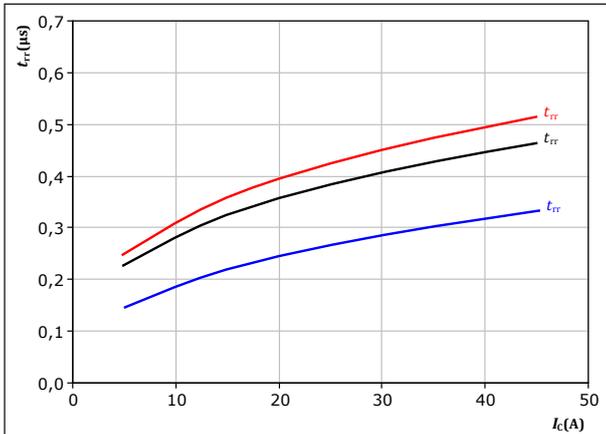
Typical switching times as a function of IGBT turn on gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $I_c = 25 \text{ A}$

figure 41. FWD

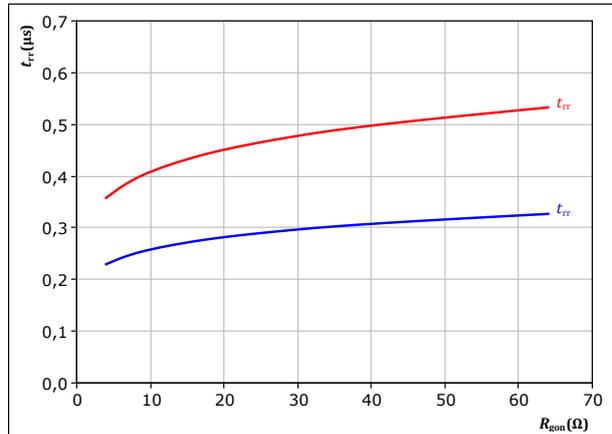
Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_c)$



With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $R_{gon} = 16 \text{ } \Omega$
 $T_j:$ — 25 °C
— 125 °C
— 150 °C

figure 42. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



With an inductive load at
 $V_{CE} = 700 \text{ V}$
 $V_{GE} = 0/15 \text{ V}$
 $I_c = 25 \text{ A}$
 $T_j:$ — 25 °C
— 125 °C
— 150 °C

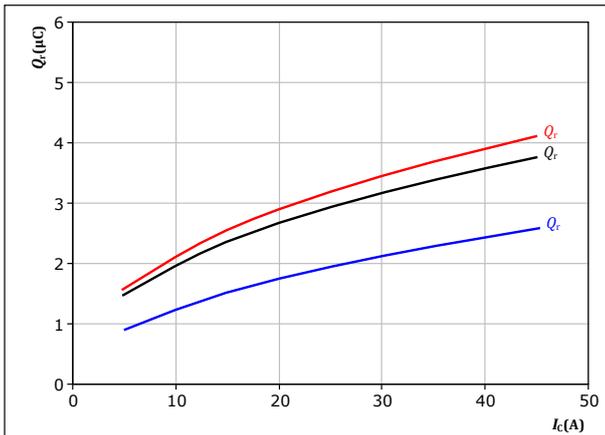


Brake Switching Characteristics

figure 43. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

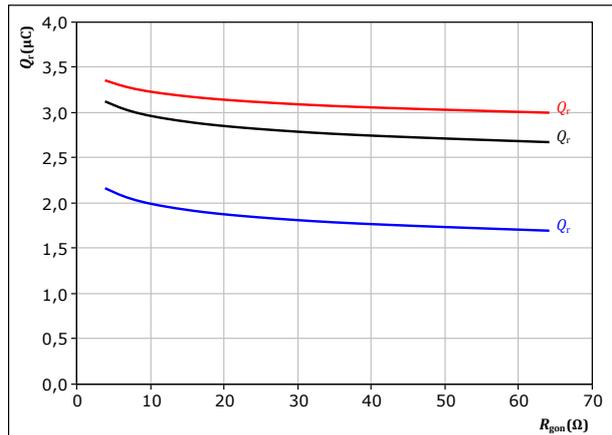
$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $R_{gon} = 16$ Ω

T_j : — 25 °C
— 125 °C
— 150 °C

figure 44. FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

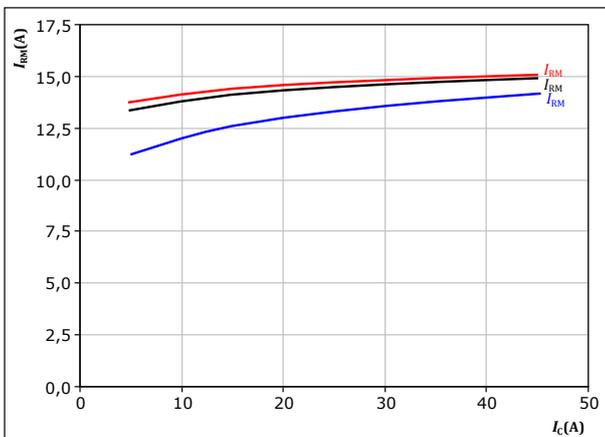
$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $I_c = 25$ A

T_j : — 25 °C
— 125 °C
— 150 °C

figure 45. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

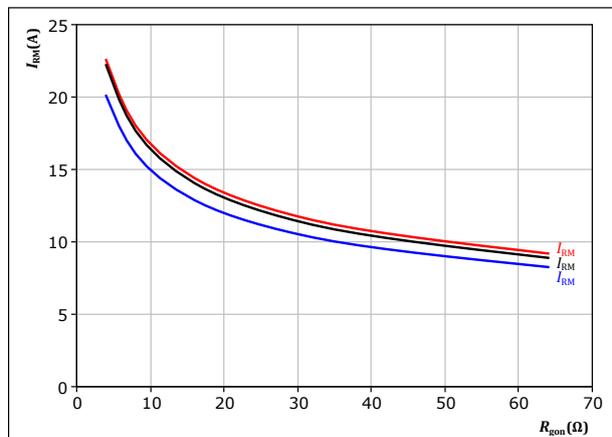
$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $R_{gon} = 16$ Ω

T_j : — 25 °C
— 125 °C
— 150 °C

figure 46. FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $I_c = 25$ A

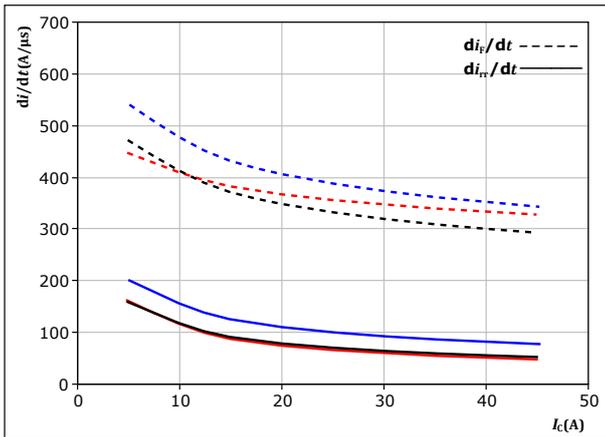
T_j : — 25 °C
— 125 °C
— 150 °C



Brake Switching Characteristics

figure 47. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_c)$



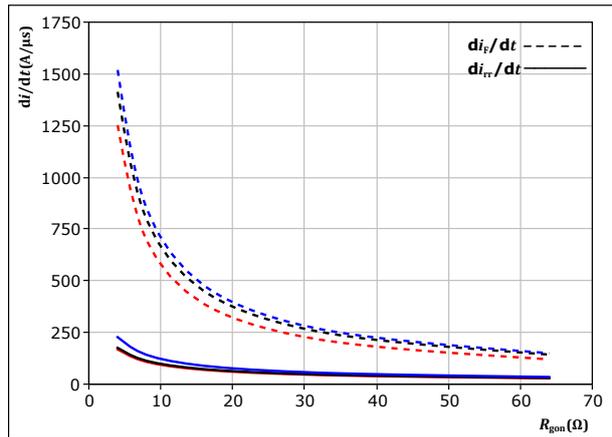
With an inductive load at

$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $R_{gon} = 16$ Ω

T_j :
 — 25 °C
 — 125 °C
 — 150 °C

figure 48. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_r/dt = f(R_{gon})$



With an inductive load at

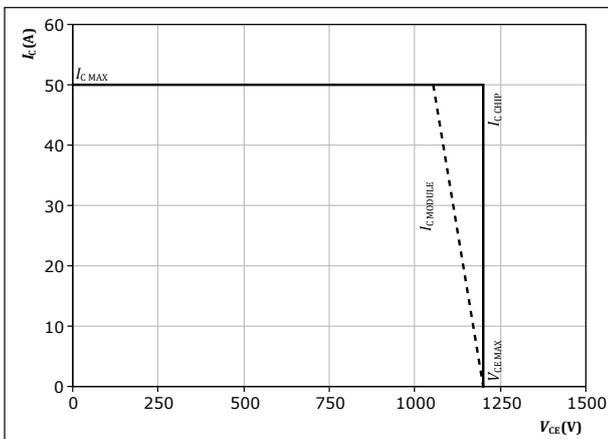
$V_{CE} = 700$ V
 $V_{GE} = 0/15$ V
 $I_c = 25$ A

T_j :
 — 25 °C
 — 125 °C
 — 150 °C

figure 49. IGBT

Reverse bias safe operating area

$I_c = f(V_{CE})$



At $T_j = 150$ °C
 $R_{gon} = 16$ Ω
 $R_{goff} = 16$ Ω



Switching Definitions

figure 50. IGBT
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

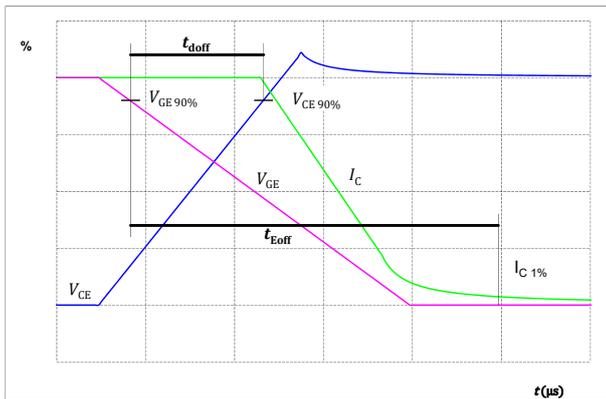


figure 51. IGBT
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

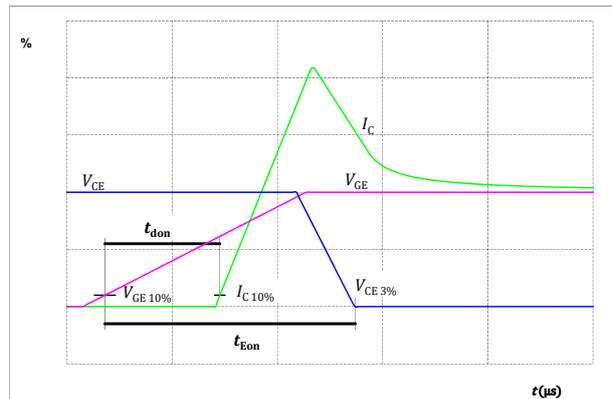


figure 52. IGBT
Turn-off Switching Waveforms & definition of t_f

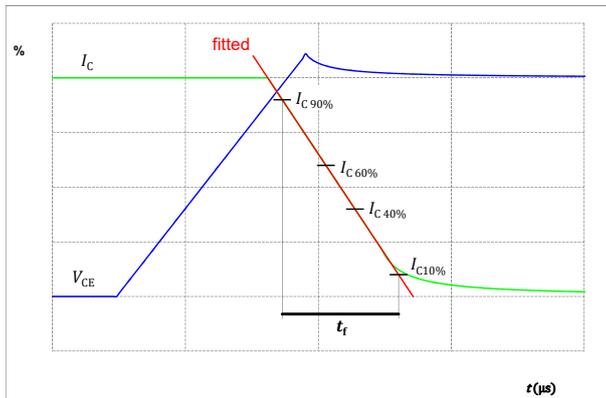
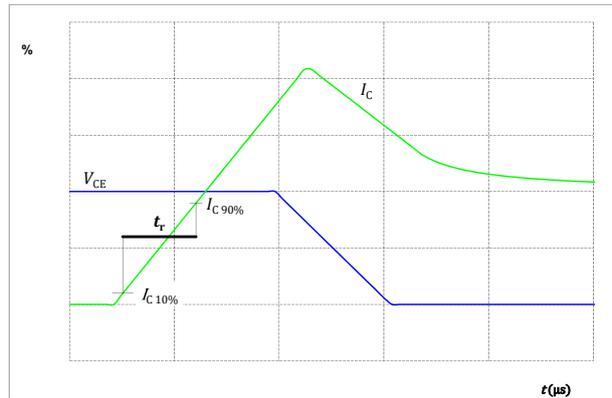


figure 53. IGBT
Turn-on Switching Waveforms & definition of t_r





Switching Definitions

figure 54. FWD

Turn-off Switching Waveforms & definition of t_{rr}

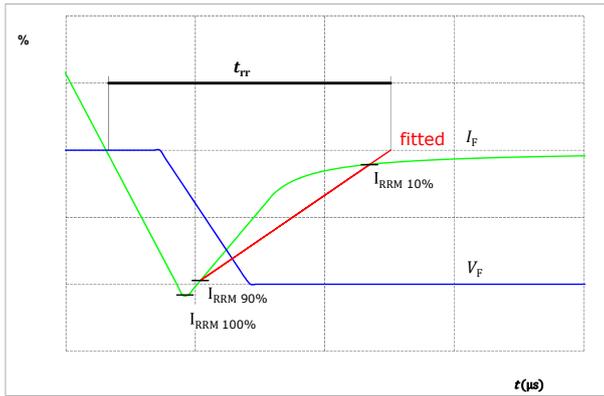
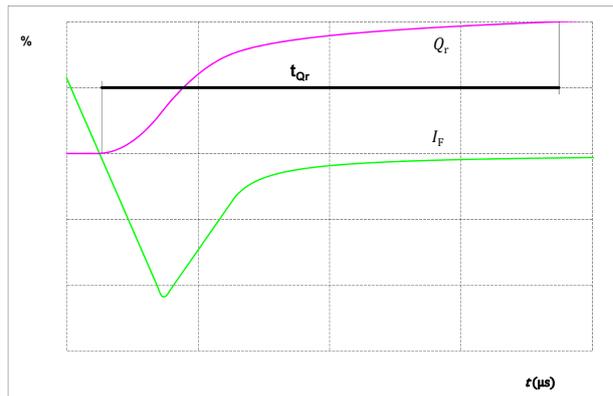


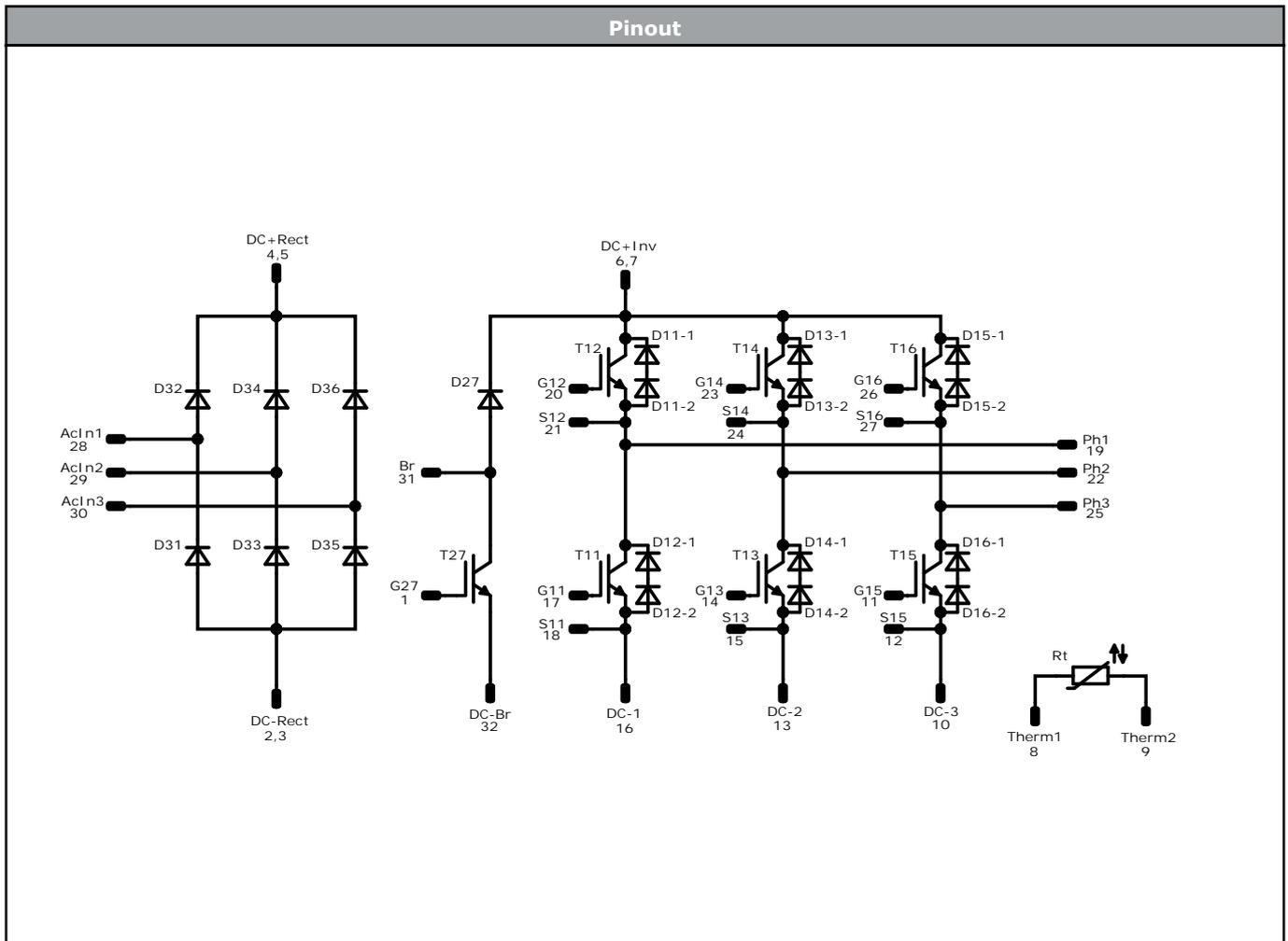
figure 55. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)





Vincotech



Identification					
ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	35 A	Inverter Switch	
D11-1, D11-2, D12-1, D12-2, D13-1, D13-2, D14-1, D14-2, D15-1, D15-2, D16-1, D16-2	FWD	1300 V	30 A	Inverter Diode	
T27	IGBT	1200 V	25 A	Brake Switch	
D27	FWD	1200 V	15 A	Brake Diode	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	45 A	Rectifier Diode	
Rt	Thermistor			Thermistor	



Vincotech

Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow 1</i> packages see vincotech.com website.

Package data
Package data for <i>flow 1</i> packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.



Document No.:	Date:	Modification:	Pages
10-FY12PMA035M703-P589A73-D1-14	5 Mar. 2026	Initial Release	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.