



Vincotech

10-EZ122PA015MS-LJ67F78T

target datasheet

flowDUAL E1 SiC

1200 V / 15 mΩ

Topology features

- Temperature sensor
- Half Bridge

Component features

- High Blocking Voltage with low drain source on state resistance
- High speed SiC-MOSFET technology
- Resistant to Latch-up

Housing features

- Base isolation: Al₂O₃
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

Target applications

- Charging Stations
- Energy Storage Systems
- General
- Industrial Drives
- Power Supply
- Servo Drives
- Solar Inverters
- UPS

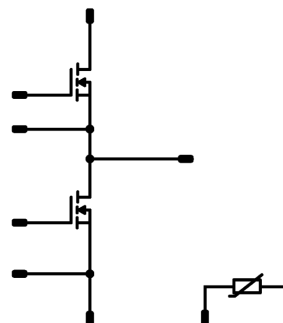
Types

- 10-EZ122PA015MS-LJ67F78T

flow E1 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	104	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	320	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	129	W
Gate-source voltage	V_{GSS}		-5 / 18	V
		dynamic	-10 / 22	
Maximum Junction Temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			8,62	mm
Comparative Tracking Index	CTI		≥ 600	



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Drain-source on-state resistance	$r_{DS(on)}$		18		80	25		15	22,5	mΩ
Gate-source threshold voltage	$V_{GS(th)}$				0,008	25	1,7	2,25	2,75	V
Gate to Source Leakage Current	I_{GSS}		22	0		25			200	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	1200		25			20	μA
Internal gate resistance	r_g							1		Ω
Gate charge	Q_g		-5/18	800	80	25		216		nC
Short-circuit input capacitance	C_{iss}	$f = 500 \text{ kHz}$	0	800	0	25		5200		pF
Short-circuit output capacitance	C_{oss}							270		
Reverse transfer capacitance	C_{rss}							12		
Diode forward voltage	V_{SD}		0		80	25		4,1		V

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						0,74		K/W
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Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 499 \text{ Ω}$				100	3,2		3,3	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,3		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 1 \%$						3380		K
Vincotech Thermistor Reference									V	



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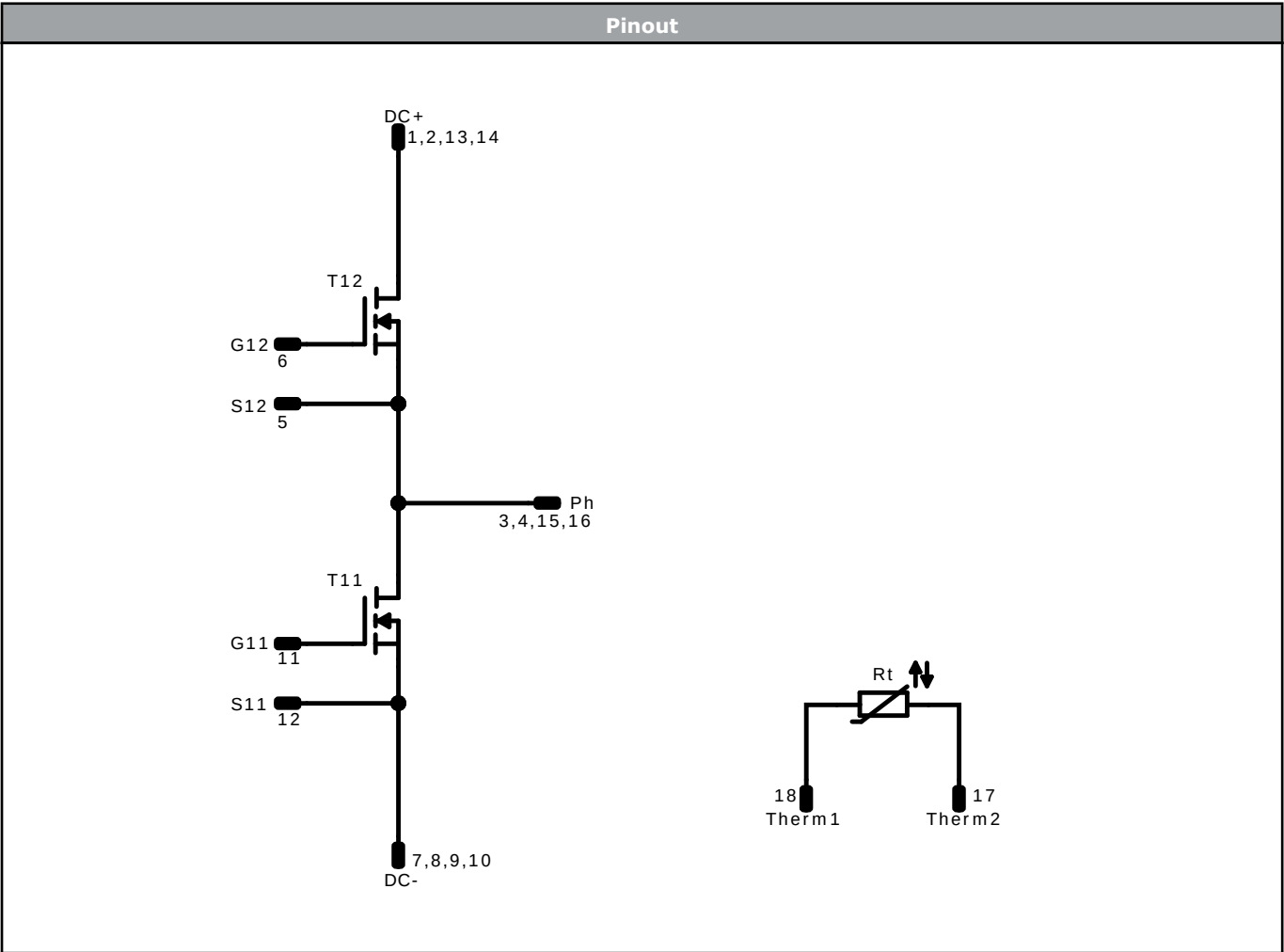
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Ordering Code	
Version	Ordering Code
Without thermal paste	10-EZ122PA015MS-LJ67F78T
With thermal paste (5,2 W/mK, PTM6000HV)	10-EZ122PA015MS-LJ67F78T-/7/

Marking						
	Text	Name	Date code	UL & VIN	Lot	Serial
		NN-NNNNNNNNNNNNNNNN- TTTT TTTT	WWYY	UL VIN	LLLLL	SSSS
	Datamatrix	Type&Ver	Lot number	Serial	Date code	
		TTTTTTTV	LLLLL	SSSS	WWYY	

Outline				
Pin table [mm]				
Pin	X	Y	Function	
1	32	3,2	DC+	
2	32	6,4	DC+	
3	32	22,4	Ph	
4	32	25,6	Ph	
5	19,2	0	S12	
6	19,2	3,2	G12	
7	25,6	12,8	DC-	
8	22,4	12,8	DC-	
9	19,2	12,8	DC-	
10	12,8	12,8	DC-	
11	19,2	22,4	G11	
12	19,2	25,6	S11	
13	6,4	3,2	DC+	
14	6,4	6,4	DC+	
15	6,4	22,4	Ph	
16	6,4	25,6	Ph	
17	0	12,8	Therm2	
18	0	16	Therm1	
				Tolerance of pinpositions: ±0.4mm at the end of pins Dimension of coordinate axis is only offset without tolerance



Identification					
ID	Component	Voltage	Current	Function	Comment
T11, T12	MOSFET	1200 V	15 mΩ	Inverter Switch	
Rt	Thermistor			Thermistor	



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Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow</i> E1 packages see vincotech.com website.

Package data
Package data for <i>flow</i> E1 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.



Document No.:	Date:	Modification:	Pages
10-EZ122PA015MS-LJ67F78T-T1-14	28 Feb. 2025	Initial Release	

Product status definition		
Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.

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