



Vincotech

10-EY12NMA010MS-LS28F76T

target datasheet

flowMNPC E2 SiC

1200 V / 10 mΩ

Topology features

- Kelvin Emitter for improved switching performance
- Temperature sensor
- Mixed Voltage Neutral Point Clamped Topology (T-Type)

Component features

- High Blocking Voltage with low drain source on state resistance
- High speed SiC-MOSFET technology
- Resistant to Latch-up

Housing features

- Base isolation: Al_2O_3
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Press-fit pin
- Reliable cold welding connection

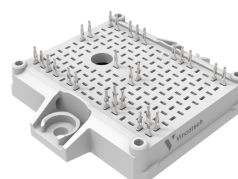
Target applications

- Charging Stations
- Industrial Drives
- Power Supply
- Solar Inverters
- UPS

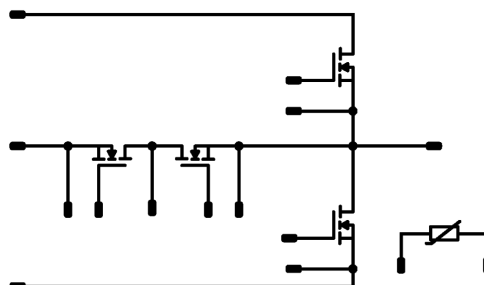
Types

- 10-EY12NMA010MS-LS28F76T

flow E2 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Buck Switch				
Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	177	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	480	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	171	W
Gate-source voltage	V_{GS}	static	-5 / 18	V
		dynamic	-10 / 22	V
Maximum Junction Temperature	T_{jmax}		175	°C

Boost Switch

Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	177	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	480	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	171	W
Gate-source voltage	V_{GS}	static	-5 / 18	V
		dynamic	-10 / 22	V
Maximum Junction Temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			9,08	mm
Comparative Tracking Index	CTI		≥ 600	



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Buck Switch

Static

Drain-source on-state resistance	$r_{DS(on)}$		18		120	25		10	15	mΩ
Gate-source threshold voltage	$V_{GS(th)}$				0,012	25	1,7	2,25	2,75	V
Gate to Source Leakage Current	I_{GSS}		22	0		25			300	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	1200		25			30	μA
Internal gate resistance	r_g							0,667		Ω
Gate charge	Q_g		-5/18	800	120	25		324		nC
Short-circuit input capacitance	C_{iss}	$f = 500 \text{ kHz}$	0	800	0	25		7800		pF
Short-circuit output capacitance	C_{oss}							405		
Reverse transfer capacitance	C_{rss}							18		
Diode forward voltage	V_{SD}		0		120	25		4,1		V

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4 \text{ W/mK}$ (PSX)						0,55		K/W
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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Boost Switch

Static

Drain-source on-state resistance	$r_{DS(on)}$		18		120	25		10	15	mΩ
Gate-source threshold voltage	$V_{GS(th)}$				0,012	25	1,7	2,25	2,75	V
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Thermal

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Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 493 \text{ Ω}$				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 2 \%$						3375		K
B-value	$B_{(25/100)}$	Tol. $\pm 2 \%$						3437		K
Vincotech Thermistor Reference									K	



target datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-EY12NMA010MS-LS28F76T
With thermal paste (5,2 W/mK, PTM6000HV)	10-EY12NMA010MS-LS28F76T-/7/

Marking							
	Text	Name		Date code	UL & VIN	Lot	Serial
		NN-NNNNNNNNNNNNN- TTTTTIVV		WWYY	UL VIN	LLLLL	SSSS
		Datamatrix	Type&Ver	Lot number	Serial	Date code	
TTTTTIVV			LLLLL	SSSS	WWYY		

Pin table [mm]			
Pin	X	Y	Function
1	32	3,2	S11
2	32	0	G11
3	3,2	0	DC+
4	0	0	DC+
5	3,2	3,2	DC+
6	0	3,2	DC+
7	3,2	12,8	GND
8	0	12,8	GND
9	3,2	16	GND
10	0	16	GND
11	0	28,8	S14
12	0	32	G14
13	0	38,4	DC-
14	0	41,6	DC-
15	0	44,8	DC-
16	0	48	DC-
17	19,2	48	S12
18	22,4	48	G12
19	32	48	Therm1
20	32	44,8	Therm2
21	32	38,4	Ph
22	32	35,2	Ph
23	32	32	Ph
24	32	19,2	Ph
25	32	16	Ph
26	32	12,8	Ph
27	16	19,2	S13
28	12,8	19,2	G13
29	16	28,8	CC

center of press-fit pin head
pin head type: TP, PCB plated through-hole Ø1mm ±0.09 / -0.06
for further PCB design rules refer to the latest handling instruction

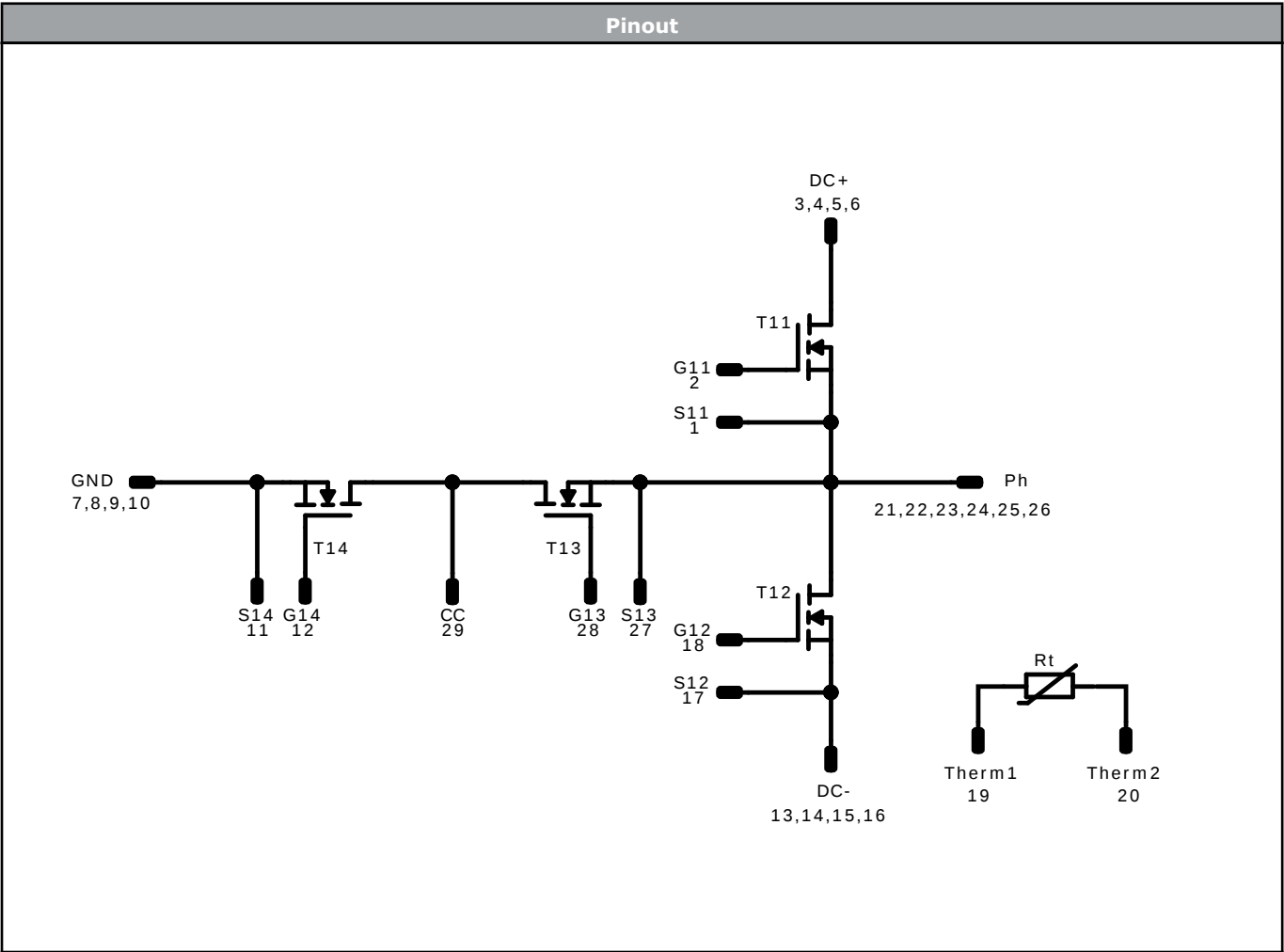
50.0 ± 0.1
3.0 ± 0.1

Tolerance of prepositions: ±0.1mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



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Identification					
ID	Component	Voltage	Current	Function	Comment
T11, T12	MOSFET	1200 V	10 mΩ	Buck Switch	
T13, T14	MOSFET	1200 V	10 mΩ	Boost Switch	
Rt	Thermistor			Thermistor	



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Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.

Package data
Package data for <i>flow</i> E2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.



Document No.:	Date:	Modification:	Pages
10-EY12NMA010MS-LS28F76T-T1-14	13 Aug. 2025	Initial release	

Product status definition		
Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.

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