



Vincotech

10-E212PNA040MS01-L188C71Z

target datasheet

flowPIM E2

1200 V / 40 mΩ

Topology features

- Converter+Inverter
- Open Emitter configuration
- SiC MOSFET
- Temperature sensor

Component features

- High Blocking Voltage with low drain source on state resistance
- High speed SiC-MOSFET technology
- Resistant to Latch-up

Housing features

- Base isolation: Al₂O₃
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Solder pin

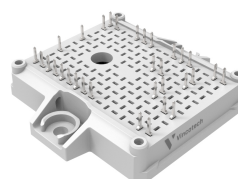
Target applications

- Embedded Drives
- Heat Pumps
- HVAC
- Industrial Drives

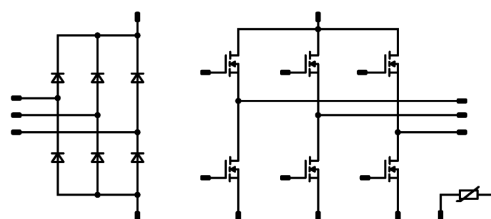
Types

- 10-E212PNA040MS01-L188C71Z

flow E2 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Drain-source voltage	V_{DS}		1200	V
Drain current (DC current)	I_D	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	44	A
Peak drain current	I_{DM}	t_p limited by T_{jmax}	120	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	77	W
Gate-source voltage	V_{GSS}	static	0 / 18	V
		dynamic	-5 / 22	V
Maximum Junction Temperature	T_{jmax}		175	°C

Rectifier Diode

Peak repetitive reverse voltage	V_{RRM}		1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	76	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 150\text{ °C}$	400	A
Surge current capability	I^2t		800	A²s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	99	W
Maximum junction temperature	T_{jmax}		175	°C

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage $t_p = 2\text{ s}$	6000	V
Creepage distance			> 12,7	mm
Clearance			9,11	mm
Comparative Tracking Index	CTI		≥ 600	



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Drain-source on-state resistance	$r_{DS(on)}$		18		30	25 150		40 44	55,2	mΩ
Gate-source threshold voltage	$V_{GS(th)}$				0,003	25	3,6	4,6	5,6	V
Gate to Source Leakage Current	I_{GSS}		22	0		25			200	nA
Zero Gate Voltage Drain Current	I_{DSS}		0	1200		25			100	μA
Internal gate resistance	r_g							3		Ω
Gate charge	Q_g		0/18		30	25		185		nC
Short-circuit input capacitance	C_{iss}		0	10	0	25		4000		pF
Short-circuit output capacitance	C_{oss}							1300		
Reverse transfer capacitance	C_{rss}							110		

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 5,2 \text{ W/mK}$ (PTM)						1,23		K/W
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Rectifier Diode

Static

Forward voltage	V_F				35	25		1	1,5	V
Reverse leakage current	I_R	$V_r = 1600 \text{ V}$				25 150			50 1000	μA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 5,2 \text{ W/mK}$ (PTM)						0,96		K/W
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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 499 \Omega$				100	3,2		3,3	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,3		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 1 \%$						3380		K
Vincotech Thermistor Reference									V	



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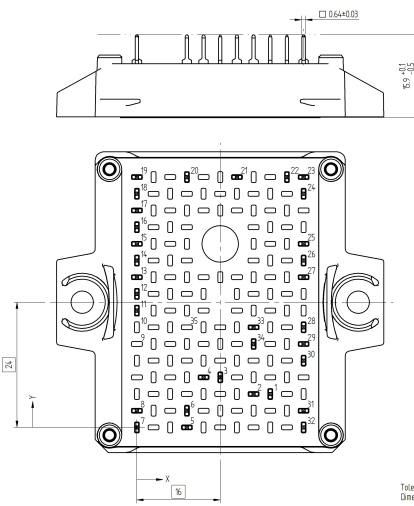
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Ordering Code	
Version	Ordering Code
Without thermal paste	10-E212PNA040MS01-L188C71Z
With thermal paste (5,2 W/mK, PTM6000HV)	10-E212PNA040MS01-L188C71Z-/7/

Marking						
	Text	Name NN-NNNNNNNNNNNNNN- TTTTTVV	Date code WWYY	UL & VIN UL VIN	Lot LLLLL	Serial SSSS
	Datamatrix	Type&Ver TTTTTTVV	Lot number LLLLL	Serial SSSS	Date code WWYY	

Outline			
Pin table [mm]			
Pin	X	Y	Function
1	25,6	6,4	ACIn2
2	22,4	6,4	ACIn2
3	16	9,6	ACIn1
4	12,8	9,6	ACIn1
5	9,6	0	DC+Rect
6	9,6	3,2	DC+Rect
7	0	0	DC-Rect
8	0	3,2	DC-Rect
9	not assembled		
10	not assembled		
11	0	22,4	G11
12	0	25,6	DC-1
13	0	28,8	DC-1
14	0	32	G13
15	0	35,2	DC-2
16	0	38,4	DC-2
17	0	41,6	G15
18	0	44,8	DC-3
19	0	48	DC-3
20	9,6	48	Therm1
21	19,2	48	Therm2
22	28,8	48	G16
23	32	48	Ph3
24	32	44,8	Ph3
25	32	35,2	G14
26	32	32	Ph2
27	32	28,8	Ph2
28	32	19,2	G12
29	32	16	Ph1
30	32	12,8	Ph1
31	32	3,2	ACIn3
32	32	0	ACIn3
33	22,4	19,2	DC+Inv
34	22,4	16	DC+Inv
35	not assembled		

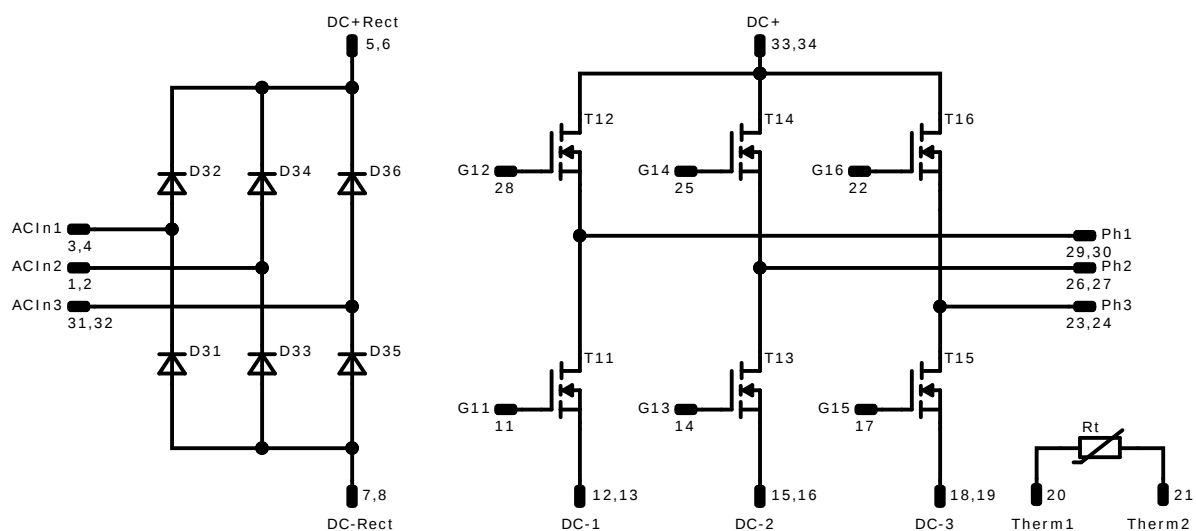


Tolerance of positions: $\pm 0,4\text{mm}$ at the end of pins
Dimension of coordinate axis is only offset without tolerance



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Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	MOSFET	1200 V	40 mΩ	Inverter Switch	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	35 A	Rectifier Diode	
Rt	Thermistor			Thermistor	



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Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample
Handling instruction				
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.				
Package data				
Package data for <i>flow</i> E2 packages see vincotech.com website.				
Vincotech thermistor reference				
See Vincotech thermistor reference table at vincotech.com website.				
UL recognition and file number				
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.				

Document No.:	Date:	Modification:	Pages
10-E212PNA040MS01-L188C71Z-T1-14	22 Oct. 2025		

Product status definition		
Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.