



flowPACK E2

1200 V / 75 A

Topology features

- Inverter
- Open Emitter configuration
- Temperature sensor

Component features

- Easy paralleling
- Low turn-off losses
- Low collector emitter saturation voltage
- Positive temperature coefficient
- Short tail current
- Switching optimized for EMC

Housing features

- Base isolation: Al₂O₃
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Solder pin

Target applications

- Industrial Drives
- UPS

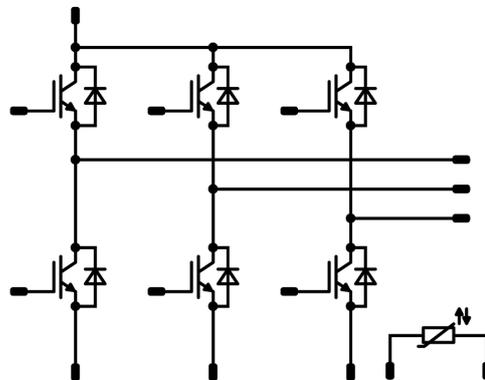
Types

- 10-E2126PA075M7-L197F78Z

flow E2 12 mm housing



Schematic





Vincotech

Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	75	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	150	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	135	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	9,5	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Inverter Diode

Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	70	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	200	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	121	W
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	$^{\circ}\text{C}$
Operation temperature under switching condition	T_{jop}		-40...+($T_{jmax} - 25$)	$^{\circ}\text{C}$

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			9,08	mm
Comparative Tracking Index	CTI		≥ 600	

*100 % tested in production



Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	I_D [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0075	25	5,4	6	6,6	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		75	25 125 150		1,55 1,7 1,75	1,9 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			100	μA
Gate-emitter leakage current	I_{GES}		20	0		25			500	nA
Internal gate resistance	r_g							4		Ω
Input capacitance	C_{ies}							16000		pF
Output capacitance	C_{oes}		0	10		25		480		pF
Reverse transfer capacitance	C_{res}							190		pF
Gate charge	Q_g	$V_{CC} = 600$ V	0/15		75	25		570		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,7		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 2 \Omega$ $R_{goff} = 2 \Omega$	±15	600	75	25		197,2		ns				
						125		208,2						
						150		211,8						
Rise time	t_r									25		28,6		ns
										125		37,6		
										150		38,6		
Turn-off delay time	$t_{d(off)}$									25		203,4		ns
						125		233						
						150		241,8						
Fall time	t_f					25		86,36		ns				
						125		112,58						
						150		111,22						
Turn-on energy (per pulse)	E_{on}	$Q_{tfwd} = 8,54 \mu C$ $Q_{tfwd} = 13,39 \mu C$ $Q_{tfwd} = 15,31 \mu C$				25		5,56		mWs				
						125		7,82						
						150		8,5						
Turn-off energy (per pulse)	E_{off}					25		5,08		mWs				
						125		6,8						
						150		7,28						



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10-E2126PA075M7-L197F78Z
datasheet

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
		V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max		
Inverter Diode										
Static										
Forward voltage	V_F				100	25 125 150		1,82 1,96 1,97	2,1 ⁽¹⁾	V
Reverse leakage current	I_R	$V_r = 1200$ V				25			40	μA
Thermal										
Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						0,79		K/W
Dynamic										
Peak recovery current	I_{RM}					25 125 150		74,72 76,64 78,09		A
Reverse recovery time	t_{rr}					25 125 150		277,69 432,14 458,54		ns
Recovered charge	Q_r	$di/dt=2268$ A/μs $di/dt=1969$ A/μs $di/dt=1970$ A/μs	±15	600	75	25 125 150		8,54 13,39 15,31		μC
Reverse recovered energy	E_{rec}					25 125 150		3,2 5,19 6		mWs
Peak rate of fall of recovery current	$(di_r/dt)_{max}$					25 125 150		801,95 613,64 544,2		A/μs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit	
		V_{GS} [V]	V_{GE} [V]	V_{DS} [V]	V_{CE} [V]	V_F [V]	I_D [A]	I_C [A]	I_F [A]		T_j [°C]

Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$A_{R/R}$	$R_{100} = 493 \Omega$				100	-5		5	%
Power dissipation	P					25		245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. $\pm 2 \%$						3375		K
B-value	$B_{(25/100)}$	Tol. $\pm 2 \%$						3437		K
Vincotech Thermistor Reference									K	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



Inverter Switch Characteristics

figure 1. IGBT

Typical output characteristics
 $I_C = f(V_{CE})$

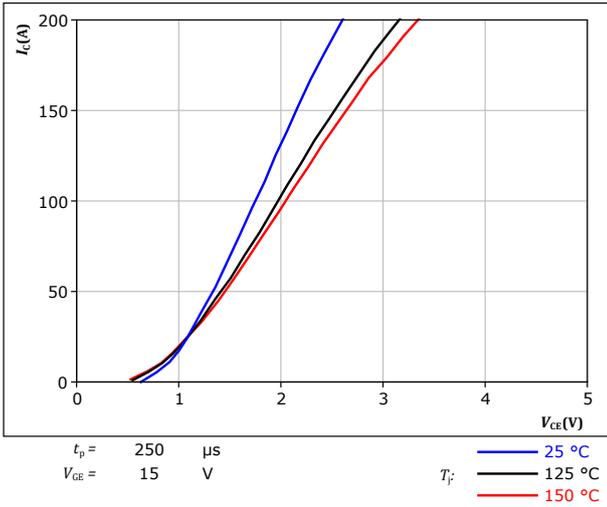


figure 2. IGBT

Typical output characteristics
 $I_C = f(V_{CE})$

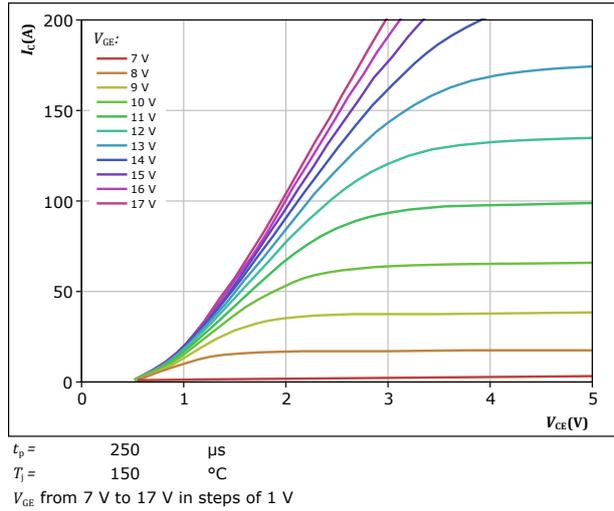


figure 3. IGBT

Typical transfer characteristics
 $I_C = f(V_{GE})$

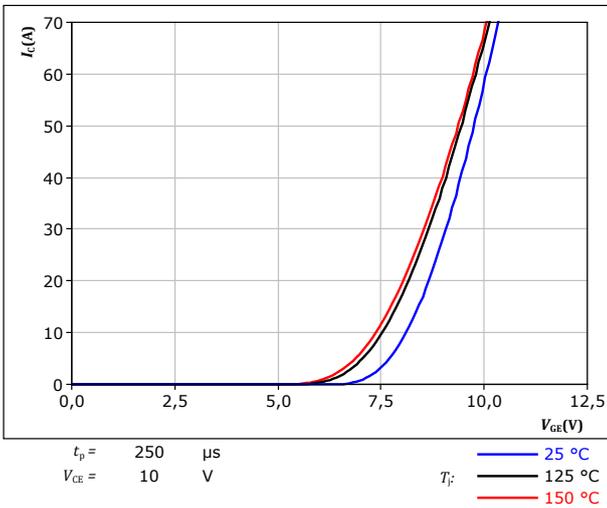
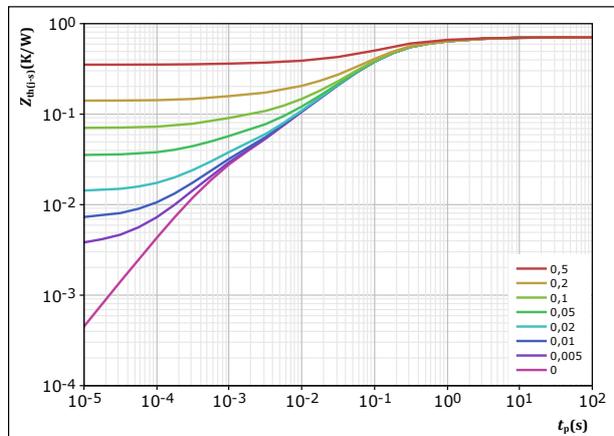


figure 4. IGBT

Transient thermal impedance as a function of pulse width
 $Z_{th(j-s)} = f(t_p)$



$D = t_p / T$
 $R_{th(j-s)} = 0,704 \text{ K/W}$

IGBT thermal model values

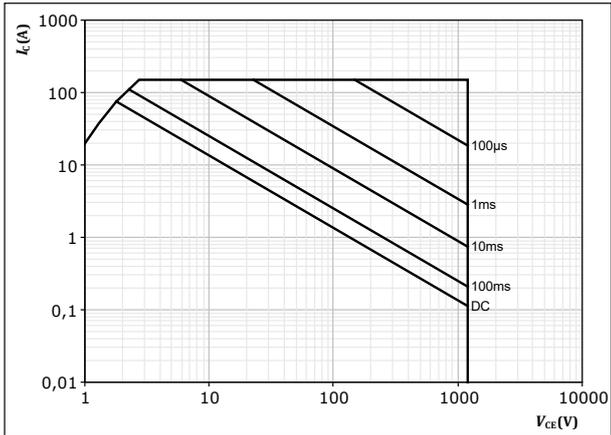
R (K/W)	τ (s)
3,63E-02	4,77E+00
9,31E-02	1,03E+00
3,22E-01	1,68E-01
1,83E-01	5,28E-02
4,91E-02	8,42E-03
2,13E-02	6,25E-04



Inverter Switch Characteristics

figure 5. IGBT

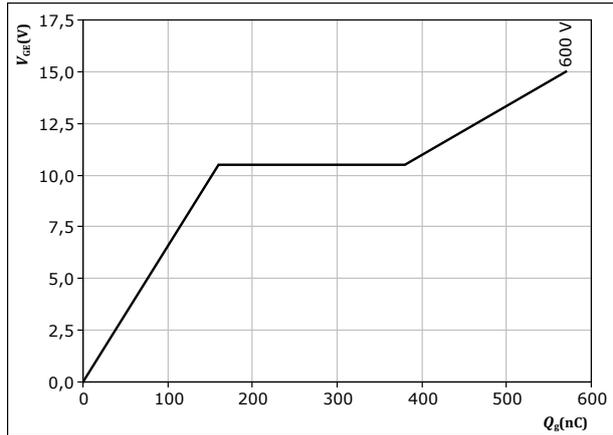
Safe operating area
 $I_C = f(V_{CE})$



$D =$ single pulse
 $T_s = 80 \text{ } ^\circ\text{C}$
 $V_{GE} = 15 \text{ V}$
 $T_j = T_{jmax}$

figure 6. IGBT

Gate voltage vs gate charge
 $V_{GE} = f(Q_g)$



$I_C = 75 \text{ A}$
 $T_j = 25 \text{ } ^\circ\text{C}$



Inverter Diode Characteristics

figure 7. FWD

Typical forward characteristics

$$I_F = f(V_F)$$

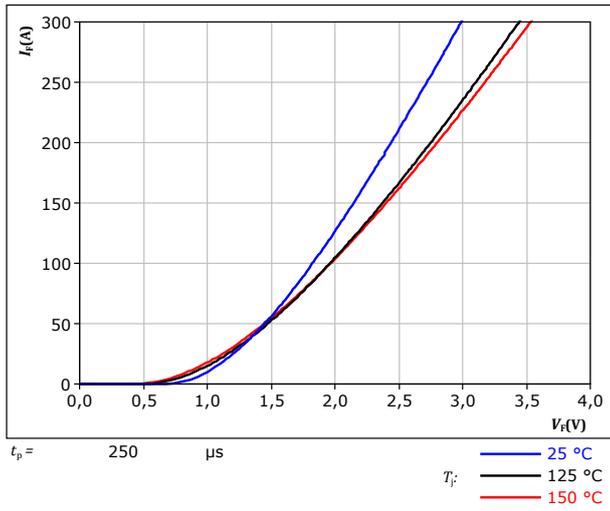
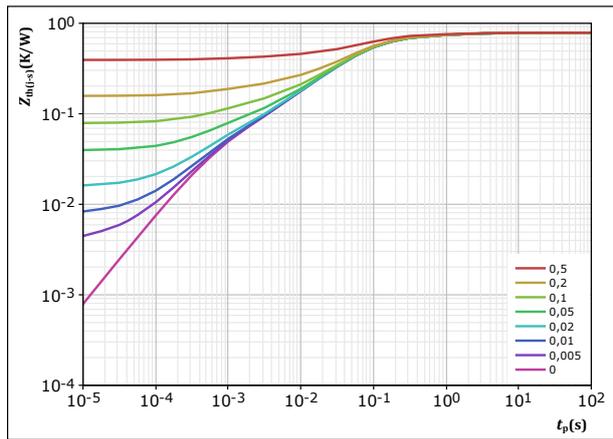


figure 8. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 0,787$ K/W
 FWD thermal model values

R (K/W)	τ (s)
4,05E-02	3,25E+00
9,02E-02	5,38E-01
3,71E-01	8,95E-02
1,97E-01	3,04E-02
5,23E-02	4,59E-03
3,58E-02	6,26E-04

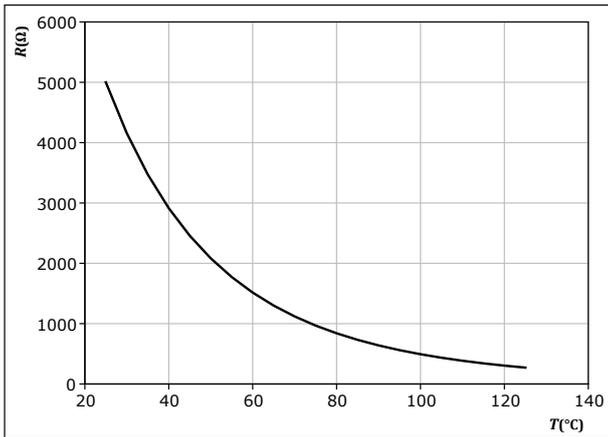


Thermistor Characteristics

figure 9. Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$

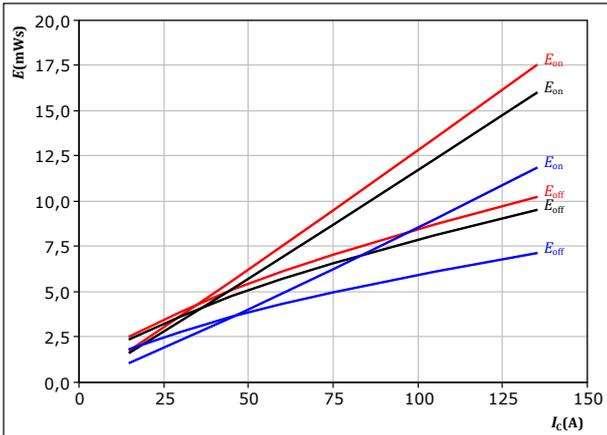




Inverter Switching Characteristics

figure 10. IGBT

Typical switching energy losses as a function of collector current
 $E = f(I_c)$

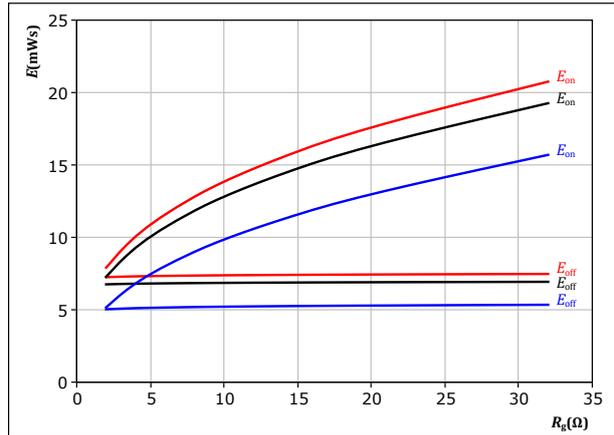


With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 2$ Ω
 $R_{goff} = 2$ Ω

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 11. IGBT

Typical switching energy losses as a function of IGBT turn on gate resistor
 $E = f(R_g)$

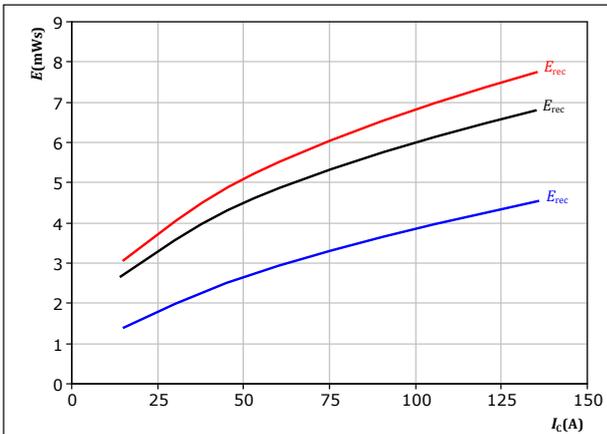


With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 75$ A

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 12. FWD

Typical reverse recovered energy loss as a function of collector current
 $E_{rec} = f(I_c)$

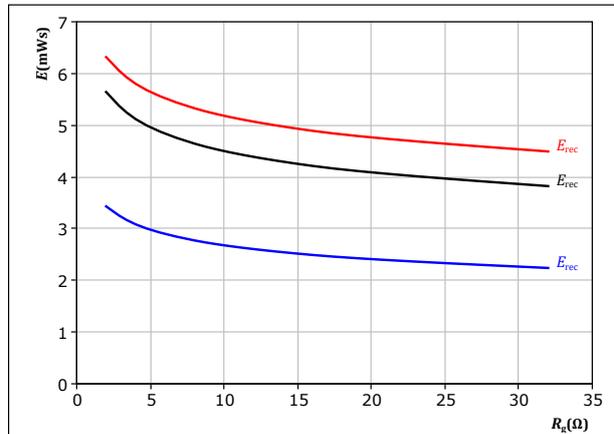


With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 2$ Ω

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 13. FWD

Typical reverse recovered energy loss as a function of IGBT turn on gate resistor
 $E_{rec} = f(R_g)$



With an inductive load at
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 75$ A

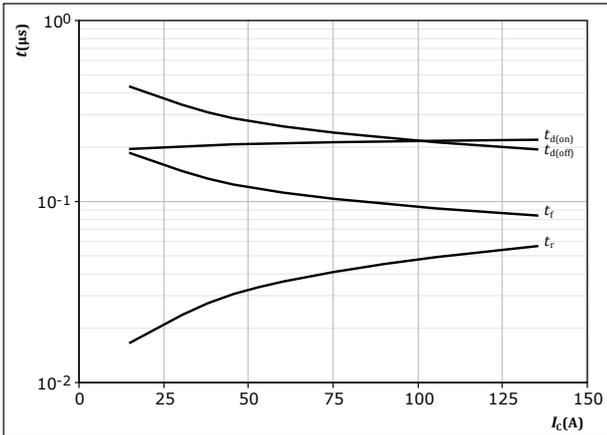
T_j : 25 °C (blue), 125 °C (black), 150 °C (red)



Inverter Switching Characteristics

figure 14. IGBT

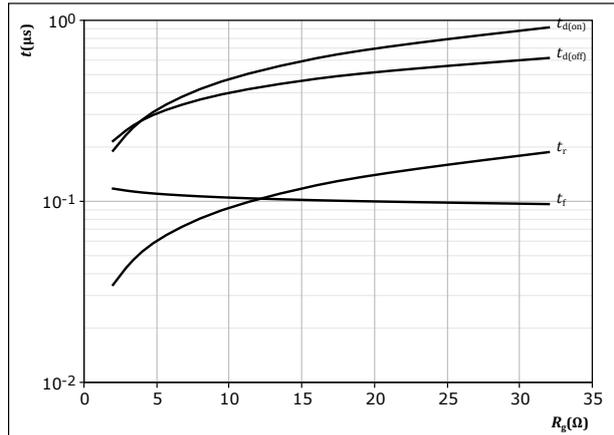
Typical switching times as a function of collector current
 $t = f(I_c)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$
 $R_{goff} = 2 \text{ } \Omega$

figure 15. IGBT

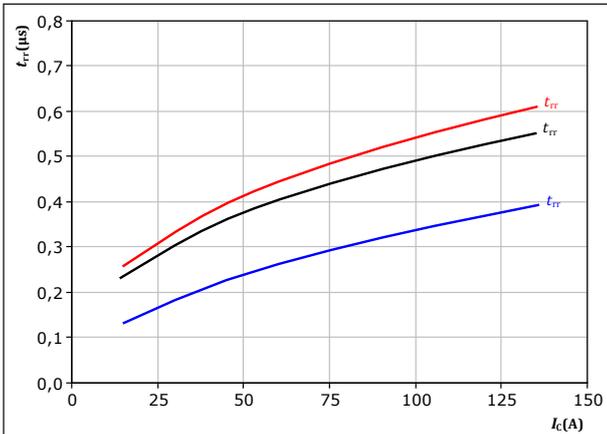
Typical switching times as a function of IGBT turn on gate resistor
 $t = f(R_g)$



With an inductive load at
 $T_j = 150 \text{ }^\circ\text{C}$
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 75 \text{ A}$

figure 16. FWD

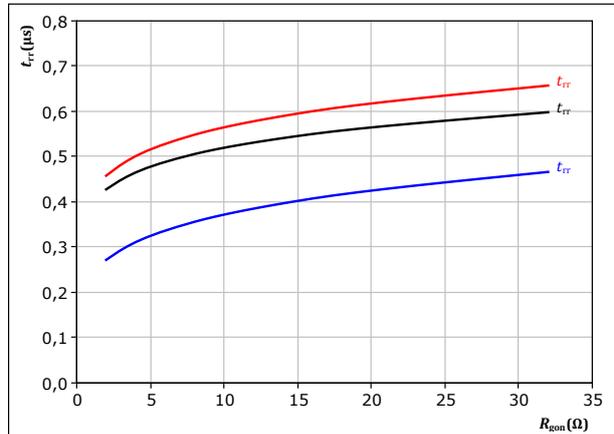
Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_c)$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 2 \text{ } \Omega$
 $T_j:$ — 25 °C
 — 125 °C
 — 150 °C

figure 17. FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$



With an inductive load at
 $V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 75 \text{ A}$
 $T_j:$ — 25 °C
 — 125 °C
 — 150 °C

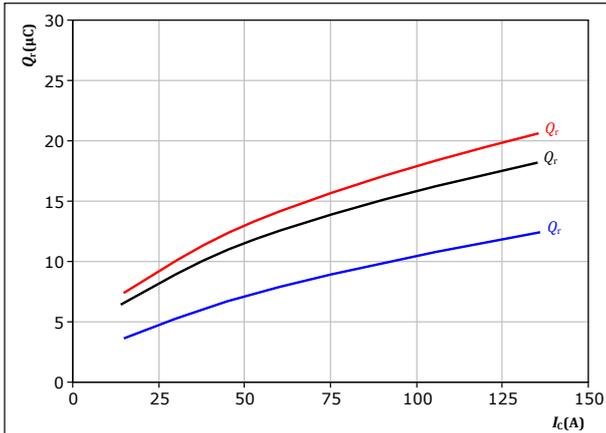


Inverter Switching Characteristics

figure 18. FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_c)$$



With an inductive load at

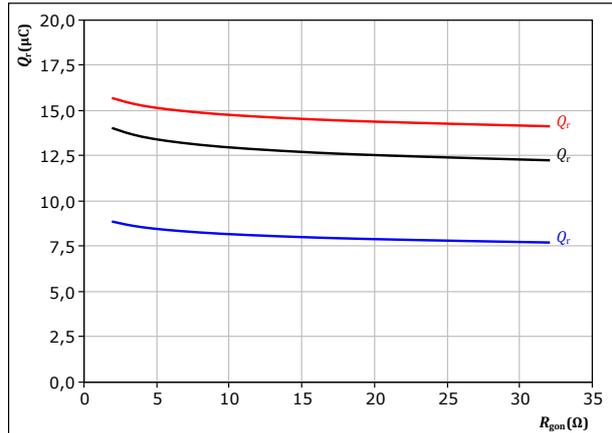
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 2 \ \Omega$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 19. FWD

Typical recovered charge as a function of IGBT turn on gate resistor

$$Q_r = f(R_{gon})$$



With an inductive load at

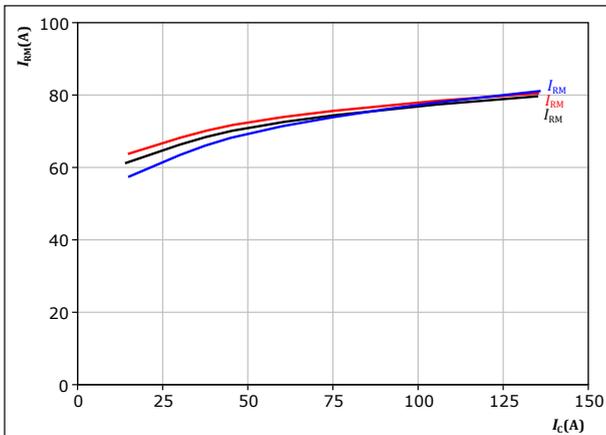
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 75 \text{ A}$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 20. FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_c)$$



With an inductive load at

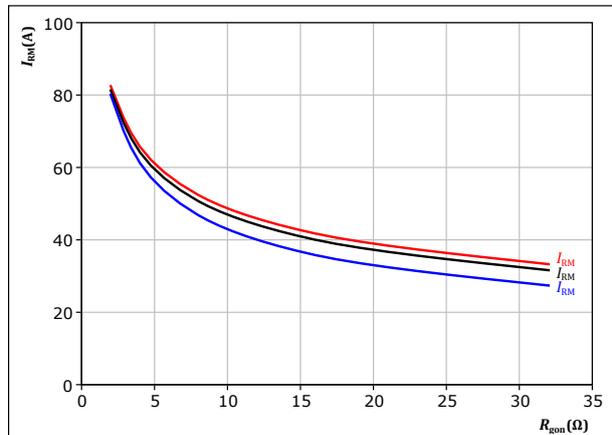
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 2 \ \Omega$

T_j : — 25 °C
— 125 °C
— 150 °C

figure 21. FWD

Typical peak reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RM} = f(R_{gon})$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 75 \text{ A}$

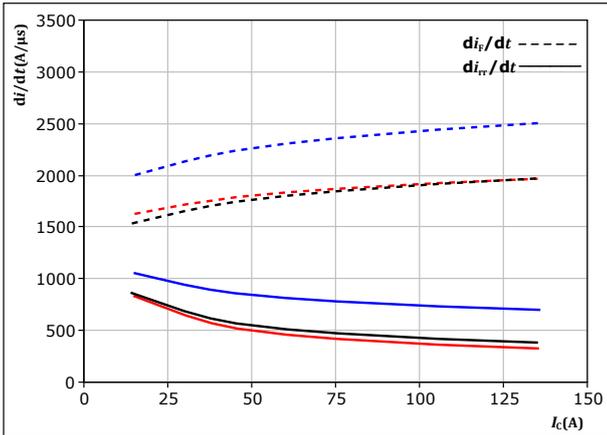
T_j : — 25 °C
— 125 °C
— 150 °C



Inverter Switching Characteristics

figure 22. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_{rr}/dt = f(I_c)$



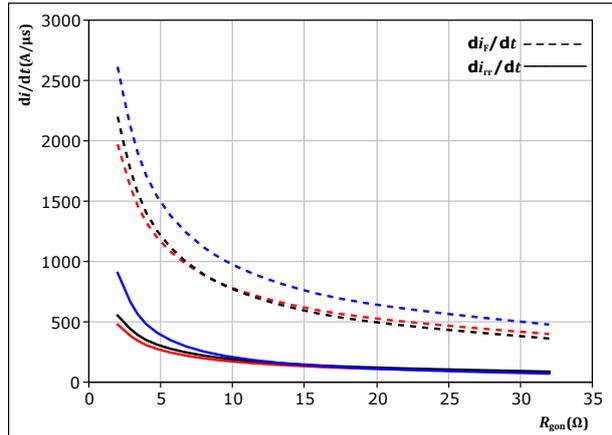
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 2 \ \Omega$

$T_j:$ — 25 °C
 — 125 °C
 — 150 °C

figure 23. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn on gate resistor
 $di_f/dt, di_{rr}/dt = f(R_{gon})$



With an inductive load at

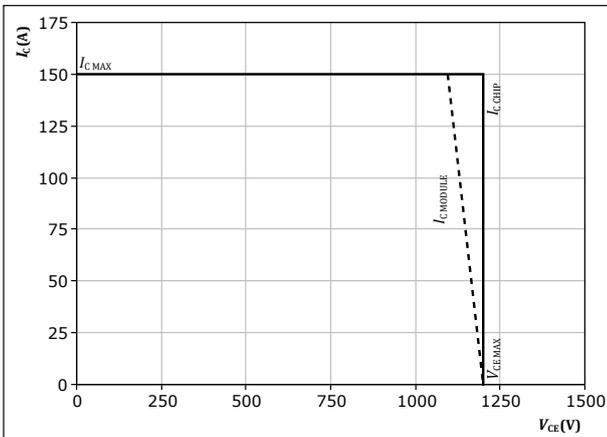
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_c = 75 \text{ A}$

$T_j:$ — 25 °C
 — 125 °C
 — 150 °C

figure 24. IGBT

Reverse bias safe operating area

$I_c = f(V_{CE})$



At $T_j = 150 \text{ °C}$
 $R_{gon} = 2 \ \Omega$
 $R_{goff} = 2 \ \Omega$



Inverter Switching Definitions

figure 25. IGBT
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

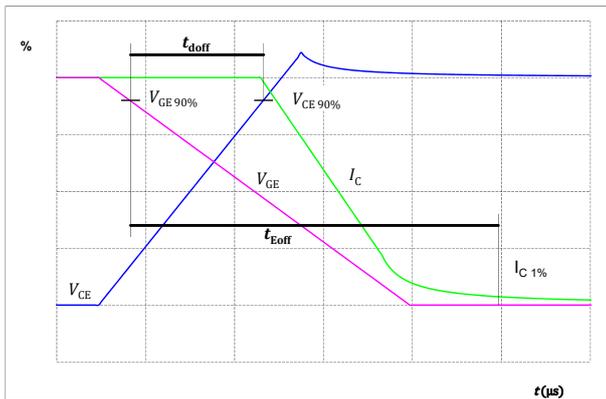


figure 26. IGBT
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

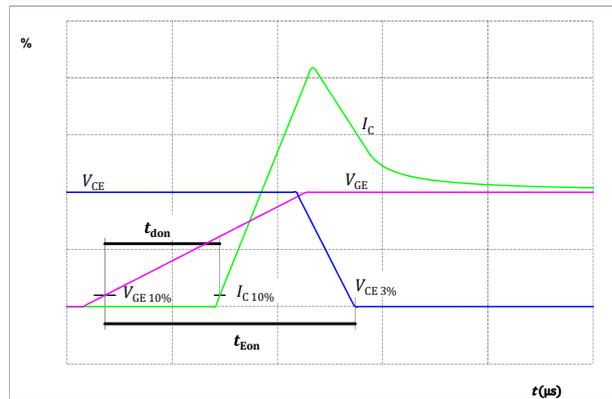


figure 27. IGBT
Turn-off Switching Waveforms & definition of t_f

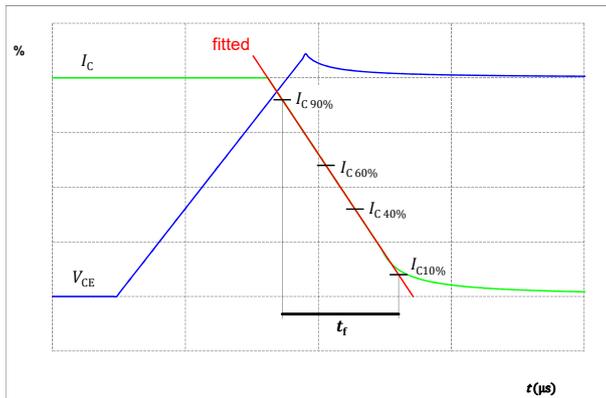
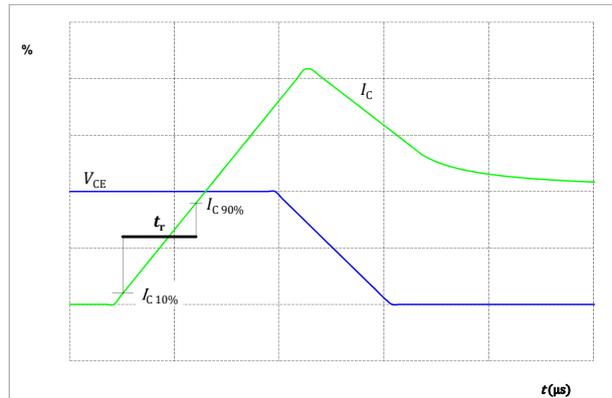


figure 28. IGBT
Turn-on Switching Waveforms & definition of t_r





Inverter Switching Definitions

figure 29. FWD

Turn-off Switching Waveforms & definition of t_{rr}

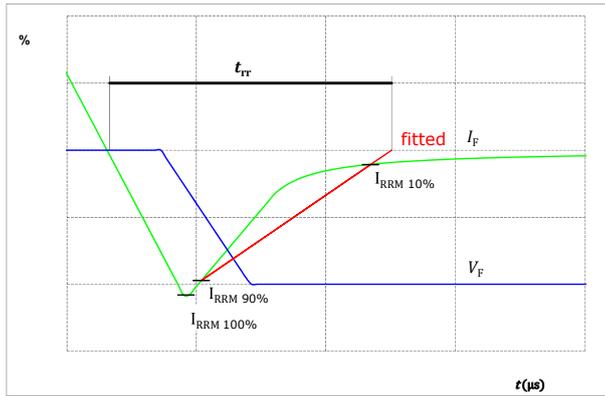
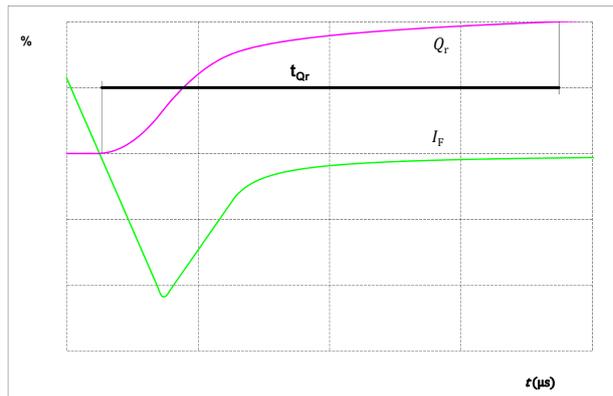


figure 30. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)





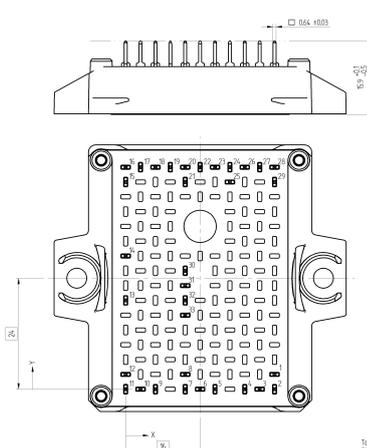
Vincotech

10-E2126PA075M7-L197F78Z
datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-E2126PA075M7-L197F78Z
With thermal paste (5,2 W/mK, PTM6000HV)	10-E2126PA075M7-L197F78Z-/7/

Marking						
	Text	Name NN-NNNNNNNNNNNNNN- TTTTTVV	Date code WWYY	UL & VIN UL VIN	Lot LLLLL	Serial SSSS
	Datamatrix	Type&Ver TTTTTTTV	Lot number LLLLL	Serial SSSS	Date code WWYY	

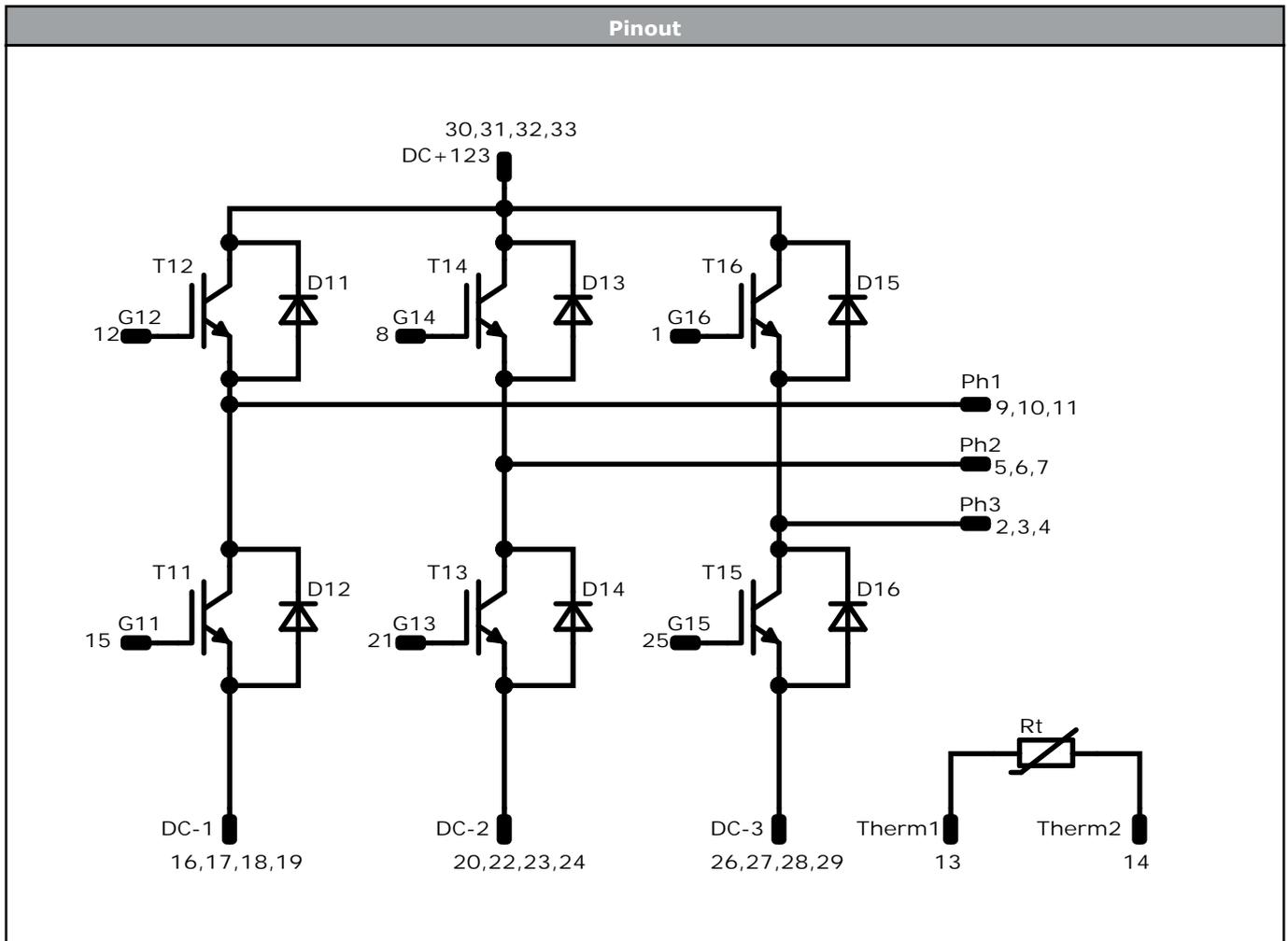
Outline				
Pin table [mm]				
Pin	X	Y	Function	
1	32	3,2	G16	
2	32	0	Ph3	
3	28,8	0	Ph3	
4	25,6	0	Ph3	
5	19,2	0	Ph2	
6	16	0	Ph2	
7	12,8	0	Ph2	
8	12,8	3,2	G14	
9	6,4	0	Ph1	
10	3,2	0	Ph1	
11	0	0	Ph1	
12	0	3,2	G12	
13	0	19,2	Therm1	
14	0	28,8	Therm2	
15	0	44,8	G11	
16	0	48	DC-1	
17	3,2	48	DC-1	
18	6,4	48	DC-1	
19	9,6	48	DC-1	
20	12,8	48	DC-2	
21	12,8	44,8	G13	
22	16	48	DC-2	
23	19,2	48	DC-2	
24	22,4	48	DC-2	
25	22,4	44,8	G15	
26	25,6	48	DC-3	
27	28,8	48	DC-3	
28	32	48	DC-3	
29	32	44,8	DC-3	
30	12,8	25,6	DC+	
31	12,8	22,4	DC+	
32	12,8	19,2	DC+	
33	12,8	16	DC+	



Tolerance of pitch/position: ±0.4mm at the end of pins
Dimension of coordinate axes in axis of first without tolerance



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Identification					
ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	75 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	100 A	Inverter Diode	
Rt	NTC			Thermistor	



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Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.

Package data
Package data for <i>flow</i> E2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.



Document No.:	Date:	Modification:	Pages
10-E2126PA075M7-L197F78Z-D6-14	16 Jan. 2026	New datasheet format. Module is unchanged.	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.