# Vincotech

# A step-by-step tutorial **VINCOSIM** + **PIEGS**

Mastering VINcoSIM PLECS model generation

EMPOWERING YOUR IDEAS



# Contents

1	Inti	oduction	2
2	Мо	deling Power Modules in PLECS	2
3	Мо	deling Power Module Topology	3
4	ΑP	LECS Library of Functional Topologies	5
5	Мо	deling Semiconductor Characteristics	6
6	Rel	ationship Between the Topology Model and the Semiconductor	
Cha	aract	eristics	8
7	Нον	v to import VINcoSIM models to PLECS	9
8	Ste	p-by-step User Guide on Using Vincotech PLECS Models	12
8	.1	Vincotech PLECS Library	12
8	.2	Simulate Vincotech Power Modules in PLECS	



# **1** Introduction

VINcoSIM, developed by Vincotech, is a comprehensive web-based power module simulator tool that helps customers identify the best power module for their application. The tool has been expanded to include a new feature that lets customers download Vincotech power module characteristics data in a PLECS-compatible format.

PLECS, the multi-domain simulation platform for power electronic systems developed by PLEXIM, lets users model and simulate comprehensive electrical systems, including power sources, converters, and loads. Simulating an entire application with proper control in PLECS requires an electrical and thermal model of the power module and topology circuit in PLECS format.

In addition to its existing library elements, Vincotech has created a library of powermodule-optimized PLECS topology models to simplify the workflow for building electrothermal simulations in PLECS that use Vincotech power modules.

This document guides readers through setting up and simulating Vincotech power modules in the PLECS simulation environment.

# 2 Modeling Power Modules in PLECS

Modeling power modules involves decomposing them into electrically functional topologies, which include semiconductor devices, such as transistors and diodes, and their interconnections. Each of these functional topologies transforms electrical energy from one form to another with an efficiency determined by the converter application parameters and the semiconductor operating characteristics. The primary objective of power module converter design is to optimize the application to maximize efficiency while meeting expectations in terms of reliability and lifetime. Power electronics design engineers rely on simulation tools to calculate the efficiency, losses and device junction temperatures of power converter systems and their constituent elements.

In this effort, the efficiency and loss calculation of the power module – a key component of the power converter – is crucial. To accurately calculate these parameters, both the topology model of the power module and the characteristics of the semiconductors and their packaging are essential.



## **3 Modeling Power Module Topology**

Power modules incorporate multiple semiconductors in various combinations, resulting in complex electrical circuits. They typically integrate various power conversion stages, such as AC/DC, DC/DC, and DC/AC, within a single package.



*Figure 1: A typical power module topology including three conversion stages.* 

Despite their complexity, power converter circuits can be decomposed into so-called functional topology elements, each responsible for a given power conversion task. For example, the power module circuit represented in Figure 1 can be split into three distinct functional topologies: a **three-phase rectifier**, a **brake chopper**, and a three-phase two-level (voltage-source) inverter (**Sixpack**).



*Figure 2: Three-phase rectifier Figure 3: Brake chopper* 

Figure 4: Sixpack



When calculating their electrical efficiency and losses, these functional topologies can be handled individually with proper excitations. The results for each functional topology can then be used to calculate the efficiency of the power module as a whole.

The primary advantage of functional topology modeling is that it significantly reduces the time required to prepare simulations by relying on reusable building blocks. These can be used in any combination to reconstruct a power module's electrical circuit, simply requiring the addition of connection wires between the functional blocks. Moreover, model selection depends on the functional topologies and not on the individual transistors or diodes, reducing simulation preparation time.



*Figure 5: Reconstructing the power module circuit by adding connection wires between individual functional topologies.* 



## 4 A PLECS Library of Functional Topologies

Vincotech has assembled a PLECS library of individual functional topologies that customers can use to reconstruct power modules. These functional topologies are also available in VINcoSIM. Because VINcoSIM also relies on these individual functional topologies when performing calculations, users can be assured that PLECS and VINcoSIM are compatible in terms of their simulation results.

VINCOTECN VINCOSIM				
PRODUCT SELECTION	flowPIM S3	B0-SP12PMA100M7-LQ99A78T_4701	1200 V; 100 A	^
flowPIM S3 / 1200 V; 100 A				
Part number: BØ-SP12PMA100M7-LQ99A76	T_4701			
Break down voltage: 1200 V			<u> </u>	
Nominal chip current: 100 A			-약-약-약	
Topology: PIM (CIB)				× 1
Chip technology [main switch]: IGBT M7			- 14:14:14	
Base isolation [e.g. ceramic]: Al203				
Electrical interconnection: Press-fit pin				
Module housing: flow S3 12 mm 4 towers				
Thermal interface: PTM6000 (Apaste = 4.4 V	V/mK]			
HEATSINK AND ENVIRONMENT SETU	JP Fixed heatsi	nk/temperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETL	JP Fixed heatsi	nivtemperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETL	JP Fixed heatsi	nivtemperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsh	nktemperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsh	nktemperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETL SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsh	nkkemperature: 80 °C		~ ^
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsi	nktemperature: 80 °C		~ ^
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP	JP Fixed heatsi	not temperature: 80 °C		~ ^
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP	JP Fixed heatsi	not temperature: 80 °C		
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP	JP Fixed heatsi	nktemperature: 80 °C		
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP	JP Fixed heatsi	nktemperature: 80 °C		
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsi	nktemperature: 80 °C		
HEATSINK AND ENVIRONMENT SETU SIMULATION SETUP To run a simulation please provide operation All fields are required.	JP Fixed heatsi	nkkemperature: 80 °C trol parameters for all unctional elements.	JA JA J JA JA JA J JA JA JA J Typical Sixpack model	



Functional topology-based modeling has its limitations, particularly when functional topologies are split between multiple power module packages. While, in such a scenario, the functional topologies successfully reproduce the electrical behavior, they fail to capture the thermal properties.



In the case of split functional topologies, it is, therefore, advisable

to build the topology using individual switch and diode circuit elements and manually draw all the required connections. Furthermore, the thermal description of each semiconductor must be created manually based on the module datasheet.

The Vincotech PLECS topology library can be downloaded from the Vincotech website and is available in the VINcoSIM simulator. A detailed guide about where to find it and how to install it into PLECS is provided in a later chapter.

# 5 Modeling Semiconductor Characteristics

The characteristics of the semiconductors used in the power module must be considered to perform a representative electrothermal simulation. These characteristics describe the electrical and thermal behavior of each semiconductor within the specific power module package.

There are three types of semiconductor characteristics for power modules:

- Static characteristics, which define conduction losses.
- Dynamic characteristics, which define switching losses.
- Thermal characteristics, which define the thermal connectivity (thermal chain) between the semiconductor and the power module package case.



Figure 7: Static characteristics Figure 8: Dynamic characteristics Figure 9: Thermal characteristics



Vincotech

Static and dynamic characteristics depend on the temperature; additionally, dynamic characteristics depend on the gate resistance, R<sub>g</sub>.

Each Vincotech power module's static, dynamic, and thermal characteristics are available in the Vincotech product datasheets and the VINcoSIM web-based simulation tool.

PLECS uses the same characteristics to simulate losses and temperatures. These can be defined in PLECS as thermal descriptions, either by manually entering datasheet values or by referring to a thermal description .xml file.

The thermal descriptor .xml file provides a much easier and faster approach than manual definition. In addition to the datasheet, Vincotech provides the characteristics in .xml format, which can be downloaded via the VINcoSIM simulator.

When no thermal descriptors are provided, PLECS simulates ideal components without considering electrical losses and thermal conductivity.

PLECS requires a thermal descriptor to simulate real conditions, including thermal conduction and switching losses. Once provided, the thermal network is excited and the simulation engine calculates the temperature increase caused by losses on the semiconductors.

The same method is relied on to simulate the power module as a whole. The only difference is that, in this case, the thermal package comprises more than one semiconductor.



*Figure 10: The calculation loop used by PLECS to determine the electrothermal behavior of a power module with an IGBT and a diode.* 



#### Vincotech

The simulation is left to run until the temperature swing reaches a steady state, reflecting the temperatures at continuous operation under specified application and simulation parameters. Achieving VINcoSIM comparable results in PLECS requires to perform a steady-state analysis.

# 6 Relationship Between the Topology Model and the Semiconductor Characteristics

As described in the previous chapter, modeling a power module in PLECS requires an electrical circuit or a topology model and semiconductor characteristics. Vincotech provides topology models as functional topologies and publishes semiconductor characteristics in a PLECS-compatible thermal descriptor .xml file format.

To run an electrothermal simulation in PLECS, the topology and characteristics must be commissioned before starting the simulation. This involves assigning the correct thermal descriptor to the corresponding functional topology.

**IMPORTANT:** Vincotech thermal descriptors in .xml files are related to specific functional topologies. They are only fully operational when applied to the relevant Vincotech functional topology and cannot be loaded onto other functional topologies or circuit elements.



# 7 How to import VINcoSIM models to PLECS

Vincotech power modules can be simulated in PLECS in just five simple steps.

1. Download the Vincotech library (Guide: steps 1 – 7)

Download the library here: <u>https://www.vincotech.com/support-and-</u> <u>documents/simulation-software.html</u>

N L	ibrary Brow	/ser	-	×
File	Window	Help		
Q Se	arch compor	ients		
> Syst	em			
> Asse	ertions			
> Con	trol			
> Elec	trical			
> The	rmal			
> Mag	Inetic			
> Med	hanical			
> PLEC	CS RT Box			
> STM	132 Target			
> 11 C	2000 Target			
~ Vinc	cotech Libra	ry		
/ 2-	Level			
/ 5-	Level			



2. Add the relevant functional topologies from the Vincotech Library.

#### (Guide: steps 8-10)



3. Reconstruct the power-module-specific circuit. (Guide: steps 11-12)





4. Download the thermal .xml file from VINcoSIM (Guide: steps 13-14)

Vincotech VINcoSIM		FEEDBACK	ABOUT VINCOSIN		
PRODUCT SELECTION flowPIM S3	B8-SP12PMA100N7-LQ99A78T_4701	1288 V; 188 A	^		
RowField SJ / 1269 V, 188 A Part number: 88: 6712794313807 - 10265078, 4011 Band down vyttager 1209 V Rowfield (Bandward 184 A Partieller) (Part 184 A Days task-tasking (parts antich), 1631 N7 Bank induktor, (Part antich), 1631 N7 Bandrah konting, film 1512 N7 N Hamm Rowfield Manufart, (Part 1512 N7 N Hamm Rowfield Rowfield (Parts - 14, 941) Rowfield Manufart, (Part 1512 N7 N + 14, 941)	<	499 499 499 499 60	>	Characteristics and models T1710AL SXFACE HOBEL Bitatic Characteristics Inverter Switch Inverter Dode Dynamic Characteristics	×
R WITHIN SHARVER I C CHART AND IN THE MEMORY HEAVER	nk temporature: 88 °C		•	Inverter La-Side Inverter Hi-Side Download Data	а за за а 1 2 3 4 6 7 у иютру()
SIMULATION SETUP To run a simulation planse provide operation parameters and co All fields are required.	ntrol parameters for all functional elements.			PLECS Thermal Model     Vinostech PLECS Library	V. 1288 V Types 273.8 °C Denne 2003 8 °C Denne 2003 8 °C 1273 V Denne Doustwinks -0 - 68 - 0 - 885 - 0 - 682 - 0 - 885 - 0 - 81 -0 - 92 - 0 - 85
Typical Sigh theodore and the	af Data meta	Hand Report of the second			biological and biolog

5. Refresh the PLECS thermal library and assign the thermal .xml files to each functional topology **(Guide: steps 15)** 

■ untilled * File Edit View Simulation Format Coder Window Hele	- 🗆 X
Heat Sink Heat S	
hid Imposture:	International Security Control     Sepect *     Bit Security Control     Sepect model       By reference     Editation     Editation     Editation       Remote     Remote     Editation     Editation       New thermal package description     Editation     Editation       Hebit     Editation     Editation



Vincotech The following chapter provides a step-by-step tutorial that guides users through the entire process of running simulations with Vincotech power modules in PLECS, starting from scratch.

## 8 Step-by-step User Guide on Using Vincotech PLECS Models

The following paragraphs outline the steps required to assign models in PLECS using Vincotech topology models and .xml thermal descriptors. They explain in detail how to download models and install them in PLECS so that they are ready for use by PLECS end users.

# 8.1 Vincotech PLECS Library

The Vincotech library includes several functional topology models, which are the basic building blocks needed to model power modules. First, the library must be downloaded and installed in PLECS. It can be downloaded from the VINcoSIM webpage as a compressed zip folder.

#### Link to the webpage:

https://www.vincotech.com/support-and-documents/simulation-software.html





Vincotech This webpage can be reached directly from www.vincotech.com by accessing the Support & Documents menu and selecting Simulation Software VINcoSIM.



This page can also be accessed from VINcoSIM by clicking on the **Characteristics & Models** menu and selecting **Vincotech PLECS Library**.

Vincotech VINCOSIM	Characteristics and models	
SIMULATION SETUP	TYPICAL SIXPACK MODEL Static Characteristics	Static Characteristics $V_{DE} = 15 V$ $- T_{J^{e}} 25.8  {}^{e} C - T_{J^{e}} 125.8  {}^{e} C - T_{J^{e}} 158.8  {}^{e} C$
To run a simulation please provide operation param	Inverter Switch	358
Il fields are required.	Inverter Diode	388
	Dynamic Characteristics	258
	i Inverter Lo-Side	158
	Inverter Hi-Side	188
	Download Data	58
	Comma Separated Values	Ø 1 2 3 4 5 6 Vottage [V]
Typical 3ph Rectifier model	Typical Brake model	V: 1200 V Inom: 100 A
LM CHARACTERISTICS AND MODELS	M CHARACTERISTICS AND MODELS	Tjmax: 175.0 °C V <sub>DEsat 89125.8 °C</sub> : 1.71 V
		Thermal Characteristics
i Operation mode RECTIFIER	i Operation mode PFC RESISTIVE OFF	- D= 8.8 - D= 8.885 - D= 8.81 - D= 8.82 - D= 8.85 -
Parameters Load	Parameters Control	— D= 0.2 — D= 0.5
Vph(RMS) fin	Vin(RMS) lin(RMS)/Log	10+1
230 V 50 Hz	230 V 100 A	10+8
	Vout. fin	10-1
	800 V 50 Hz	IM2 10.2
		Zth
		10-3



#### Step 1:

On the Simulation Software VINcoSIM webpage, select Download Vincotech PLECS Library to download the library .zip file.

	PRODUCTS	SUPPORT & DOCUMENTS	TECHNOLOGY & INNOVA	TION COMPAN	Y NEWS CAR
HOME / SUPPORT 6 DOCU	MENTS / SIMULATION SOFTW	ARE VINCONIN			
VINcoSIM -	The Integrat	ted Simulation	Environment		
VINCOSIM is a web- to simulate junction power modules. The conditions as voltan give a good fitting applications. All pro- based on real mean Why use VI	based integrated sin on temperatures and he software is design ages, currents and sy to the operation of t ower loss and tempe isurements taken of <b>NCOSIM?</b>	nulation environment I loss for Vincotech ed to choose operation vitching frequencies to he power module in real rature calculations are each module.	Vincetech Simulation Erro		VINCoSIM
🗸 II	is accurate	🗸 It is fast :	and complete	🗸 Iti	s easy to use
We use real me topologies are option	easurement data, all available, heatsink is included.	The procedure ta and the results rev one gl	kes only seconds eal a lot of data in ance.	Only 1 configura flexible option downloading r	ation step is needed, s for your needs, no necessary, no cost.
Unique simulat	ion core				
VINcoSIM is based on a even increase the num	an unique simulation corr nber of simulation metho	e which offers the possibility t ds was never easier.	o simulate every module v	vith every topology. A	Adding new modules and
	background:				



#### Step 2:

Extract the **Vincotech\_Library.zip** file to a designated folder. Choose the folder intentionally, as it will need to be linked to PLECS. Later, the thermal descriptor .xml files will also need to be copied into this folder structure.

↑      ◆ > This PC > Downloads					
Name		Date modified	Туре	Size	
v Today (1)					
Vincotech_Library.zip		2024-02-20 09:33	Compressed (zipp	717 KB	
					×
	🔶 🔋 Extract Cor	mpressed (Zipped) Folders			Â
	Select a De	stination and Extract	Files		
	Files will be ex	tracted to this folder: <u>\Vincote</u>	ch_Library		Browse
	Show extra	cted files when complete			
					_
					_
				Ext	ract Cancel



#### Step 3:

After creating the folder and extracting the library, install it into PLECS. The installation steps are also described in the **install.txt** text file in the extracted folder structure.

	Vincotech_Library > plecs > Library >					
Name	Date modified	Туре	Size			
Bocumentation	2024-02-20-09:34	File folder				
📙 lua	2024-02-20 09:34	File folder				
VincotechThermalDescriptions	2024-02-20 09:34	File folder				
DS_Store	2024-01-19 12:40	DS_STORE File	11 KB			
info.xml	2023-08-17 15:23	XML Document	1 KB			
📄 Install.txt	2024-02-20 09:31	Text Document	1 KB			
C vincotech.svg	2023-11-22 10:37	Microsoft Edge H	2 KB			
VincotechComponentLibrary.plecs	2024-01-19 12:26	PLECS model file	786 KB			
VincotechComponentLibrary.plecs.autos	2023-12-07 17:04	AUTOSAVE File	1,027 KB			

#### Step 4:

To install the library, open the **PLECS Library Browser** and select **PLECS Preferences** from the **File** menu.

۲ ا	library Browser	_		Х
File	Window Help			
	New Model	Ctrl+N		L
	Open	Ctrl+0		
	Open Recent		•	
	Import from Blockset			
	PLECS Preferences			
	Compare	Ctrl+Sh	ift+C	
	PLECS Extensions			
	Quit PLECS			
	Quit FLECS			J



#### Step 5:

At the PLECS preferences window, select the **Thermal** tab and add the extracted library by clicking the **+** button.

PLECS P	references					
General	Libraries	Thermal	Scope Colors	Updates	Coder	
Thermal de	escription sea	rch path:				
<b>(+)</b>						
-						
Ť						
$\mathbf{+}$						
Q						

#### Step 6:

In the Select Directory window, browse to the directory where you extracted the .zip file, navigate to the **Vincotech\_Library\plecs\Library** folder, and click the **Select Folder** button.





**Step 7.** Restart the PLECS application. If you executed all steps correctly, the Vincotech Library will appear in the PLECS Library Browser, displaying the 2-Level and 3-Level, topology sections.



The Vincotech Topology Library in PLECS is ready to use. Select the desired functional topology from the library browser to add it to your simulation setup.





Vincotech

# 8.2 Simulate Vincotech Power Modules in PLECS

Once you have installed the Vincotech PLECS library, you can simulate a specific Vincotech power module. The following steps are required to set up the simulation correctly:

- 1. Add the functional topologies from the Vincotech library.
- 2. Reconstruct the power module specific circuit by adding connection wires between the functional topologies.
- 3. Download the thermal descriptor .xml files for the functional topologies from VINcoSIM.
- 4. Assign the thermal descriptions to the corresponding functional topologies.

The following example guides you through these steps to help you correctly set up your PLECS simulation.

#### Step 8:

Find the product you want to simulate in PLECS on the Vincotech website under the Products menu. Use the built-in filters to find your desired product easier.

Select the product from the list view and open VINcoSIM by clicking **Simulate**.

¥2 2-P544-A28-PM										*10.27	
Vincotech	PRODU Series	CIB-DE-NTC	UPPORT & DI	DCUMENTS 1200	s т 15	ECHNOLOGY E	HINNOVATION	12 CC	MPANY	NEWS	
PART-NO DETAILS / PART-NO DATASHEET	PRODUCT STATUS	\$ SUB- Topology	PRODUCT LINE	VOLTAGE IN V	¢ CURRENT IN A	MAIN CHIP TECHNOLOGY	+ HOUSING FAMILY	HEIGHT IN MM	ISOLATION	SIMULATION	
10-E212PMA015M7- L186A78Z	Series	CIB-OE-NTC	flowPIM® E2	1200	15	IGBT M7	flow E2 🧿	12	AL203	SIMULATE	☆
<u>V23990-P544-A27Y-</u> PM	Series	CIB-OE-NTC	flowPIM® 0	600	15	IGBT3	flow 0 😐	17	AL <sub>2</sub> O <sub>3</sub>	SIMULATE	*
<u>V23990-P545-A28-PM</u>	Series	CIB-DE-NTC	flowPIM® 0	600	20	IGBT3	flow 0 🧿	12	Al <sub>2</sub> 0 <sub>3</sub>	SIMULATE	☆
80-SP12PMA100M7- LQ99A78T	Samples available	CIB-OE-NTC	flowPIM® S3	1200	100	IGBT M7	flow S3	12	AL203	SIMULATE	*
<u>V23990-P649-A59Y-</u> PM 🔁	Series	CIB-OE-NTC	flowPIM® 0	1200	8	IGBT4	flow 0 👁	17	AL203	SIMULATE	☆
80- M212PMA035M731- K220A72	Series	CIB-KE- Tandem Diode-NTC	MiniSKiiP® PIM 2	1200	35	IGBT M7	MiniSKiiP® 2 🧿	16	AL203	SIMULATE	*
80-M312PMA100M7- K420A70	Series	CIB-KE-NTC	MiniSKiiP® PIM 3	1200	100	IGBT M7	MiniSKiiP® 3 🤨	16	AL203	SIMULATE	☆
<u>V23990-P848-C58-PM</u>	Series	CI-DE-NTC	flowPIM® 0	1200	4	IGBT4	flow 0 😐	12	Al <sub>2</sub> O <sub>3</sub>	SIMULATE	*
<u>V23990-P589-A41-PM</u>	Series	CIB-OE-KE- NTC 💿	flowPIM® 1	1200	25	IGBT4	flow 1 😶	17	Al <sub>2</sub> 03	SIMULATE	☆



Vincotech Alternatively, launch VINcoSIM for the selected product by clicking the **Simulate this module** button at the bottom of the product webpage.

REQUEST A QUOTE		PUT ON WISHLIS	ST 📩
Basic module informati	ion		
Part number:	B0-SP12PMA100M7-LQ99A78T		
Product line:	flowPIM® S3	with a	
Product status:	Samples available	the second se	
Break down voltage:	1200 V	and the the state of the	
Nominal chip current rating:	100 A	· here is a stranger	
Standard Packing Quantity:	45	· r	
Module datasheet:	D PDF, S MB		
Product details			Q
Topology:	PIM (CIB)		
/ Open Emitter configuration			
/ Temperature sensor			
/ Converter+Brake+Inverter			
Chip technology (main switch):	IGBT M7		
/ Easy paralleling			
/ Low turn-off losses			
/ Low collector emitter saturation	n voltage		Q
/ Positive temperature coefficient	t		
/ Short tail current			
/ Switching optimized for EMC			
Base isolation (e.g. ceramic):	Al <sub>2</sub> O <sub>3</sub>		
Electrical interconnection:	Press-fit pin		





#### Step 9:

The product related VINcoSIM webpage opens. Next, add the functional topologies that are used in the product. The VINcoSIM webpage structure will help you identify the relevant functional topologies.

			FEEDBACK	ABOUT
PRODUCT SELECTION	flowPIM S3	B0-SP12PMA100M7-LQ99A78T_4701	1200 V; 100 A	^
flowPIM S3 / 1200 V; 100 A Part number: 80-SP12PMA100M7-LQ99A78T_ Break down voltage: 1200 V Nominal chip current: 100 A Topology: PIM [CIB] Chip technology [main switch]: IGBT M7 Base isolation [e.g. ceramic]: Al2O3 Electrical interconnection: Press-fit pin Module housing: flow S3 12 mm 4 towers Thormal interface: PIM6000 [2,paste = 4.4 W/r	4701 mK)			>
R MODULE DATASHEET 🛛 🖓 CHOOSE ANOTHER	PRODUCT			
HEATSINK AND ENVIRONMENT SETUP	Fixed heats	ık temperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETUP	Fixed heats	ik temperature: 80 °C		~
HEATSINK AND ENVIRONMENT SETUP SIMULATION SETUP To run a simulation please provide operation p All fields are required.	Fixed heatsin	ik temperature: 80 °C trol parameters for al functional elements.		



#### Step 10:

Use the library browser in PLECS and the Vincotech library elements to add the desired functional topologies to the simulation page.





#### Step 11:

After placing the functional topologies, it is important to reconstruct the module-specific circuit by adding wire connections between the functional topologies.

Vincotech VINCOSIM			FEEDBACK	ABOUT VINCOSIM
PRODUCT SELECTION	flowPIM S3 B0-SP12PN	1A100M7-LQ99A78T_4701	1200 V; 100 A	^
flowPIM S3 / 1200 V; 100 A Part number: B0-SP12PMA100M7-LQ99A78T_47 Break down voltage: 1200 V Nominal chip current: 100 A Topology: PIM (CIB) Chip technology [main switch]: IGBT M7 Base isolation [e.g. ceramic]: Al203 Electrical interconnection: Press-fit pin Module housing: flow S3 12 mm 4 towers Thermal interface: PTM6000 [\apate = 4.4 W/mK	01 С			
₹	Brake		- KA - KA - KA Sixpack	



#### Step 12:

The final step before assigning the thermal model is to add a shared heatsink to the circuit and connect it to a grounded thermal source. This heatsink, which could be the power module backside copper or the baseplate, integrates the circuit into a thermally equivalent package.



This makes the simulation model setup ready for the next step: assigning a thermal description to each functional topology.



#### Step 13:

To assign a thermal description to each functional topology block, download the relevant .xml files from VINcoSIM. In VINcoSIM, click on **Characteristics & Models**, located below the related functional topology. In the sidebar that opens, click on **PLECS Thermal Model** to download the .xml file for this product and functional topology. A green bar in VINcoSIM will indicate that the download is complete. If an error occurs during the file export, information about the causes will be provided in an error message.

Part number: B8-SP12PMA100M7-L099A78T_4701		Characteristics and models	×
Brask down voltage: 1288 V Kominati obje purnet: 189 A Topology: PM (CB) Dip technology (Pmin switch): ISBT M7 Base isolation (e.g. ceramic): A1003 Electrical interconnection: Press-Ift pin Module havising (mod 312 pm m1 tewers Thermal interface: PTMSEB0 (Jpuste = 0.4 W(mK))		1199EAL SIXPACK MODEL Beats Due Static Dharacteristics Inverter Switch 200 Inverter Switch 200 Dynamic Dharacteristics 2 200	natemates V <sub>00</sub> = 15 V
E NOOLE DAIASHEET 🕑 CHOOSE ANOTHER PRODUCT	1.1	Inverter Lo-Side	
		Download Data	
HEATSINK AND ENVIRONMENT SETUP Fixed heatsink temperature	:: 80 °C	Comma Separated Values	1 2 3 4 5 6 7 Votrage [V]
		PLECS Thermat Model V: 1288 V	I <sub>nom</sub> ; 188 A
SIMULATION SETUP	^	Vincotech PLECS Library Tyrae: 175.8	*C V <sub>CEnt</sub> @125.8*C 1.71 V
	for all functional elements	- D-88	- D= 8.885 - D= 8.81 - D= 8.82 - D= 8.85 - D= 8.1
To run a simulation please provide operation parameters and control parameters			
To run a simulation please provide operation parameters and control parameters All fields are required.			- 0+ 8.2 - 0+ 8.5

Repeat these steps to download the .xml files for all functional topologies for the selected power module. In the end, the number of .xml files should equal the number of functional topologies in the product.



Vincotech The .xml filenames include the **product name** (e.g., B0-SP12PMA100M7-LQ99A78T) and the designation of the relevant **functional topology** (e.g., Typical Sixpack).

to Quick	Copy Paste	Move Copy to v to v	New item •	Properties	Select all Select none	
	Clipboard	Organize	New	Open	Select	
$\rightarrow$ $^{\prime}$	↑ 🕹 > This PC > Download	s	v Ö 🗸	earch Downloads		
	Name Today (4)	^	Date modified	Туре	Size	
	Vincotech Library		2024-02-20 09:34	File folder		
	B0-SP12PMA100M7-LQ99A78T	_4701_1_Typical 3ph Rectifier model.	cml 2024-02-20 10:04	XML Document	55 KB	
4	B0-SP12PMA100M7-LQ99A78T	4701_2_Typical Brake model.xml	2024-02-20 10:04	XML Document	23 KB	
	B0-SP12PMA100M7-LO99A78T	4701 3 Typical Sixpack model xml	2024-02-20 10:06	XML Document	134 KB	

#### Step 14:

Copy these .xml files to the correct subdirectory in the Vincotech PLECS library folder. To do so, navigate to the folder where you unzipped the Vincotech PLECS library.

The topology folders are in the following folder structure:

#### VincotechLibrary\plecs\Library\VincotechThermalDescriptions\Vincotech

Copy the downloaded .xml files to the right folder, referring to the .xml filenames.

For example, copy the *B0-SP12PMA100M7-LQ99A78T\_4701\_3\_Typical* **Sixpack** *model.xml* file to the **Sixpack** folder.

Copy Paste Shortcut	Copy b to -	New item •	Properties	Select all Select none	Pin to Quick access	Copy Paste	Cut Copy path Paste shortcut	Move Cop to* to	py Delete Rename	New item •	Properties     Protection     Properties     Properties     Properties     Properties     Properties     Protection	Select all	
Clipboard	Organize	New New	Open a	Select		Clipboard			Organize	New	Open	Select	
The subconclustery is prec	s > clorary > vincotech inem	naidescriptions > vinco	uecn V O	jo search vincotech		Τ • Ι	nis PC > Download	as		v 0 2	search Downloads		
lame	Date modified	Type	Size		<b>_</b> ^	Name				Date modified	Type	Size	
2phRectifier	2024-02-20 10:07	File folder			1 I N	Today (4)							
3phRectifier	2024-02-20 10:06	File folder				Vincotech	Library			2024-02-20 09:34	File folder		
ANPC	2024-02-20 10:07	File folder			-	B0-SP12PM	A100M7-LQ99A781	T_4701_1_Typic	al 3ph Rectifier model.x	ml 2024-02-20 10:04	XML Document	55 KB	
ANPEC	2024-02-20 10:07	File folder			-	BO-SP12PM	A100M7-LO99A781	T 4701 2 Typic	al Brake model.xml	2024-02-20 10:04	XML Document	23 KB	
Boost	2023-11-17 21:33	File folder			1	B0-SP12PM	A100M7-LO99A781	T 4701 3 Typic	al Sixpack model.xml	2024-02-20 10:06	XML Document	134 KB	
Brake ┥	2024-02-20 10:14	File folder				-							
CSPFC	2024-02-20 10:07	File folder											
FC Boost	2023-11-30 12:40	File folder			1								
FC Buck	2023-11-30 12:38	File folder											
FC-3L	2024-02-20 10:07	File folder	/										
FC-4L	2024-02-20 10:07	File folder			1 1								
H6.5	2023-11-30 10:54	File folder			-								
Half-Bridge	2024-02-20 10:07	File folder											
Halfe-Bridge	2024-02-20 10:07	File folder			-								
H-Bridge	2024-02-20 10:07	File folder			1								
MNPC	2024 02-20 10:07	File folder											
NPC	2024-02-20 10:07	File folder											
NPFC	2024-02-20 10:07	File folder			1								
PassiveRectifier	2024-02-20 10:07	File folder											
Sixpack	2024-02-20 10:07	File folder											
SPEC	2024-02-20 10:07	File folder			-								
Switch	2024-02-20 10:07	File folder											
Sym Boost	2023-11-30 12:57	File folder			-								
Svm Buck-Boost	2023-11-30 12:57	File folder											
ViennaRectifier	2024-02-20 10:07	File folder											
					9								
					<u> </u>								



PLECS Preferences	×
General Libraries Thermal Scope Colors Updates Coder	
Thermal description search path:	
+ C:// /Vincotech_Librar	ry/plecs/Library
*	
ğ	
	OK Cancel Apply Help

**Step 15.** Click the Refresh button on the Thermal tab in PLECS Preferences to see changes in the library.

#### Step 16:

To assign a thermal model to a Vincotech functional topology, double-click on that and then select the appropriate thermal model by clicking on **From library**.





Follow these steps for all functional topologies in the order presented. If a folder contains multiple models, they will also be available in the list. After selecting the model, define additional parameters such as  $R_{gon}$  and  $R_{goff}$  in the dialog window.



Be sure to carefully select the models related to the same product if the power module package comprises more than one functional topology.



After this step, the power module model set-up is complete.



#### Step 17:

Before running a simulation with the model, initialize the simulation parameters. To do so, open this dialog in PLECS by entering the **Simulation** menu and selecting **Simulation parameters**.



In the pop-up dialog, select the initialization tab and set up your simulation initialization and model parameters.

Solver	Options	Diagnostics	Initialization	1					
System	state								
Initializ	e from: ()	Block parameter	2						
		energy parameter							
		Stored system s	tate					Store curre	ent state
Model i	nitialization	commands							
1	& Parar	maters							^
2	Vdc = 8	800:							
3	Vph rms	s = 230:							
4	cosphi	=0.8;							
5	Iph rms	s = 10;							
6	fout =	50;							
7	fsw = S	5e3;							
8	t init	= 80;							
9	IL ripp	ple = 20;							
10	Vph_pea	ak=Vph_rms'	sqrt(2);						
11	Lout =	Vdc/(4*IL	ripple/1	00*Iph_rms*f	(sw);				
12	Cout =	1/(2*pi*fs	sw) ^2*Lou	t;					
13	Phi=aco	os(cosphi);							
14	Phi_deq	g={-Phi*180	<pre>/pi};</pre>						
15	Sout=Vp	ph_rms*Iph_	rms;						
16	Pout=So	out*cosphi;							
17	Qout=so	grt ( (Sout^2	2) - (Pout^	2));					
18	XL=2*pi	i*fout*Lout	.,						
19	theta=a	atan (Pout/	(Qout+Vph	_rms^2/XL));					
20	theta_	leg=theta*1	L80/pi;						
21	Vinv=Po	out*XL/ (Vpr	_rms*sin	(theta));					
22	M1=(Vp)	n_rms*sqrt	(2) + (Vdc/	2))/Vdc;					
23	vinv_ir	hit=vinv*so	Irt (2) * 51	n(-tneta);					
24	IL init	t_1=-sqrt(2	() * 1pn_rm	s*sin(-Pni)-	((vinv_ir	11t/2/Lou	()/ISW);	16	
25	IL_init	2=-(sin(p	01-2/3-Ph	1) * Sqrt (2) * 1	pn_rms)-(	(Vinv_in	it/2/Lout)	/ISW);	
20	TP_TUT	(SIN()	01-4/3-FH	1)-Sqrt(2)-1	ipn_ims)-(	(vinv_in	IIC/2/1000C)	/15W);	
20	& Conto	101							
20	Prop =	20.							
30	Rgoff =	= 20:							
31	tDead =	= 100e-9:							
32									

With this step, you are ready to run your simulation.



#### Step 18:

To simulate the losses, efficiency, and temperature of a power module, run a steady-state analysis. For this, open the **Simulation** menu and select **Analysis tools**.

Sixpack *		_	$\times$
File Edit View Simulation Format Coder	Window Help		
Start Pause Simulation parameters	Ctrl+T Space Ctrl+E		
Analysis tools	Ctrl+Shift+Y		
Simulation scripts	Ctrl+Shift+T		Scope
Heat Sink			

In the pop-up dialog window, define the steady-state analysis parameters. When you are ready, click **Start analysis** to run the simulation.

Analyses	Analysis type: Steady-Stat	e Analysis			
steady-State Analysis iteady-State Analysis	Description: Steady-Sta	ite Analysis			
	Setup Options				
	Operating point:	periodic	~		
	System period (s):	1/fout			
	Simulation start time (s):	: 0			
	Show final cycles:	4			



When the simulation is complete, the results can be displayed using standard PLECS tools, including the probe, scope, or switch loss calculator functions. The electrothermal behavior of the Vincotech power module is represented in the simulation results, e.g., in the temperature swing on the semiconductors displayed below.





## 9 Summary

This tutorial offers step-by-step instructions on how to simulate Vincotech power modules in a PLECS environment.

PLECS software is supported by Plexim. Trial license can be requested on the following link: <u>https://www.plexim.com/trial</u>

The Vincotech library and VINcoSIM generated .xml files are supported by Vincotech from PLECS version 4.8.1 or higher.

If you identify a problem with the Vincotech Library or a VINcoSIM .xml file, please get in touch with your local Vincotech sales support representative.

If you encounter problems with the PLECS software (e.g., convergence, probing difficulties, etc.), please reach out to your dedicated PLEXIM support.

# Vincotech GmbH

Biberger Strasse 93 82008 Unterhaching / Germany

T +49 89 878 067-0 / F +49 89 878 067-300 info@vincotech.com / www.vincotech.com

Copyright © 2024 / Vincotech GmbH