



Vincotech

10-E112PNB015M702-PS08C76Z

target datasheet

flowPIM E1

1200 V / 15 A

Topology features

- Converter+Inverter
- Open Emitter configuration
- Tandem diode
- Temperature sensor

Component features

- Easy paralleling
- Low turn-off losses
- Low collector emitter saturation voltage
- Positive temperature coefficient
- Short tail current
- Switching optimized for EMC

Housing features

- Base isolation: Al_2O_3
- Convex shaped substrate for superior thermal contact
- Compact housing
- CTI600 housing material
- Thermo-mechanical push-and-pull force relief
- Solder pin

Target applications

- Embedded Drives
- HVAC
- Industrial Drives

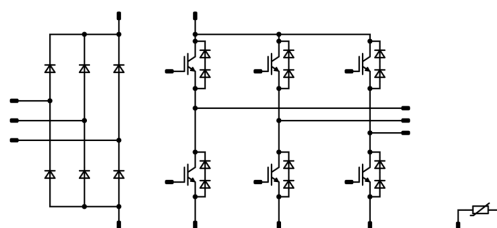
Types

- 10-E112PNB015M702-PS08C76Z

flow E1 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	26	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	30	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	77	W
Gate-emitter voltage	V_{GES}		±20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	9,5	µs
Maximum junction temperature	T_{jmax}		175	°C

Inverter Diode

Peak repetitive reverse voltage	V_{RRM}		1300	V
Forward current (DC current) ⁽²⁾	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	19	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	45	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	61	W
Maximum junction temperature	T_{jmax}		175	°C

⁽¹⁾Calculation based on chip supplier datasheet at $T_j=175\text{ °C}$

Rectifier Diode

Peak repetitive reverse voltage	V_{RRM}		1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	50	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 150\text{ °C}$	270	A
Surge current capability	I^2t		365	A²s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	61	W
Maximum junction temperature	T_{jmax}		150	°C



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Parameter	Symbol	Conditions	Value	Unit
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Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+(T_{jmax} - 25)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage $t_p = 2\text{ s}$	6000	V
Creepage distance			>12,7	mm
Clearance			>12,7	mm
Comparative Tracking Index	CTI		≥ 600	



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0015	25	5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CEsat}		15		15	25 150		1,65 1,95	2,1	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			60	µA
Gate-emitter leakage current	I_{GES}		20	0		25			200	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	0	10	25				2900		pF
Output capacitance	C_{oes}							120		pF
Reverse transfer capacitance	C_{res}							34		pF
Gate charge	Q_g	$V_{CC} = 600$ V	0/15		15	25		110		nC

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,23		K/W
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Inverter Diode

Static

Forward voltage	V_F				15	25		2,9	3,84	V
Reverse leakage current	I_R	$V_r = 1300$ V				25			0,94	µA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,57		K/W
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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Rectifier Diode

Static

Forward voltage	V_F				28	25		1	1,5	V
Reverse leakage current	I_R	$V_r = 1600$ V				25 150			50 1000	μA

Thermal

Thermal resistance junction to sink	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,14		K/W
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Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R100	$\Delta_{R/R}$	$R_{100} = 499$ Ω				100	3,2		3,3	%
Power dissipation	P					25		130		mW
Power dissipation constant	d					25		1,3		mW/K
B-value	$B_{(25/50)}$	Tol. ± 1 %						3380		K
Vincotech Thermistor Reference									V	



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Ordering Code	
Version	Ordering Code
Without thermal paste	10-E112PNB015M702-PS08C76Z
With thermal paste (5,2 W/mK, PTM6000HV)	10-E112PNB015M702-PS08C76Z-/7/

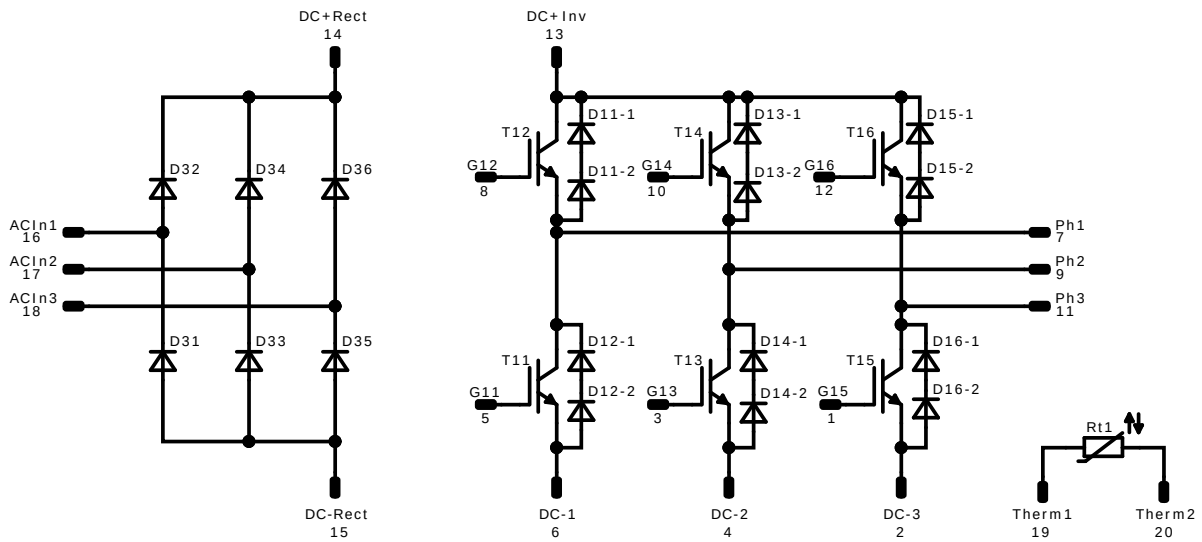
Marking						
	Text	Name	Date code	UL & VIN	Lot	Serial
		NN-NNNNNNNNNNNNNNNN- TTTTIV	WWYY	UL VIN	LLLL	SSSS
	Datamatrix	Type&Ver TTTTTIV	Lot number LLLLL	Serial SSSS	Date code WWYY	

Outline				
Pin table [mm]				Tolerance of positions: ±0.4mm at the end of pins. Dimension of coordinate axis is only offset without tolerance
Pin	X	Y	Function	
1	16	0	G15	
2	12,8	0	DC-3	
3	9,6	0	G13	
4	6,4	0	DC-2	
5	3,2	0	G11	
6	0	0	DC-1	
7	0	25,6	Ph1	
8	3,2	25,6	G12	
9	9,6	25,6	Ph2	
10	12,8	25,6	G14	
11	19,2	25,6	Ph3	
12	22,4	25,6	G16	
13	28,8	25,6	DC+Inv	
14	32	25,6	DC+Rect	
15	28,8	19,2	DC-Rect	
16	25,6	12,8	ACIn1	
17	25,6	6,4	ACIn2	
18	32	0	ACIn3	
19	16	19,2	Therm1	
20	16	16	Therm2	



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Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	15 A	Inverter Switch	
D11-1, D11-2, D12-1, D12-2, D13-1, D13-2, D14-1, D14-2, D15-1, D15-2, D16-1, D16-2	FWD	1300 V	15 A	Inverter Diode	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	28 A	Rectifier Diode	
Rt	Thermistor			Thermistor	



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Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample
Handling instruction				
Handling instructions for <i>flow</i> E1 packages see vincotech.com website.				
Package data				
Package data for <i>flow</i> E1 packages see vincotech.com website.				
Vincotech thermistor reference				
See Vincotech thermistor reference table at vincotech.com website.				
UL recognition and file number				
This device is UL 1557 recognized under E192116 up to a junction temperature under switching condition $T_{j,op}=175^{\circ}\text{C}$ and up to 3500VAC/1min isolation voltage. For more information see vincotech.com website.				

Document No.:	Date:	Modification:	Pages
10-E112PNB015M702-PS08C76Z-T1-14	11 Sep. 2026	Initial release	

Product status definition		
Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.

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