



Vincotech

10-EY12PNA025M7-L187C78T

datasheet

flowPIM E2

1200 V / 25 A

Features

- IGBT M7 with low VCEsat and improved EMC behavior
- Standard industrial housing
- Optimized Rth(j-s) with Phase Change Material
- Built-in NTC

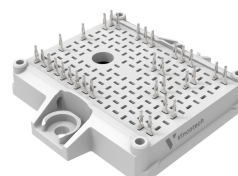
Target applications

- Industrial Drives

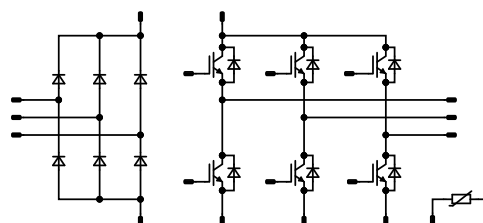
Types

- 10-EY12PNA025M7-L187C78T

flow E2 12 mm housing



Schematic





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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Inverter Switch				
Collector-emitter voltage	V_{CES}		1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	33	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}	50	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	79	W
Gate-emitter voltage	V_{GES}		± 20	V
Short circuit ratings	t_{SC}	$V_{GE} = 15\text{ V}$, $V_{CC} = 800\text{ V}$ $T_j = 150\text{ °C}$	9,5	μs
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Inverter Diode

Peak repetitive reverse voltage	V_{RRM}		1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	36	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	50	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	71	W
Maximum junction temperature	T_{jmax}		175	$^{\circ}\text{C}$

Rectifier Diode

Peak repetitive reverse voltage	V_{RRM}		1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	58	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, $t_p = 10\text{ ms}$ $T_j = 150\text{ °C}$	400	A
Surge current capability	I^2_t		800	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$ $T_s = 80\text{ °C}$	68	W
Maximum junction temperature	T_{jmax}		150	$^{\circ}\text{C}$



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Maximum Ratings

$T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
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Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{jop}		-40...+(T_{jmax} - 25)	°C

Isolation Properties

Isolation voltage	V_{isol}	DC Test Voltage* $t_p = 2\text{ s}$	6000	V
Isolation voltage	V_{isol}	AC Voltage $t_p = 1\text{ min}$	2500	V
Creepage distance			>12,7	mm
Clearance			8,97	mm
Comparative Tracking Index	CTI		≥ 600	

*100 % tested in production



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Switch

Static

Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0025	25	5,4	6	6,6	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		25	25 125 150		1,64 1,89 1,95	2,15 ⁽¹⁾	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			70	µA
Gate-emitter leakage current	I_{GES}		0	0		25			500	nA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}	0	10	25				4800		pF
Output capacitance	C_{oes}							170		pF
Reverse transfer capacitance	C_{res}							57		pF
Gate charge	Q_g	$V_{CC} = 600$ V	15		25	25		180		nC

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,2		K/W
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Dynamic

Turn-on delay time	$t_{d(on)}$	$R_{gon} = 16$ Ω $R_{goff} = 16$ Ω	±15	600	25	25 125 150		147,2 149,2 145,2		ns
Rise time	t_r					25 125 150		28,6 33,4 34		ns
Turn-off delay time	$t_{d(off)}$					25 125 150		170,6 191,4 195,6		ns
Fall time	t_f					25 125 150		95,03 109,96 114,76		ns
Turn-on energy (per pulse)	E_{on}					25 125 150		2,06 2,66 2,82		mWs
Turn-off energy (per pulse)	E_{off}					25 125 150		1,67 2,18 2,29		mWs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Inverter Diode

Static

Forward voltage	V_F				25	25 125 150		1,63 1,7 1,69	2,1 ⁽¹⁾	V
Reverse leakage current	I_R	$V_i = 1200$ V				25			35	µA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,34		K/W
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Dynamic

Peak recovery current	I_{RRM}	$di/dt=645$ A/µs $di/dt=673$ A/µs $di/dt=633$ A/µs	± 15	600	25	25 125 150		20,99 22,6 23,21		A
Reverse recovery time	t_{rr}					25 125 150		254,14 367,32 404,24		ns
Recovered charge	Q_r					25 125 150		2,54 3,88 4,28		µC
Reverse recovered energy	E_{rec}					25 125 150		0,884 1,45 1,61		mWs
Peak rate of fall of recovery current	$(di_{rr}/dt)_{max}$					25 125 150		217,37 134,32 132,01		A/µs



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Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	

Rectifier Diode

Static

Forward voltage	V_F				35	25 125 150		1,09 1,02 1,02	1,5 ⁽¹⁾	V
Reverse leakage current	I_R	$V_i = 1600$ V				25 150			100 2000	μA

Thermal

Thermal resistance junction to sink ⁽²⁾	$R_{th(j-s)}$	$\lambda_{paste} = 3,4$ W/mK (PSX)						1,03		K/W
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Thermistor

Static

Rated resistance	R					25		5		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	$R_{100} = 493$ Ω				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. ± 2 %						3375		K
B-value	$B_{(25/100)}$	Tol. ± 2 %						3437		K
Vincotech Thermistor Reference									K	

⁽¹⁾ Value at chip level

⁽²⁾ Only valid with pre-applied Vincotech thermal interface material.



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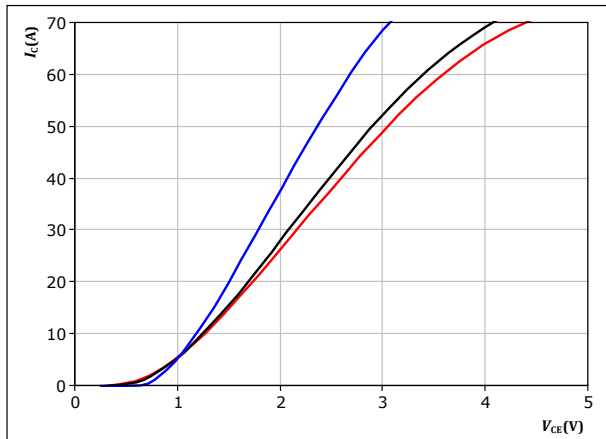
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Inverter Switch Characteristics

figure 1. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$



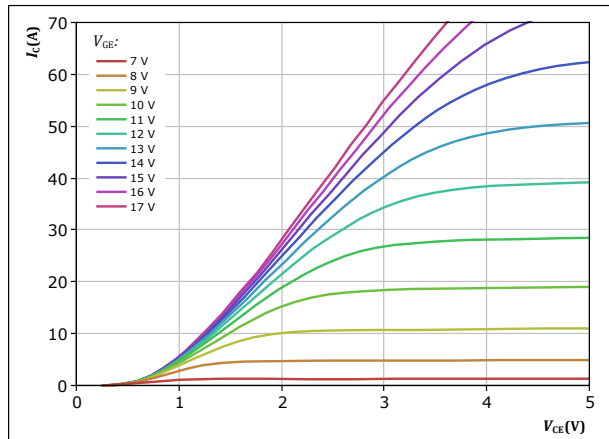
$t_p = 250 \mu s$
 $V_{GE} = 15 V$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 2. IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

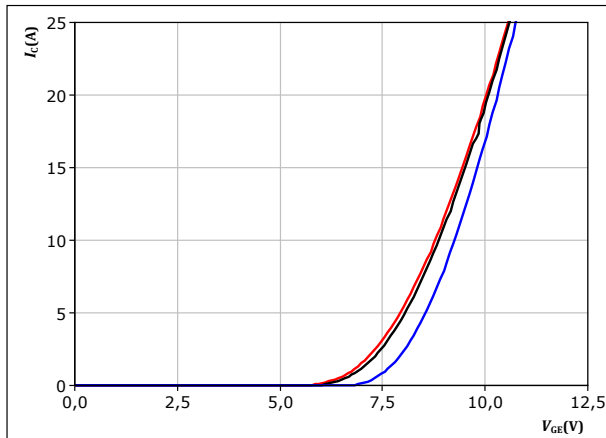


$t_p = 250 \mu s$
 $T_j = 150 ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$



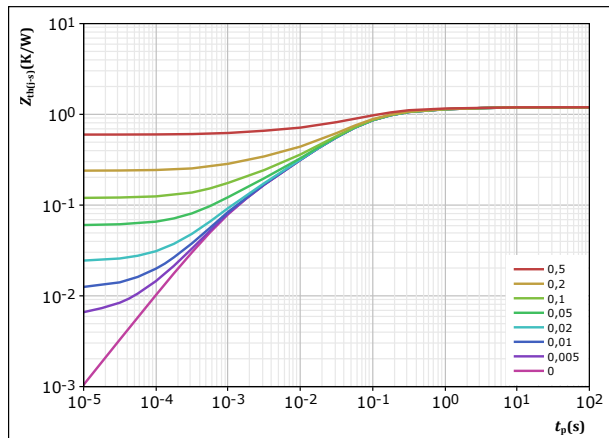
$t_p = 250 \mu s$
 $V_{CE} = 10 V$

T_j :
— 25 °C
— 125 °C
— 150 °C

figure 4. IGBT

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$
 $R_{th(j-s)} = 1,196 K/W$
IGBT thermal model values

$R (K/W)$	$\tau (s)$
7,19E-02	2,96E+00
1,76E-01	3,16E-01
6,46E-01	6,48E-02
2,16E-01	1,08E-02
8,54E-02	1,15E-03



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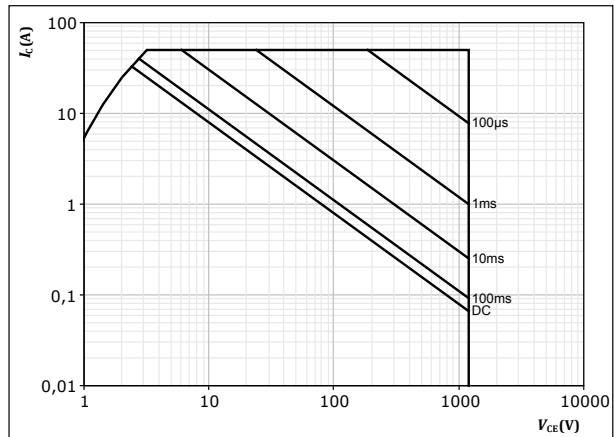
10-EY12PNA025M7-L187C78T
datasheet

Inverter Switch Characteristics

figure 5. IGBT

Safe operating area

$$I_C = f(V_{CE})$$



D = single pulse

$T_s = 80$ °C

$V_{GE} = 15$ V

$T_j = T_{jmax}$



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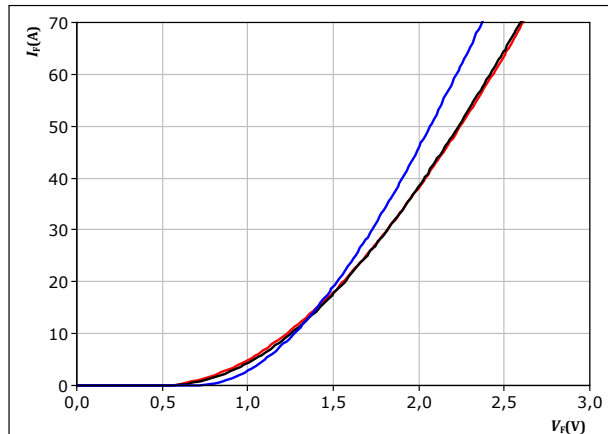
10-EY12PNA025M7-L187C78T datasheet

Inverter Diode Characteristics

figure 6. FWD

Typical forward characteristics

$$I_F = f(V_F)$$



$t_p = 250 \mu s$

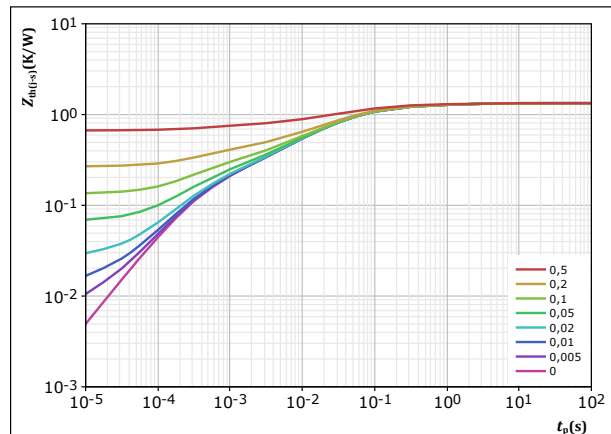
T_j :

- 25 °C
- 125 °C
- 150 °C

figure 7. FWD

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$



$D = t_p / T$

$R_{th(j-s)} = 1,335 \text{ K/W}$

FWD thermal model values

$R \text{ (K/W)}$	$\tau \text{ (s)}$
7,90E-02	2,29E+00
1,38E-01	2,89E-01
5,15E-01	5,53E-02
3,34E-01	1,22E-02
1,30E-01	2,48E-03
1,40E-01	3,42E-04



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Rectifier Diode Characteristics

figure 8.

Rectifier

Typical forward characteristics

$$I_F = f(V_F)$$

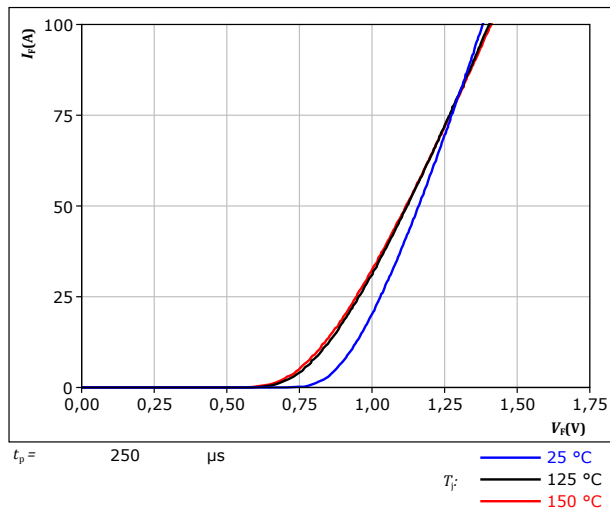
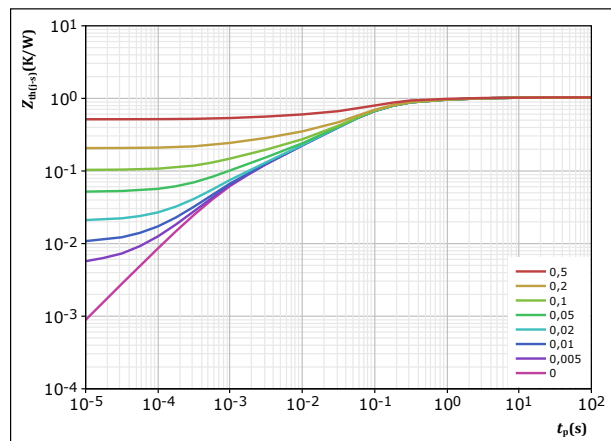


figure 9.

Rectifier

Transient thermal impedance as a function of pulse width

$$Z_{th(j-s)} = f(t_p)$$





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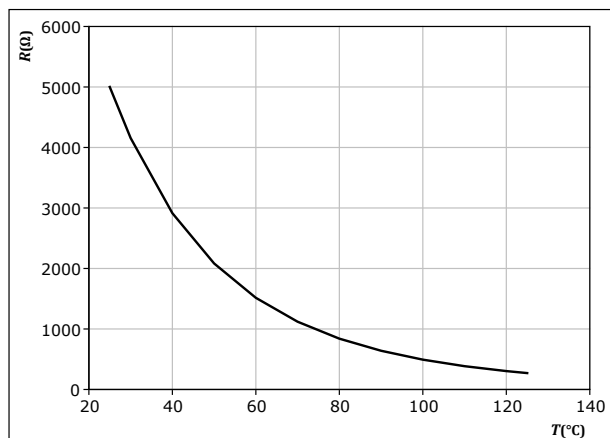
Thermistor Characteristics

figure 10.

Thermistor

Typical NTC characteristic as function of temperature

$$R_T = f(T)$$





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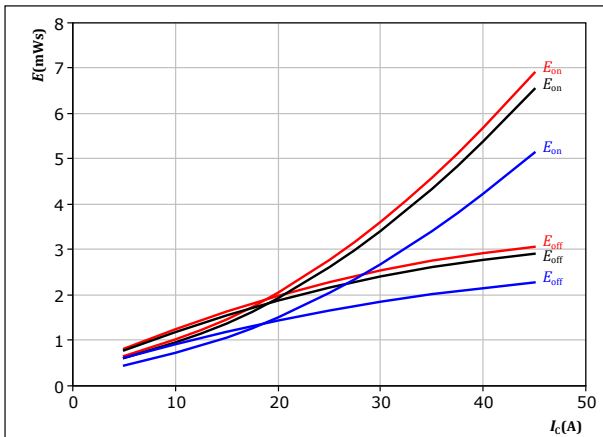
Inverter Switching Characteristics

figure 11.

IGBT

Typical switching energy losses as a function of collector current

$$E = f(I_C)$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 16 \text{ } \Omega$
 $R_{goff} = 16 \text{ } \Omega$

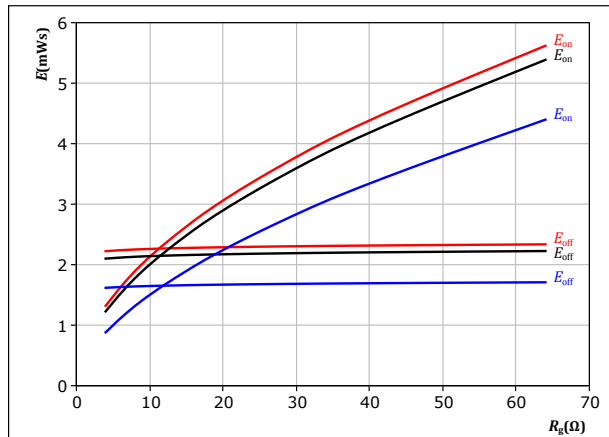
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 12.

IGBT

Typical switching energy losses as a function of gate resistor

$$E = f(R_g)$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 25 \text{ A}$

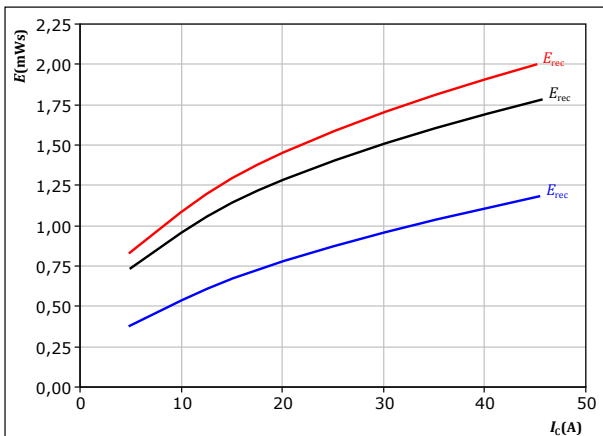
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 13.

FWD

Typical reverse recovered energy loss as a function of collector current

$$E_{rec} = f(I_C)$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{gon} = 16 \text{ } \Omega$

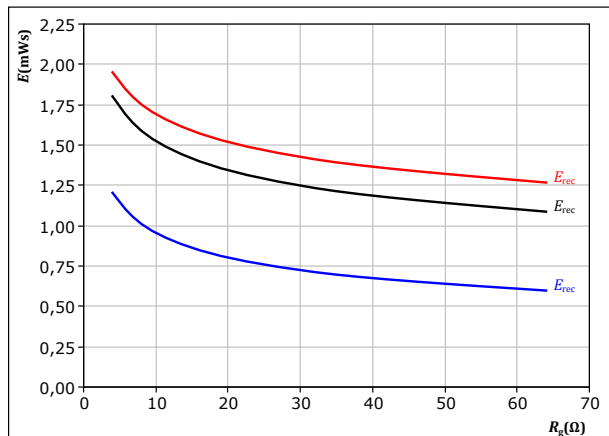
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 14.

FWD

Typical reverse recovered energy loss as a function of gate resistor

$$E_{rec} = f(R_g)$$



With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 25 \text{ A}$

T_j :
— 25 °C
— 125 °C
— 150 °C



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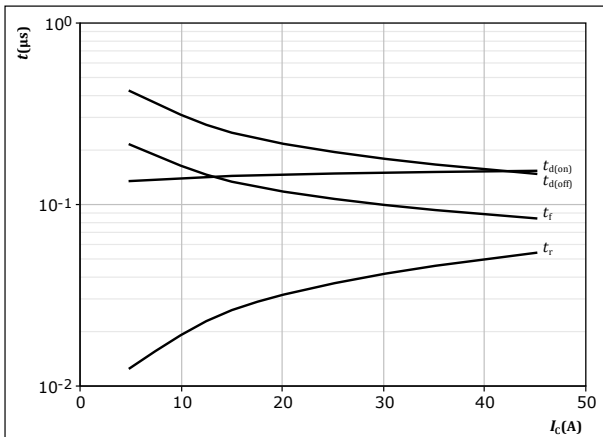
Inverter Switching Characteristics

figure 15.

IGBT

Typical switching times as a function of collector current

$$t = f(I_c)$$



With an inductive load at

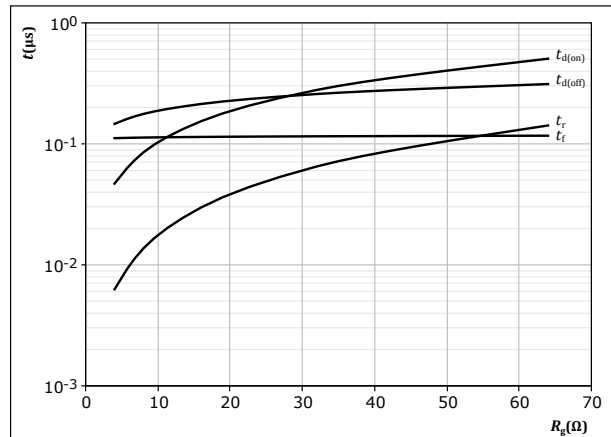
$T_j = 150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω
 $R_{goff} = 16$ Ω

figure 16.

IGBT

Typical switching times as a function of gate resistor

$$t = f(R_g)$$



With an inductive load at

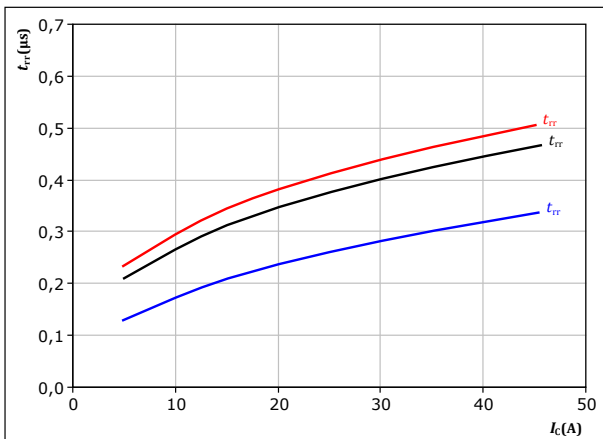
$T_j = 150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 25$ A

figure 17.

FWD

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_c)$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω

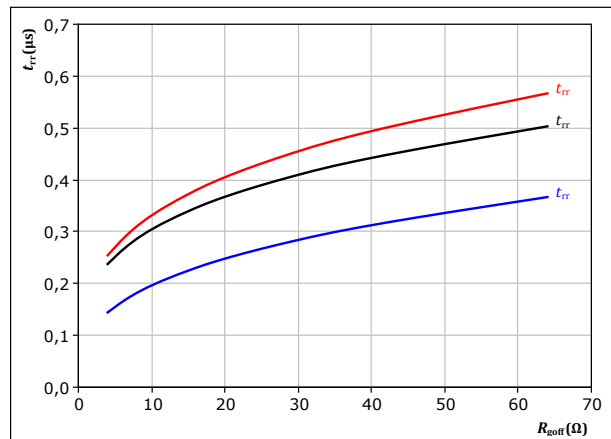
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 18.

FWD

Typical reverse recovery time as a function of IGBT turn off gate resistor

$$t_{rr} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_c = 25$ A

T_j :
— 25 °C
— 125 °C
— 150 °C



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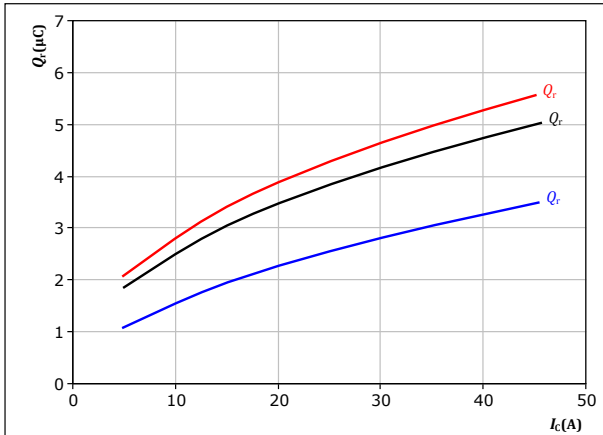
Inverter Switching Characteristics

figure 19.

FWD

Typical recovered charge as a function of collector current

$$Q_r = f(I_C)$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω

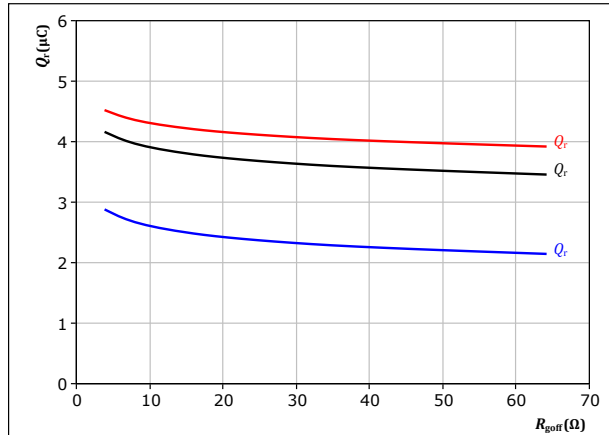
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 20.

FWD

Typical recovered charge as a function of turn off gate resistor

$$Q_r = f(R_{goff})$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 25$ A

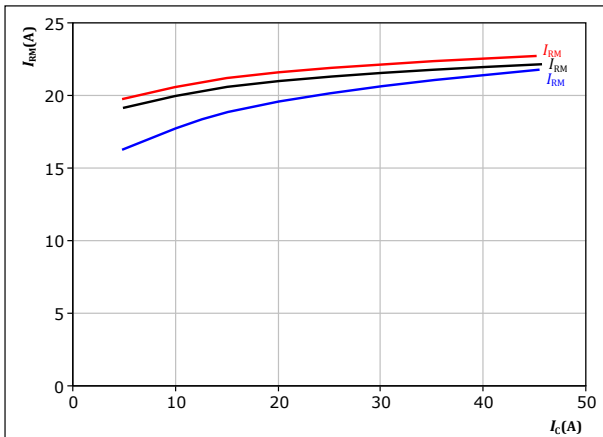
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 21.

FWD

Typical peak reverse recovery current as a function of collector current

$$I_{RM} = f(I_C)$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 16$ Ω

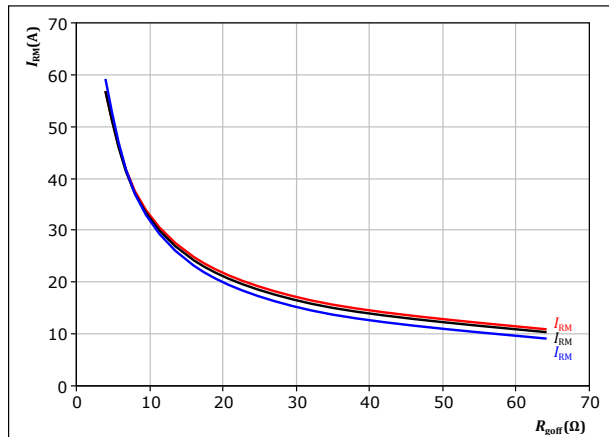
T_j :
— 25 °C
— 125 °C
— 150 °C

figure 22.

FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$$I_{RM} = f(R_{goff})$$



With an inductive load at

$V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $I_C = 25$ A

T_j :
— 25 °C
— 125 °C
— 150 °C



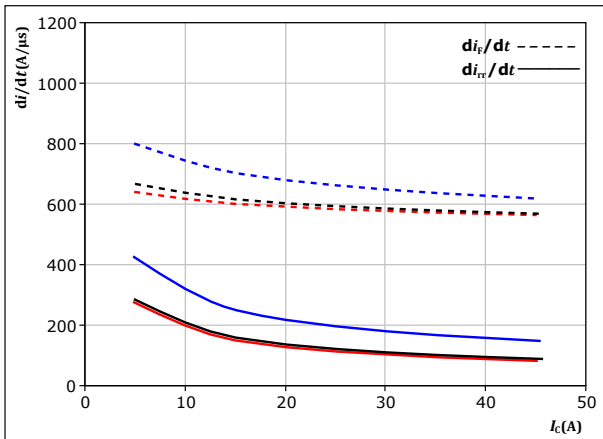
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Inverter Switching Characteristics

figure 23. FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_f/dt, di_r/dt = f(I_C)$



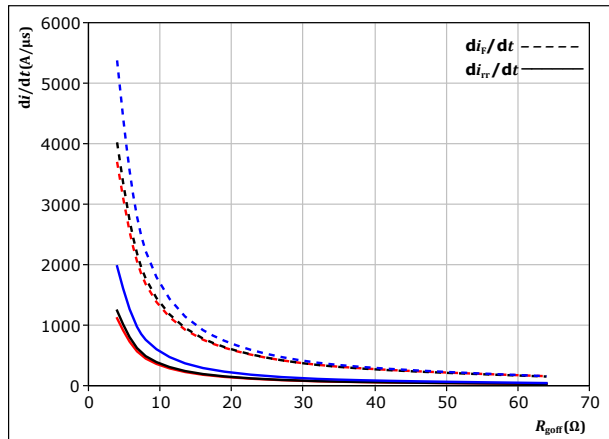
With an inductive load at

$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{goff} = 16 \text{ } \Omega$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 24. FWD

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor
 $di_f/dt, di_r/dt = f(R_{goff})$



With an inductive load at

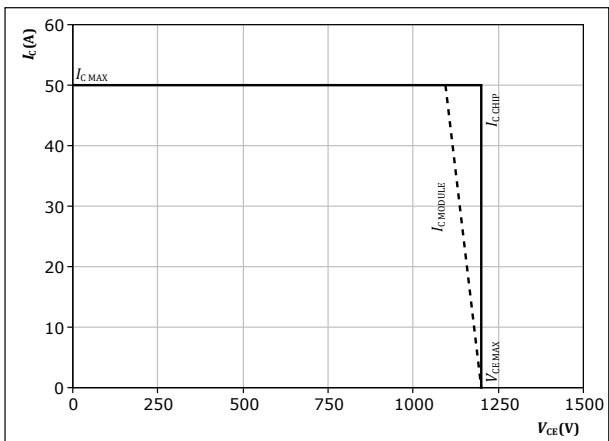
$V_{CE} = 600 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 25 \text{ A}$

T_j : 25 °C (blue), 125 °C (black), 150 °C (red)

figure 25. IGBT

Reverse bias safe operating area

$I_C = f(V_{CE})$



At $T_j = 150 \text{ } ^\circ\text{C}$
 $R_{goff} = 16 \text{ } \Omega$
 $R_{goff} = 16 \text{ } \Omega$



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Inverter Switching Definitions

figure 26. IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})

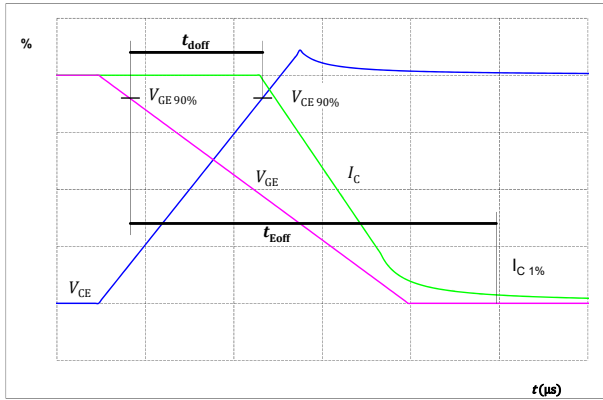


figure 27. IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})

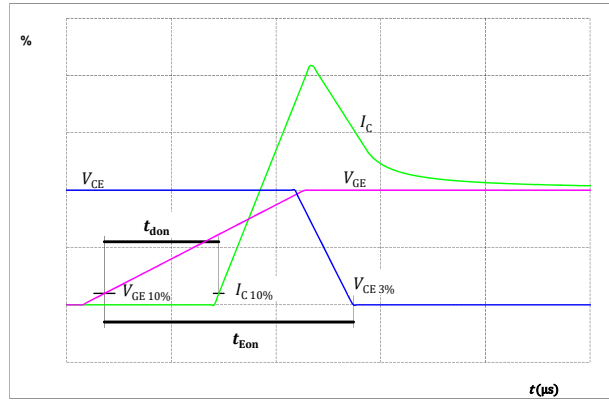


figure 28. IGBT

Turn-off Switching Waveforms & definition of t_f

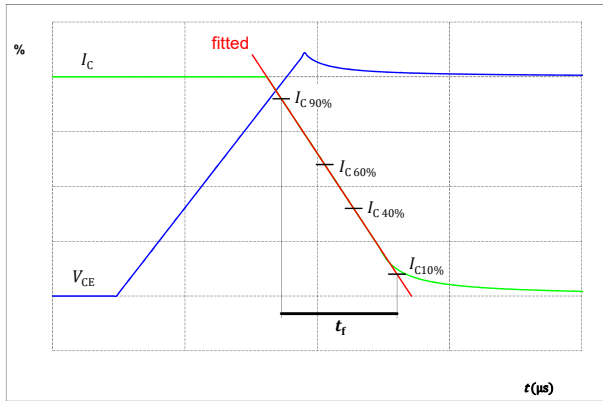
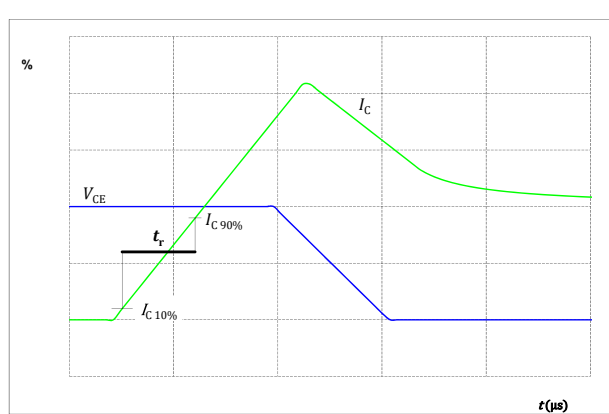


figure 29. IGBT

Turn-on Switching Waveforms & definition of t_r





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Inverter Switching Definitions

figure 30.

FWD

Turn-off Switching Waveforms & definition of t_{rr}

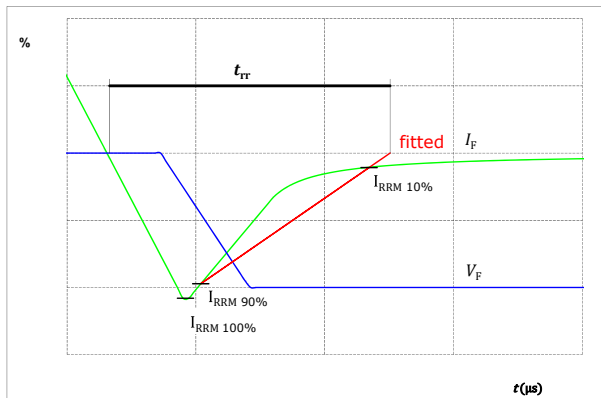
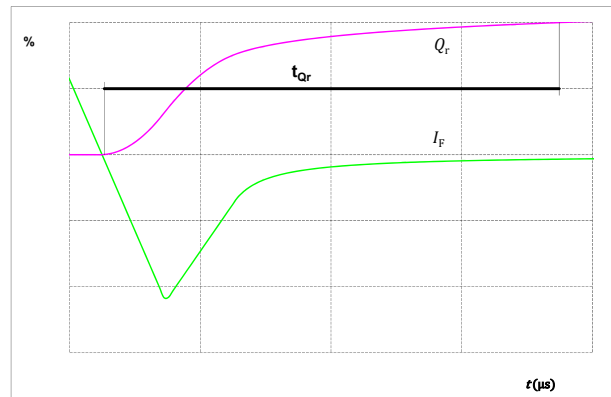


figure 31.

FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)





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10-EY12PNA025M7-L187C78T

datasheet

Ordering Code	
Version	Ordering Code
Without thermal paste	10-EY12PNA025M7-L187C78T
With thermal paste	10-EY12PNA025M7-L187C78T-/3/

Marking						
Text	Name		Date code	UL & VIN	Lot	Serial
	NN-NNNNNNNNNNNNNN- TTTTTVV		WWYY	UL VIN	LLLLL	SSSS
Datamatrix	Type&Ver	Lot number	Serial	Date code		
	TTTTTTTV	LLLLL	SSSS	WWYY		

Outline			
Pin table [mm]			
Pin	X	Y	Function
1	25,6	6,4	ACIn2
2	22,4	6,4	ACIn2
3	16	9,6	ACIn1
4	12,8	9,6	ACIn1
5	9,6	0	DC+Rect
6	9,6	3,2	DC+Rect
7	0	0	DC-Rect
8	0	3,2	DC-Rect
9	not assembled		
10	not assembled		
11	0	22,4	G11
12	0	25,6	DC-1
13	0	28,8	DC-1
14	0	32	G13
15	0	35,2	DC-2
16	0	38,4	DC-2
17	0	41,6	G15
18	0	44,8	DC-3
19	0	48	DC-3
20	9,6	48	Therm1
21	19,2	48	Therm2
22	28,8	48	G16
23	32	48	Ph3
24	32	44,8	Ph3
25	32	35,2	G14
26	32	32	Ph2
27	32	28,8	Ph2
28	32	19,2	G12
29	32	16	Ph1
30	32	12,8	Ph1
31	32	3,2	ACIn3
32	32	0	ACIn3
33	22,4	19,2	DC+Inv
34	22,4	16	DC+Inv
35	not assembled		

center of press-fit pinhead
for connection parameter see the handling instruction

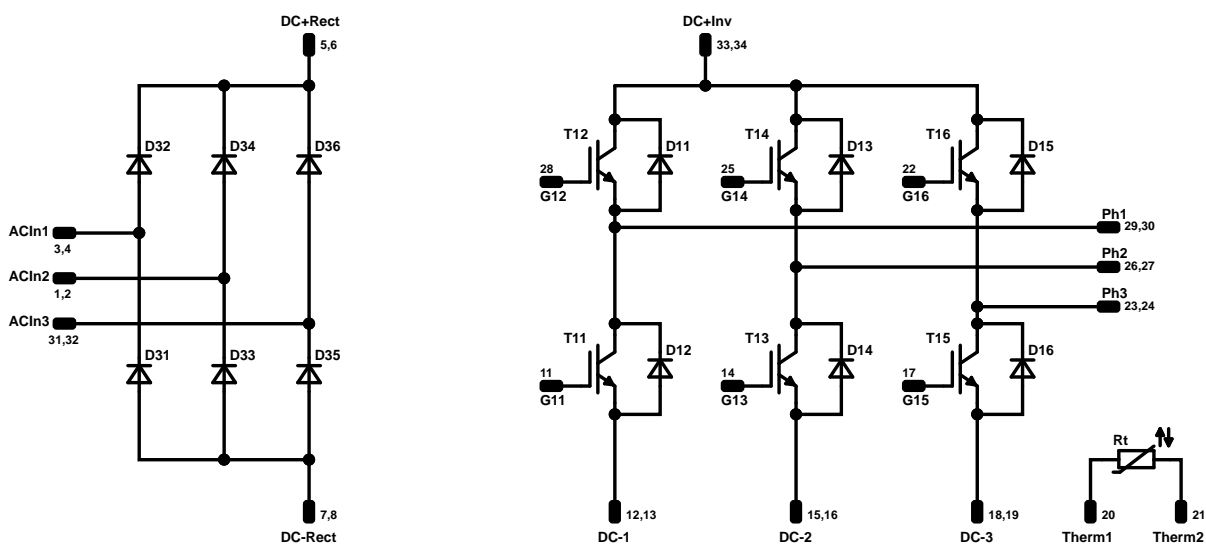
19,8 ±0,1 mm

Tolerance of positions: ±0,4mm at the end of pins
Dimension of coordinate axis is only offset without tolerance



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Pinout




Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	25 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	25 A	Inverter Diode	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	35 A	Rectifier Diode	
Rt	NTC			Thermistor	



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10-EY12PNA025M7-L187C78T
datasheet

Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample
Handling instruction				
Handling instructions for <i>flow</i> E2 packages see vincotech.com website.				
Package data				
Package data for <i>flow</i> E2 packages see vincotech.com website.				
Vincotech thermistor reference				
See Vincotech thermistor reference table at vincotech.com website.				
UL recognition and file number				
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.				

Document No.:	Date:	Modification:	Pages
10-EY12PNA025M7-L187C78T-D2-14	20 Apr. 2021	New datasheet format Update characteristics of rectifier diode, leakage current max value from 50 -> 100 uA	

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.