Vincotech

MiniSKiiP® 2 PACK — 600V/100A

Features

- SixPack (inverter) topology
- Solder less interconnection
- Designed for motor drives up to 11 kW
- Fully compatible with Semikron pedant 28AC066V1
- Temperature sensor
- Standard (6.5mm) and thin (2.8mm) lids,16mm housing
- Optional with pre-applied thermal grease

Target Applications

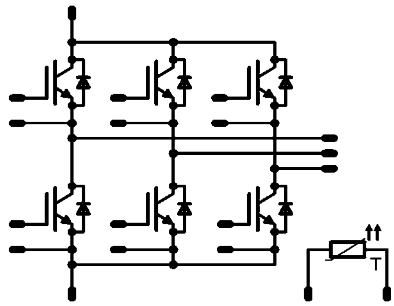
- Industrial Motor Drives
- Power Generation
- UPS

Types

- V23990-K305-F-PM

MiniSKiiP® 2 Housing

Schematic



Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition		Value	Unit
T1,T2,T3,T4,T5,T6 IGBT					
Collector-emitter break down voltage	V_{CE}			600	V
DC collector current	I_C	$T_j=T_j$max	T_h=80°C T_c=80°C	91 100	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_j$max		150	A
Power dissipation per IGBT	P_{tot}	$T_j=T_j$max	T_h=80°C T_c=80°C	154 234	W
Gate-emitter peak voltage	V_{GE}			±20	V
Short circuit ratings	t_{SC} V_{CC}	T_j≤150°C V_{GE}=15V		6 360	µs V
Maximum Junction Temperature	$T_j$max			175	°C
D1,D2,D3,D4,D5,D6 FWD					
Peak Repetitive Reverse Voltage	V_{RRM}			600	V
DC forward current	I_F	$T_j=T_j$max	T_h=80°C T_c=80°C	91 100	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j$max		157	A
Power dissipation per Diode	P_{tot}	$T_j=T_j$max	T_h=80°C T_c=80°C	119 181	W
Maximum Junction Temperature	$T_j$max			175	°C

Maximum Ratings

T_j=25℃, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Thermal Properties				
Storage temperature	T_{stg}		-40…+125	℃
Operation temperature under switching condition	T_{op}		-40…+(Tjmax - 25)	℃
Insulation Properties				
Insulation voltage	V_{is}	t=2s DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

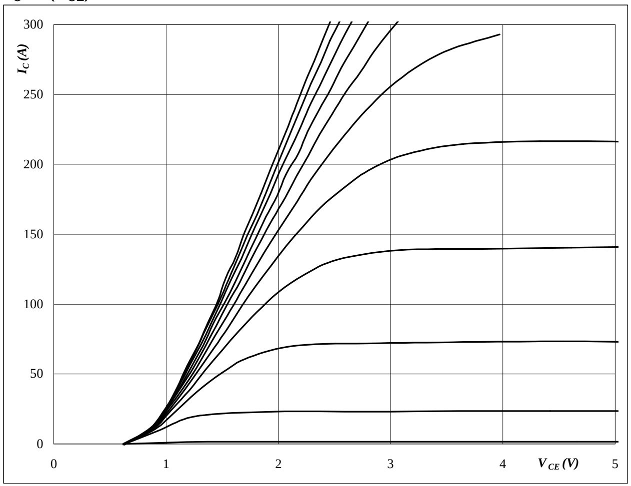
Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_C [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	
T1,T2,T3,T4,T5,T6 IGBT										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0016	Tj=25℃ Tj=125℃	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		100	Tj=25℃ Tj=125℃		1,48 1,67		V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	600		Tj=25℃ Tj=125℃			0,2	mA
Gate-emitter leakage current	I_{GES}		±20	0		Tj=25℃ Tj=125℃			700	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	Rgoff=8 Ω Rgon=8 Ω	±15	300	50	Tj=25℃ Tj=125℃		197 199		ns
Rise time	t_r					Tj=25℃ Tj=125℃		29 33		
Turn-off delay time	$t_{d(off)}$					Tj=25℃ Tj=125℃		261 216		
Fall time	t_f					Tj=25℃ Tj=125℃		87 97		
Turn-on energy loss per pulse	E_{on}					Tj=25℃ Tj=125℃		2,40 3,11		mWs
Turn-off energy loss per pulse	E_{off}					Tj=25℃ Tj=125℃		2,51 3,07		
Input capacitance	C_{ies}	f=1MHz	0	25		Tj=25℃		3140		pF
Output capacitance	C_{oss}							200		
Reverse transfer capacitance	C_{rss}							93		
Gate charge	Q_{Gate}		±15	480	50	Tj=25℃		310		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						0,6		K/W
D1,D2,D3,D4,D5,D6 FWD										
Diode forward voltage	V_F				100	Tj=25℃ Tj=125℃	1	1,35 1,36	2,3	V
Peak reverse recovery current	I_{RRM}	Rgoff=8 Ω	±15	300	100	Tj=25℃ Tj=125℃		94 114		A
Reverse recovery time	t_{rr}					Tj=25℃ Tj=125℃		144 289		ns
Reverse recovered charge	Q_{rr}					Tj=25℃ Tj=125℃		6,29 11,43		µC
Peak rate of fall of recovery current	di(rec)max /dt					Tj=25℃ Tj=125℃		3061 2051		A/µs
Reverse recovered energy	Erec					Tj=25℃ Tj=125℃		1,19 2,20		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						0,8		K/W
Thermistor										
Rated resistance	R					T=25℃		1000		Ω
Deviation of R100	ΔR/R	R100=1670 Ω				T=100℃	-3		3	%
R100	P					T=100℃		1670,3125		Ω
A-value	B(25/50)	Tol. %				T=25℃		7,635*10-3		1/K
B-value	B(25/100)	Tol. %				T=25℃		1,731*10-5		1/K²
Vincotech NTC Reference									E	

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 1 T1,T2,T3,T4,T5,T6 IGBT

Typical output characteristics

$I_C = f(V_{CE})$



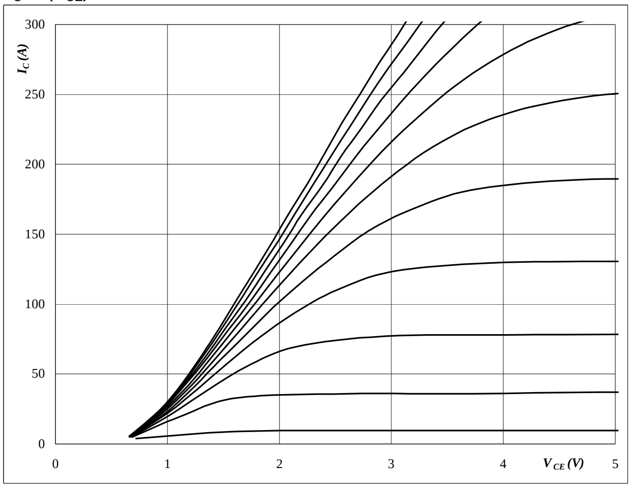
At

t_p =	250	μs
T_j =	25	℃
V_{GE} from	7 V to 17 V in steps of 1 V	

Figure 2 T1,T2,T3,T4,T5,T6 IGBT

Typical output characteristics

$I_C = f(V_{CE})$



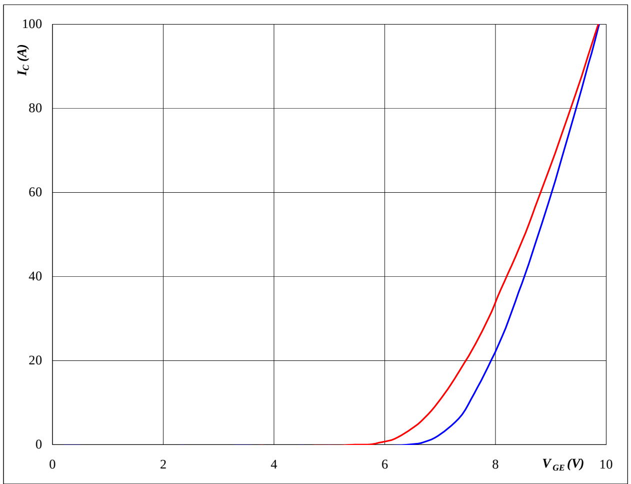
At

t_p =	250	μs
T_j =	125	℃
V_{GE} from	7 V to 17 V in steps of 1 V	

Figure 3 T1,T2,T3,T4,T5,T6 IGBT

Typical transfer characteristics

$I_C = f(V_{GE})$



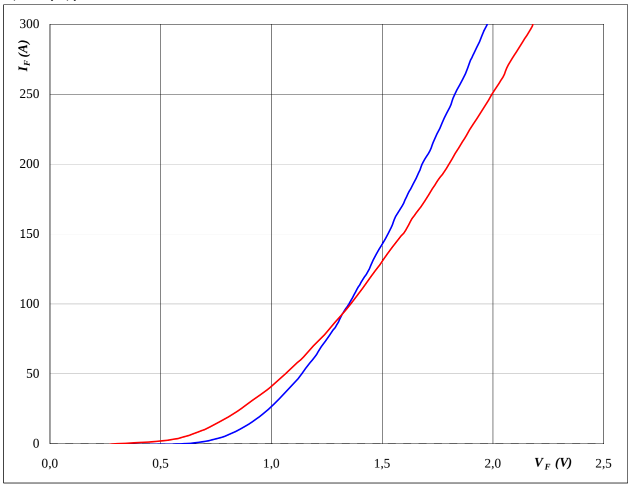
At

Tj =	25/125	℃
t_p =	250	μs
V_{CE} =	10	V

Figure 4 D1,D2,D3,D4,D5,D6 FWD

Typical diode forward current as a function of forward voltage

$I_F = f(V_F)$



At

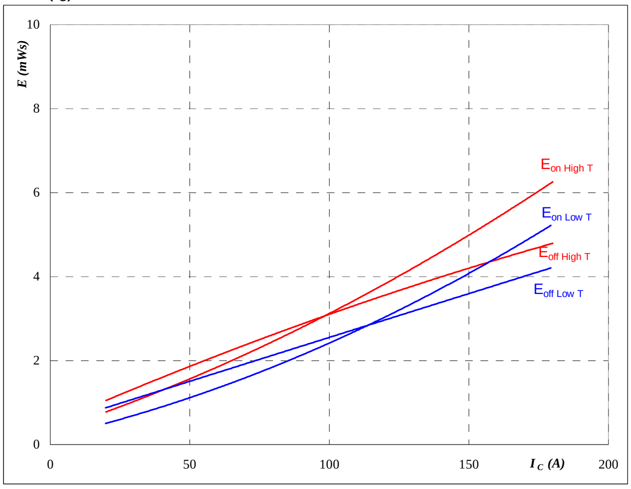
Tj =	25/125	℃
t_p =	250	μs

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 5 T1,T2,T3,T4,T5,T6 IGBT

Typical switching energy losses as a function of collector current

$E = f(I_C)$



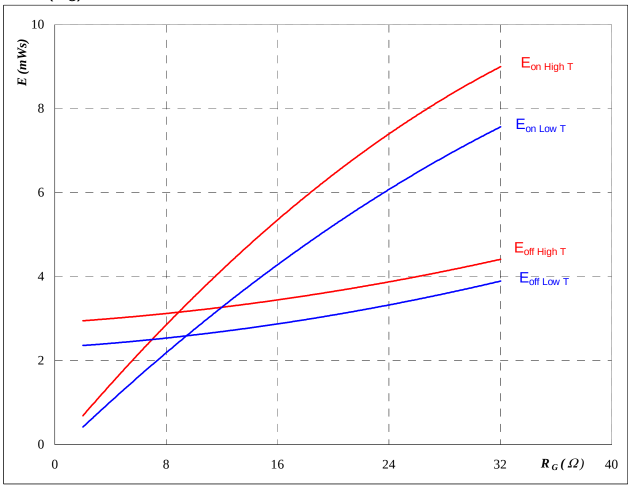
With an inductive load at

T_j =	25/125	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω
R_{goff} =	8	Ω

Figure 6 T1,T2,T3,T4,T5,T6 IGBT

Typical switching energy losses as a function of gate resistor

$E = f(R_G)$



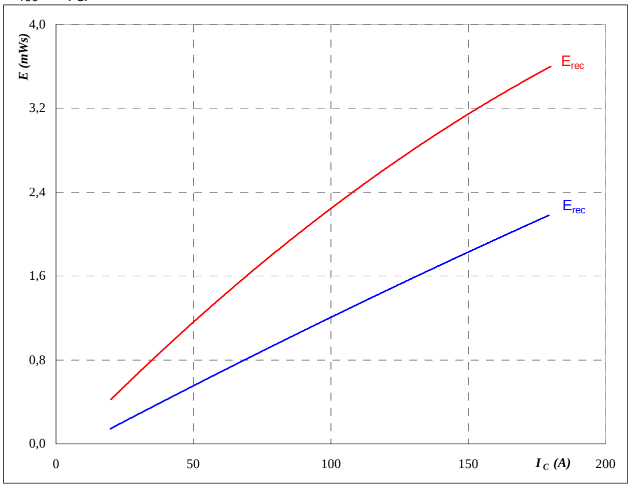
With an inductive load at

T_j =	25/125	°C
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	99	A

Figure 7 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery energy loss as a function of collector current

$E_{rec} = f(I_C)$



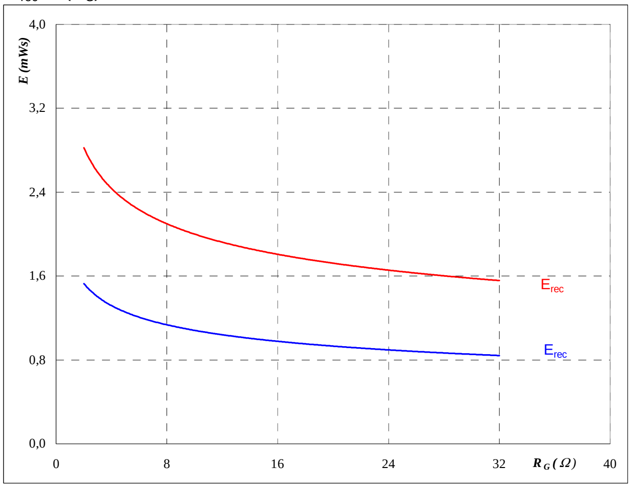
With an inductive load at

T_j =	25/125	°C
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

Figure 8 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery energy loss as a function of gate resistor

$E_{rec} = f(R_G)$



With an inductive load at

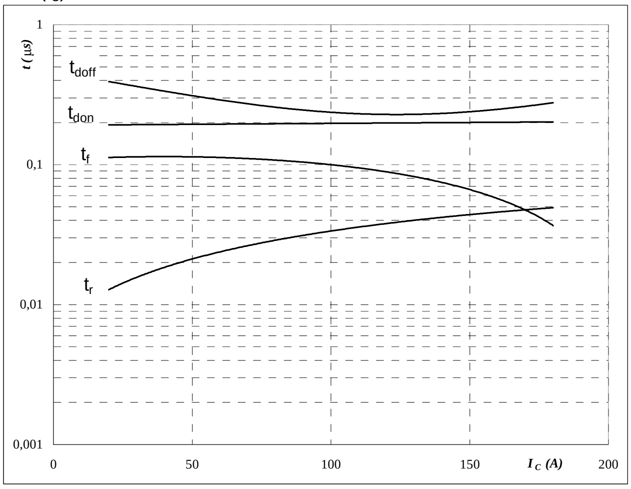
T_j =	25/125	°C
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	99	A

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 9 T1,T2,T3,T4,T5,T6 IGBT

Typical switching times as a function of collector current

$t = f(I_C)$



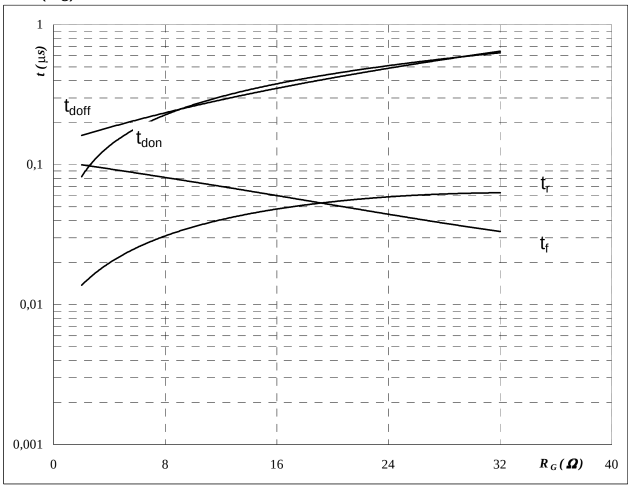
With an inductive load at

T_j =	125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω
R_{goff} =	8	Ω

Figure 10 T1,T2,T3,T4,T5,T6 IGBT

Typical switching times as a function of gate resistor

$t = f(R_G)$



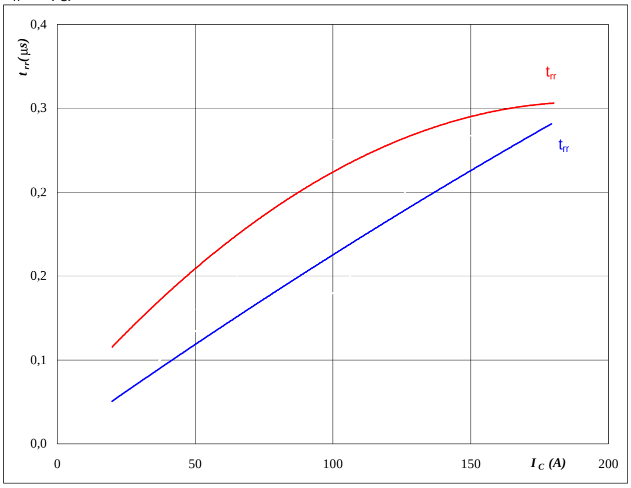
With an inductive load at

T_j =	125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
I_C =	99	A

Figure 11 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of collector current

$t_{rr} = f(I_C)$



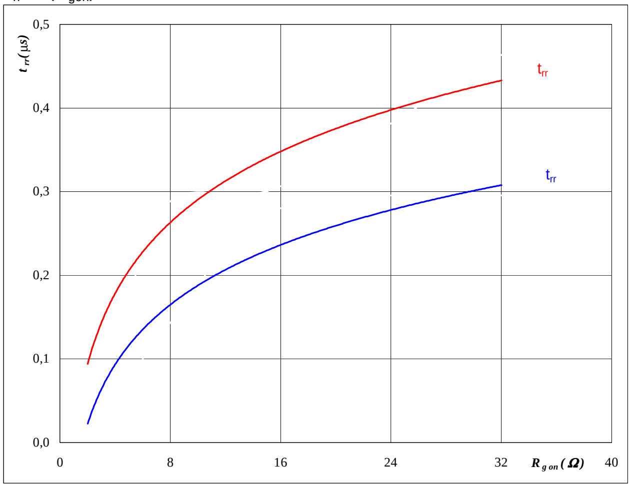
At

T_j =	25/125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

Figure 12 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$t_{rr} = f(R_{gon})$



At

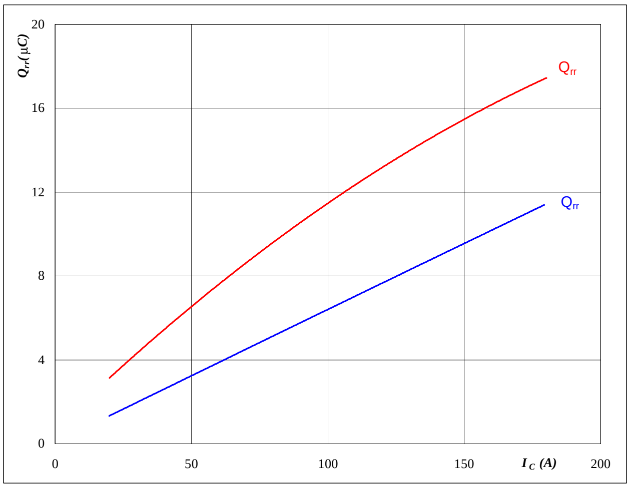
T_j =	25/125	℃
V_R =	300	V
I_F =	99	A
V_{GE} =	±15	V

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 13 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of collector current

$Q_{rr} = f(I_C)$



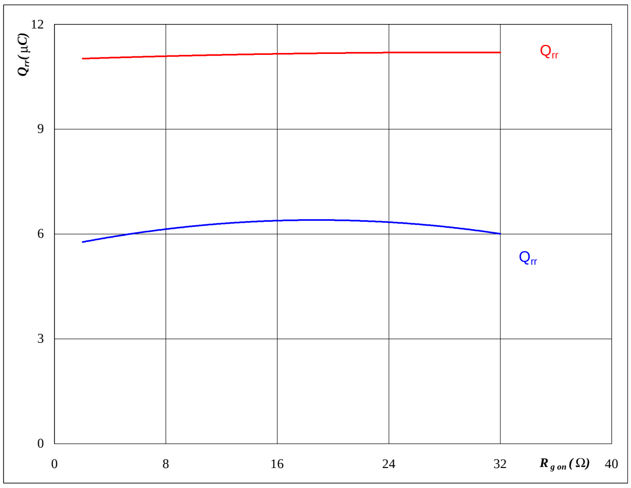
At

T_j =	25/125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

Figure 14 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$Q_{rr} = f(R_{gon})$



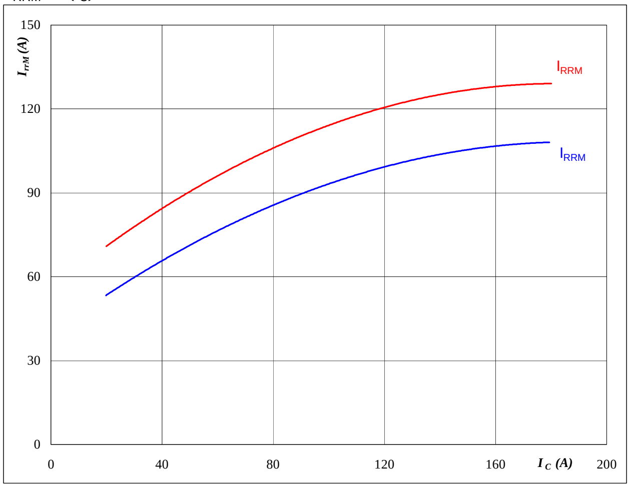
At

T_j =	25/125	℃
V_R =	300	V
I_F =	99	A
V_{GE} =	±15	V

Figure 15 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of collector current

$I_{RRM} = f(I_C)$



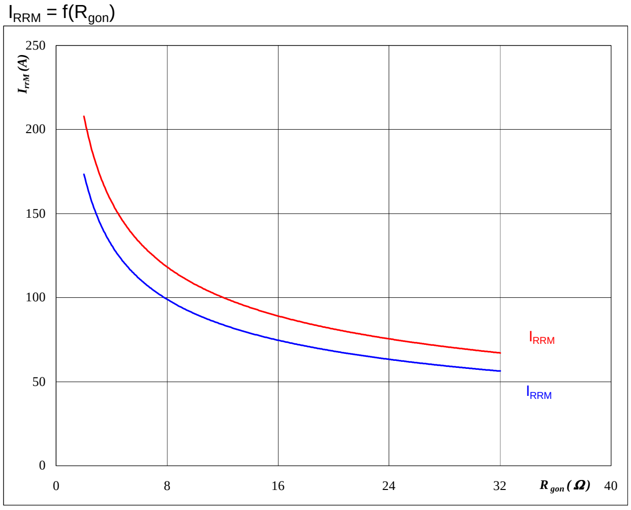
At

T_j =	25/125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

Figure 16 D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$I_{RRM} = f(R_{gon})$



At

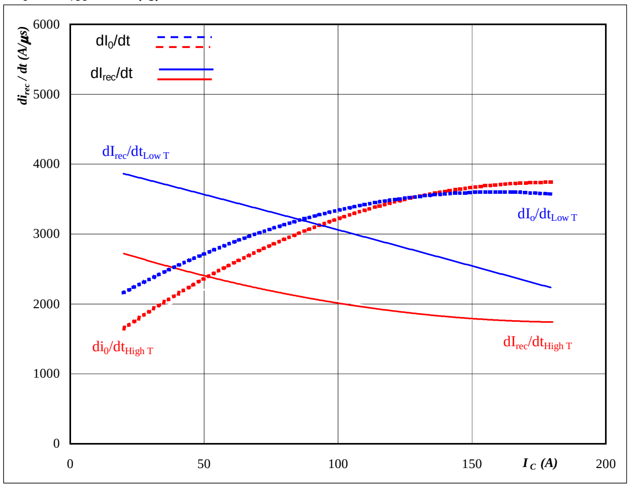
T_j =	25/125	℃
V_R =	300	V
I_F =	99	A
V_{GE} =	±15	V

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 17 D1,D2,D3,D4,D5,D6 FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current

$dI_0/dt, dI_{rec}/dt = f(I_C)$



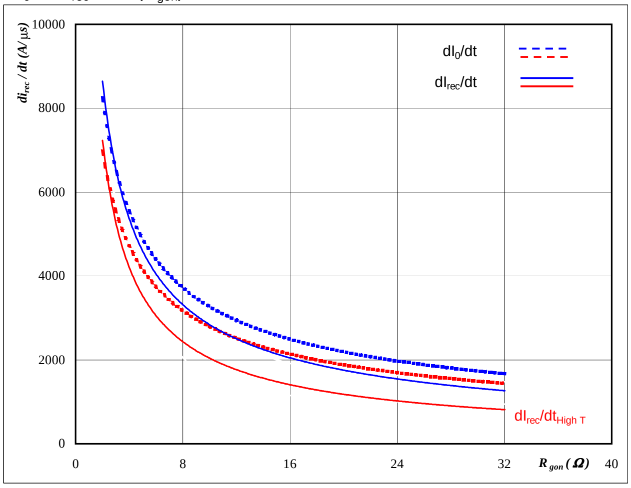
At

T_j =	25/125	℃
V_{CE} =	300	V
V_{GE} =	±15	V
R_{gon} =	8	Ω

Figure 18 D1,D2,D3,D4,D5,D6 FWD

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$dI_0/dt, dI_{rec}/dt = f(R_{gon})$



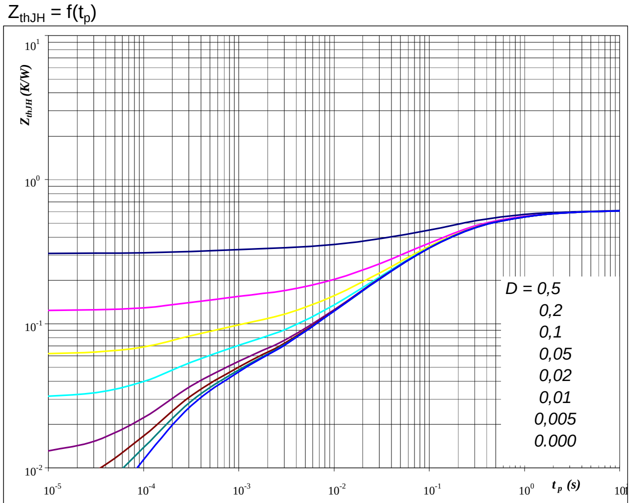
At

T_j =	25/125	℃
V_R =	300	V
I_F =	99	A
V_{GE} =	±15	V

Figure 19 T1,T2,T3,T4,T5,T6 IGBT

IGBT transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



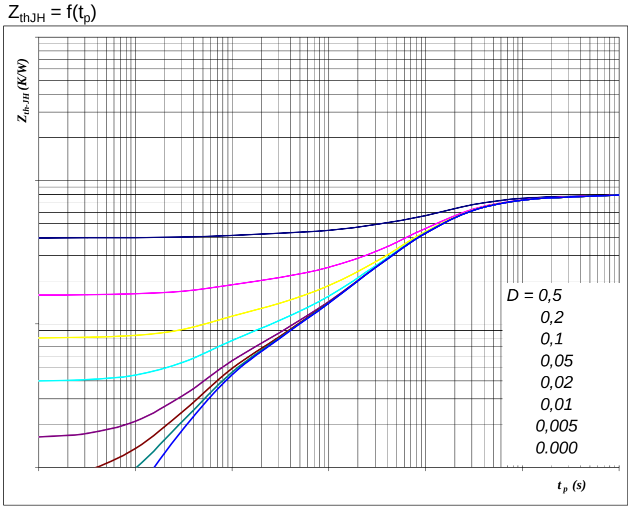
At

D =	t_p / T	
R_{thJH} =	0,6	K/W

Figure 20 D1,D2,D3,D4,D5,D6 FWD

FWD transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	0,8	K/W

IGBT thermal model values

Thermal grease

R (C/W)	Tau (s)
0,04	6,5E+00
0,09	1,0E+00
0,23	2,0E-01
0,15	5,9E-02
0,07	1,2E-02
0,02	2,2E-03
0,03	2,7E-04

FWD thermal model values

Thermal grease

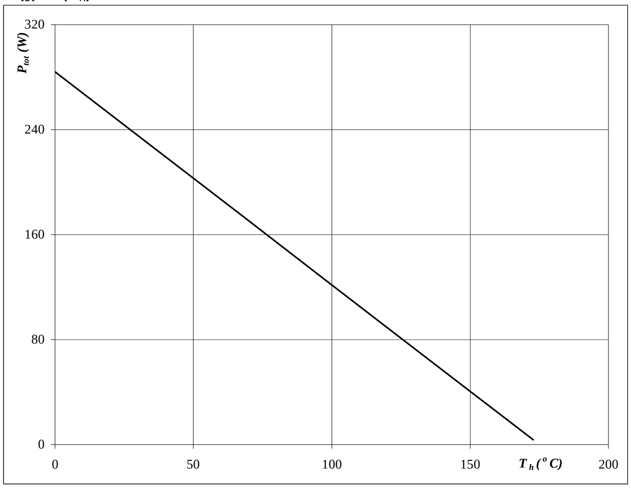
R (C/W)	Tau (s)
0,08	2,9E+00
0,26	3,2E-01
0,33	8,4E-02
0,08	1,1E-02
0,05	7,9E-04

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 21 T1,T2,T3,T4,T5,T6 IGBT

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



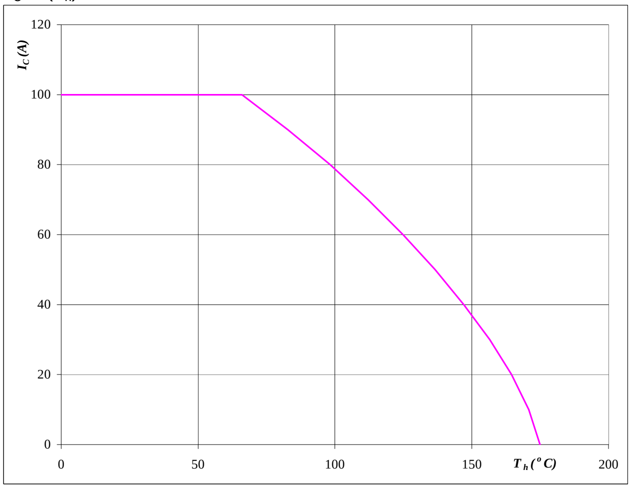
At

T_j = 175 °C

Figure 22 T1,T2,T3,T4,T5,T6 IGBT

Collector current as a function of heatsink temperature

$I_C = f(T_h)$



At

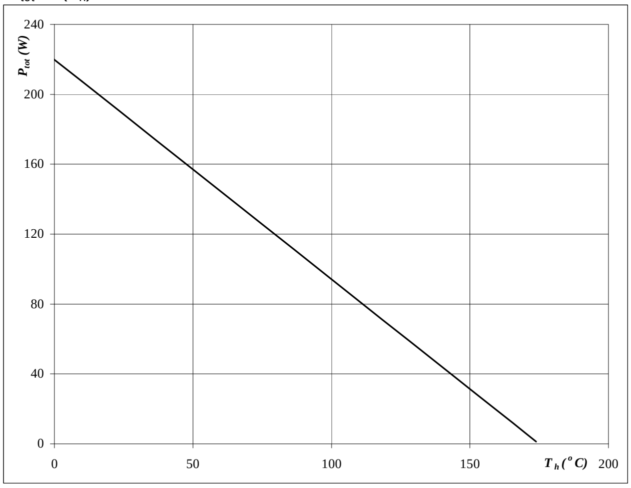
T_j = 175 °C

V_{GE} = 15 V

Figure 23 D1,D2,D3,D4,D5,D6 FWD

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



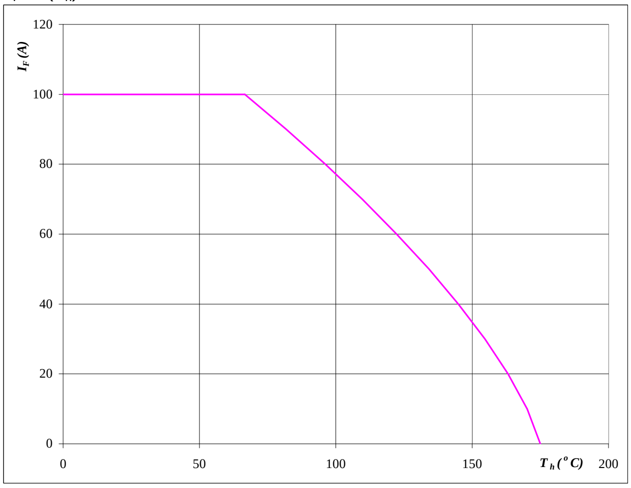
At

T_j = 175 °C

Figure 24 D1,D2,D3,D4,D5,D6 FWD

Forward current as a function of heatsink temperature

$I_F = f(T_h)$



At

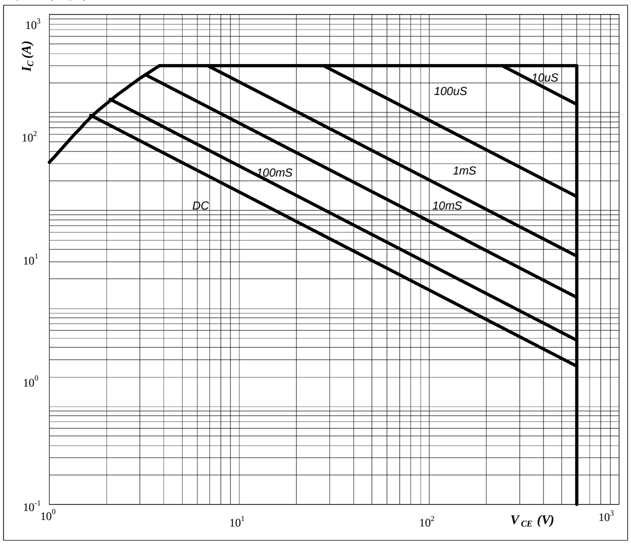
T_j = 175 °C

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 25 T1,T2,T3,T4,T5,T6 IGBT

Safe operating area as a function of collector-emitter voltage

$I_C = f(V_{CE})$



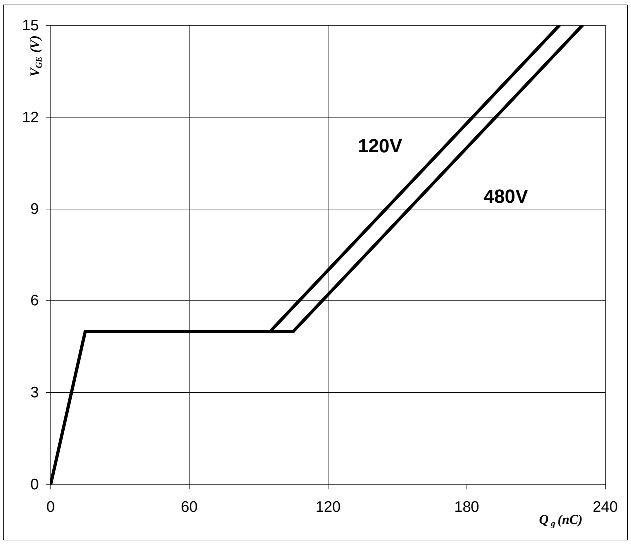
At

D =	single pulse	
T_h =	80	ºC
V_{GE} =	±15	V
T_j =	T_{jmax}	ºC

Figure 26 T1,T2,T3,T4,T5,T6 IGBT

Gate voltage vs Gate charge

$V_{GE} = f(Q_{GE})$



At

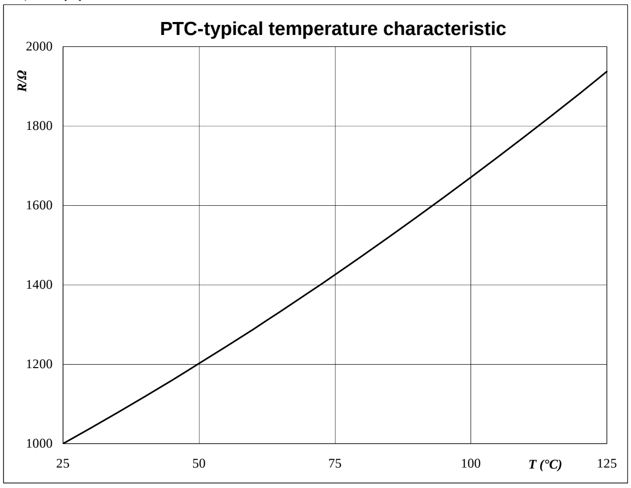
I_C =	99	A

Thermistor

Figure 1 Thermistor

Typical PTC characteristic as a function of temperature

$R_T = f(T)$



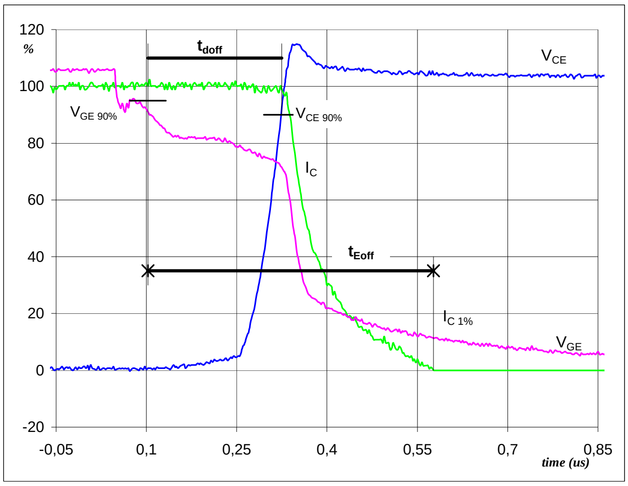
Switching Definitions Output Inverter

General conditions

T_j	**=**	**125 °C**
R_{gon}	**=**	**8 Ω**
R_{goff}	**=**	**8 Ω**

Figure 1 Output inverter IGBT

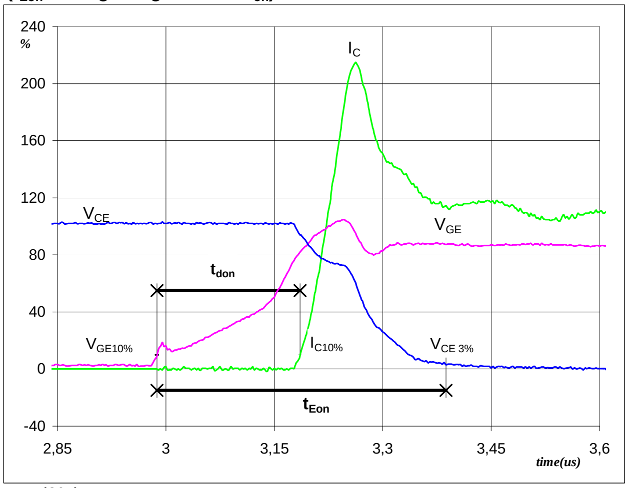
Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	300	V
I_C (100%) =	99	A
t_{doff} =	0,22	µs
t_{Eoff} =	0,47	µs

Figure 2 Output inverter IGBT

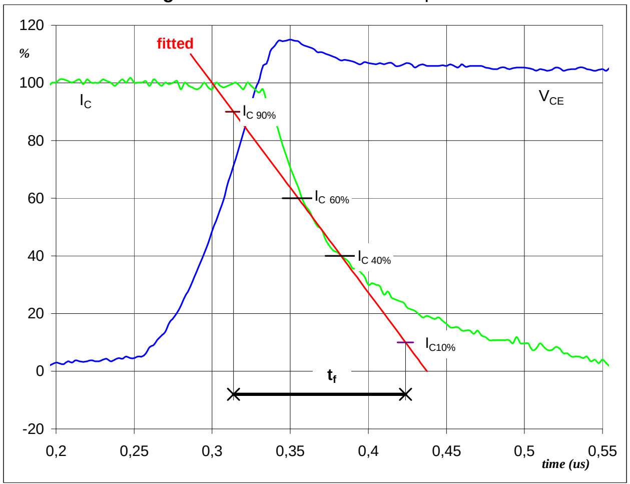
Turn-on Switching Waveforms & definition of tdon, t_{Eon}
(t_{Eon} = integrating time for E_{on})



V_{GE} (0%) =	-15	V
V_{GE} (100%) =	15	V
V_C (100%) =	300	V
I_C (100%) =	99	A
t_{don} =	0,20	µs
t_{Eon} =	0,40	µs

Figure 3 Output inverter IGBT

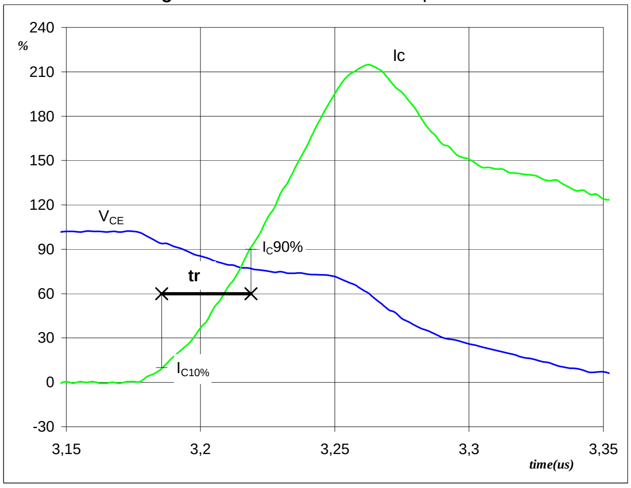
Turn-off Switching Waveforms & definition of t_f



V_C (100%) =	300	V
I_C (100%) =	99	A
t_f =	0,10	µs

Figure 4 Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

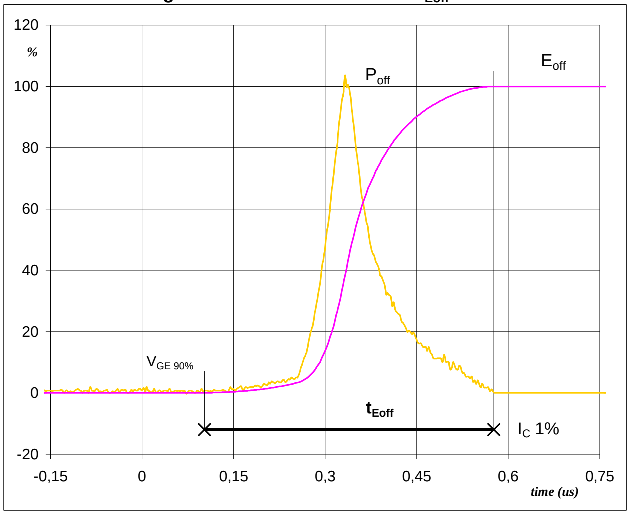


V_C (100%) =	300	V
I_C (100%) =	99	A
t_r =	0,03	µs

Switching Definitions Output Inverter

Figure 5 Output inverter IGBT

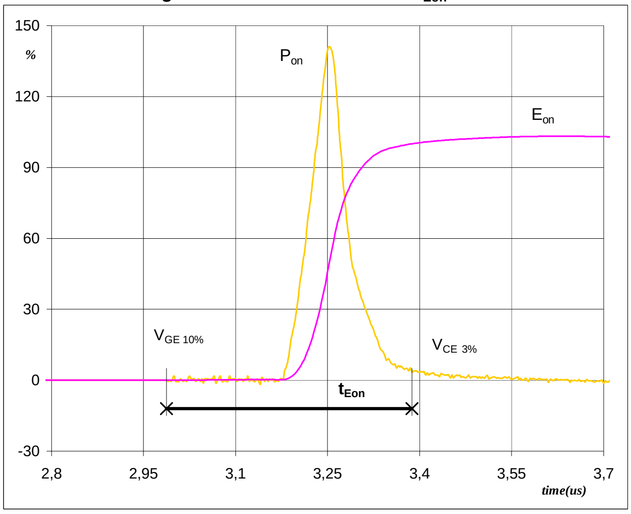
Turn-off Switching Waveforms & definition of t_{Eoff}



P_{off} (100%) =	29,71	kW
E_{off} (100%) =	3,07	mJ
t_{Eoff} =	0,47	μs

Figure 6 Output inverter IGBT

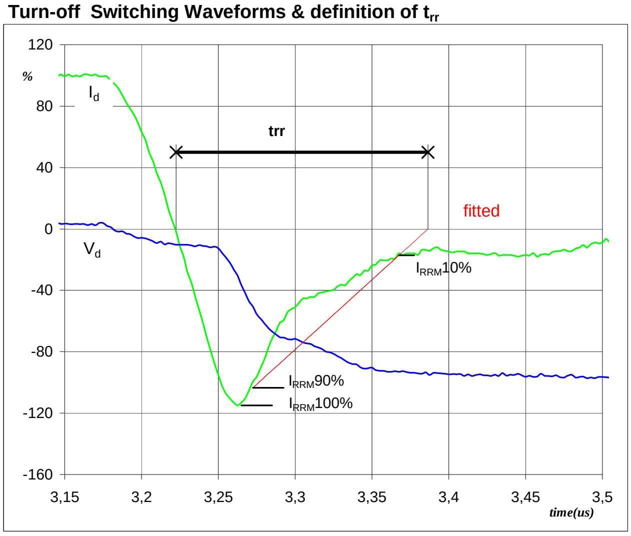
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) =	29,71	kW
E_{on} (100%) =	3,11	mJ
t_{Eon} =	0,40	μs

Figure 7 Output inverter FWD

Turn-off Switching Waveforms & definition of t_{rr}

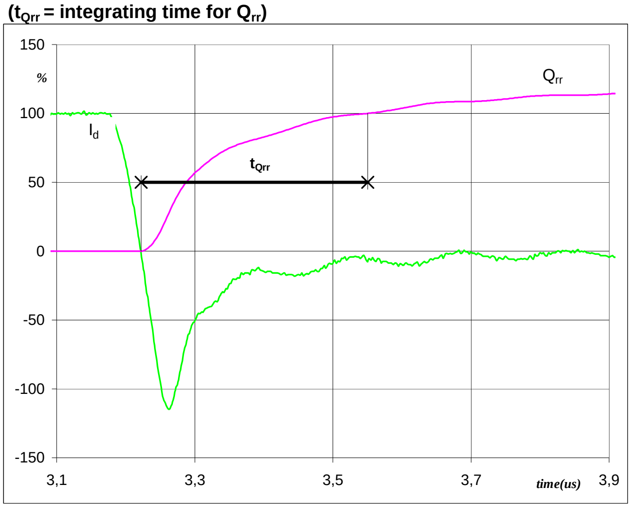


V_d (100%) =	300	V
I_d (100%) =	99	A
I_{RRM} (100%) =	-114	A
t_{rr} =	0,29	μs

Switching Definitions Output Inverter

Figure 8 Output inverter FWD

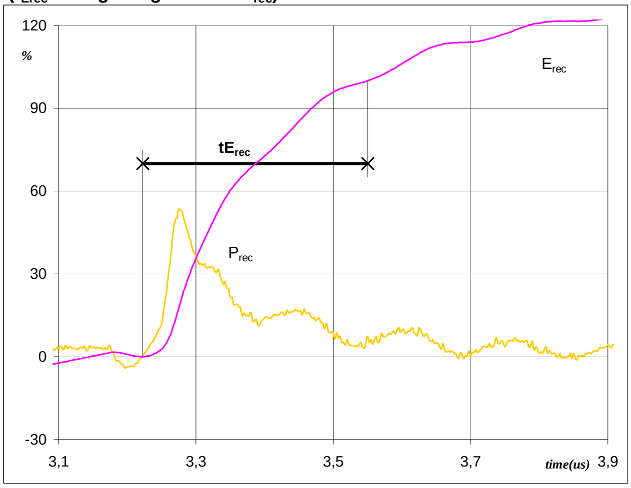
Turn-on Switching Waveforms & definition of t_{Qrr}
(t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	99	A
Q_{rr} (100%) =	11,43	µC
t_{Qrr} =	0,33	µs

Figure 9 Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
(t_{Erec}= integrating time for E_{rec})



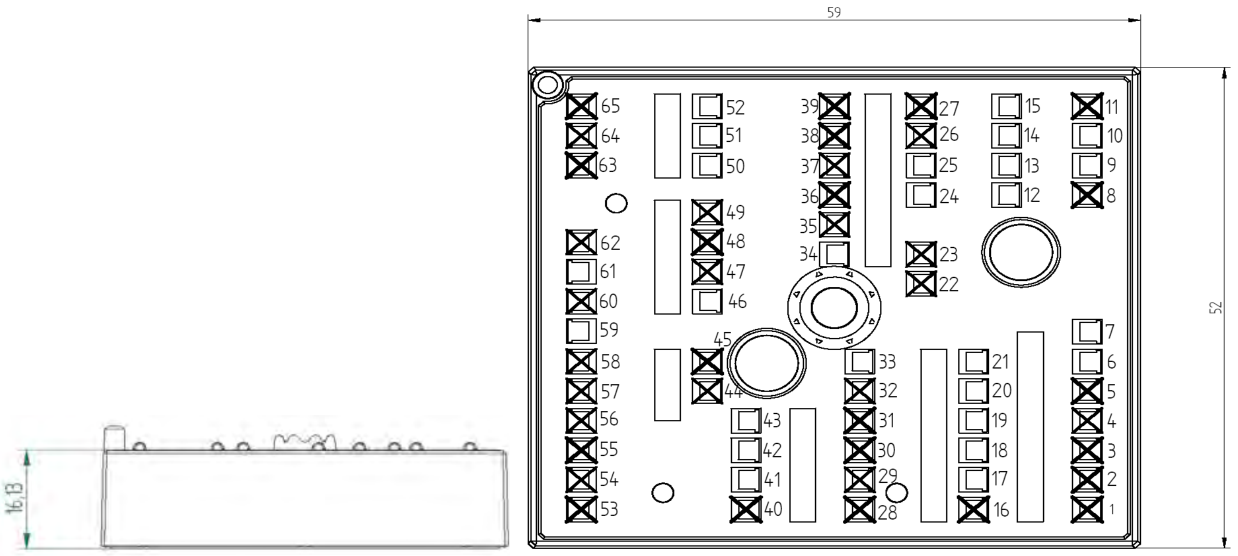
P_{rec} (100%) =	29,71	kW
E_{rec} (100%) =	2,20	mJ
t_{Erec} =	0,33	µs

Ordering Code and Marking - Outline - Pinout

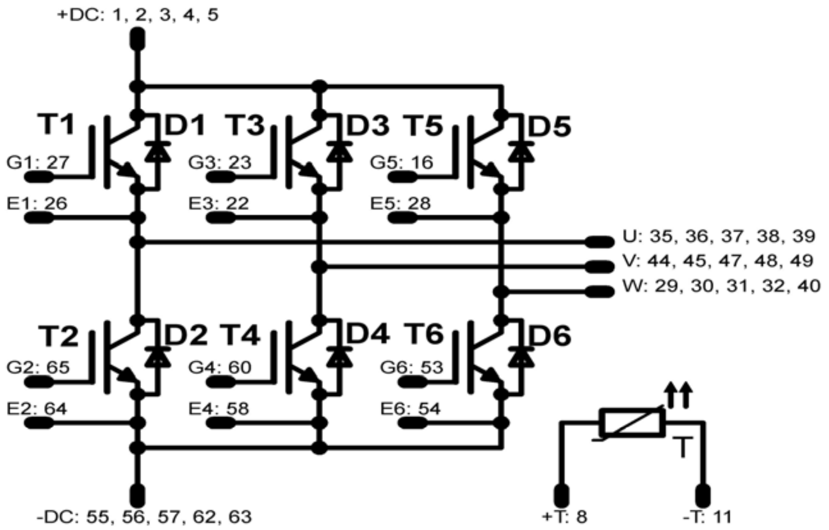
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K22-T-PM)	V23990-K305-F-/1A/	K305-F	K305-F
with std lid (black V23990-K22-T-PM) and P12	V23990-K305-F-/1B/	K305-F	K305-F
with thin lid (white V23990-K23-T-PM)	V23990-K305-F-/0A/	K305-F	K305-F
with thin lid (white V23990-K23-T-PM) and P12	V23990-K305-F-/0B/	K305-F	K305-F

Outline



Pinout



DISCLAIMER

The information given in this datasheet describes the type of component and does not represent assured characteristics. For tested values please contact Vincotech.Vincotech reserves the right to make changes without further notice to any products herein to improve reliability, function or design. Vincotech does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights, nor the rights of others.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.