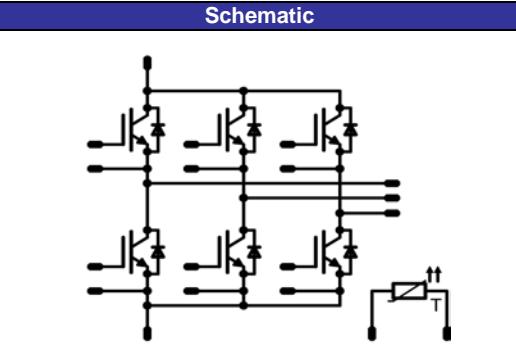


MiniSKiiP®2 PACK		1200V/35A
Features	• Solderless interconnection • Trench Fieldstop IGBT ⁴ technology	MiniSKiiP®2 housing 
Target Applications	• Servo Drives • Industrial Motor Drives • UPS	Schematic 
Types	• V23990-K238-F40-PM	

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
T1,T2,T3,T4,T5,T6				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	41	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j\max}$	105	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$, $T_j \leq T_{op\max}$	70	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	112	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

D1,D2,D3,D4,D5,D6

Peak Repetitive Reverse Voltage	V_{RRM}		1200	V
DC forward current	I_F	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	33	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_{j\max}$	66	A
Power dissipation per Diode	P_{tot}	$T_j=T_{j\max}$ $T_h=80^\circ\text{C}$	77	W
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{op}		-40...+125	°C

Insulation Properties

Insulation voltage	V_{is}	t=2s	DC voltage	4000	V
Creepage distance				min 12.7	mm
Clearance				min 12.7	mm

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit	
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	

T1,T2,T3,T4,T5,T6

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0,0012	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,6	1,88 2,32	2,2	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0,06	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			120	nA
Integrated Gate resistor	R_{gint}							none		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=16\Omega$ $R_{gon}=16\Omega$	± 15	600	35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	96			ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	22			
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	218	291		
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	92			
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,95	3,07		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,95	3,28		
Input capacitance	C_{ies}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		1950		pF
Output capacitance	C_{oss}							155		
Reverse transfer capacitance	C_{rss}							115		
Gate charge	Q_{Gate}		± 15			$T_j=25^\circ\text{C}$		270		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=1\text{W/mK}$						0,85		K/W

D1,D2,D3,D4,D5,D6

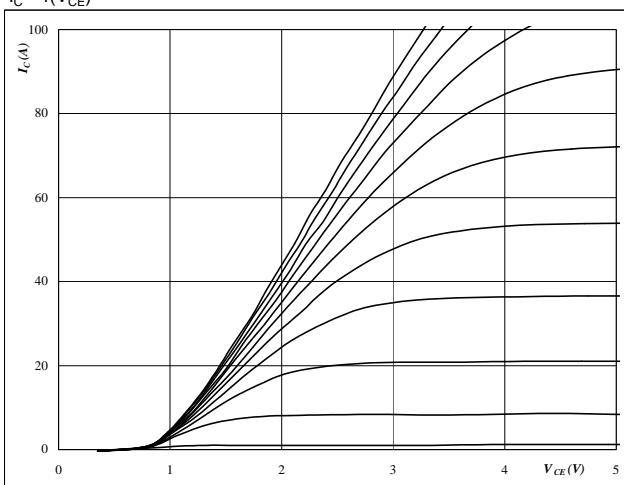
Diode forward voltage	V_F				35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1,3	2,34 2,32	2,7	V
Peak reverse recovery current	I_{RRM}	$R_{gon}=16\Omega$	± 15	600	35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		30,3 40,2		A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	126 292			ns
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	2,06 5,34			μC
Peak rate of fall of recovery current	$di(rec)\max /dt$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1074 257			$\text{A}/\mu\text{s}$
Reverse recovered energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	0,69 2,07			mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=1\text{W/mK}$						1,2		K/W

Thermistor

Rated resistance	R					$T=25^\circ\text{C}$		1000		Ω
Deviation of R100	$\Delta R/R$	$R_{100}=1670\ \Omega$				$T=100^\circ\text{C}$	-3		3	%
R100	P					$T=100^\circ\text{C}$		1670,313		Ω
A-value	B(25/50)	Tol. %				$T=25^\circ\text{C}$		7,635*10-3		$1/\text{K}$
B-value	B(25/100)	Tol. %				$T=25^\circ\text{C}$		1,731*10-5		$1/\text{K}^2$
Vincotech NTC Reference									E	

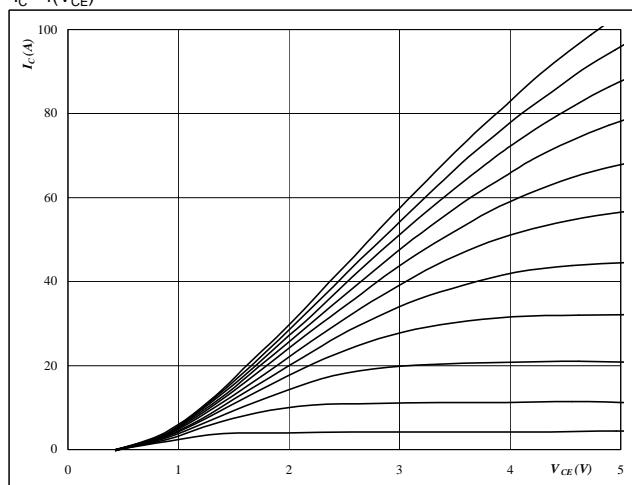
T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



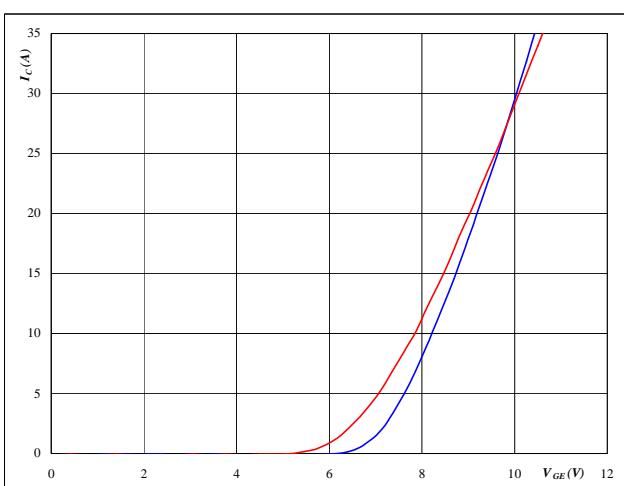
At
 $t_p = 350 \mu s$
 $T_j = 25^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



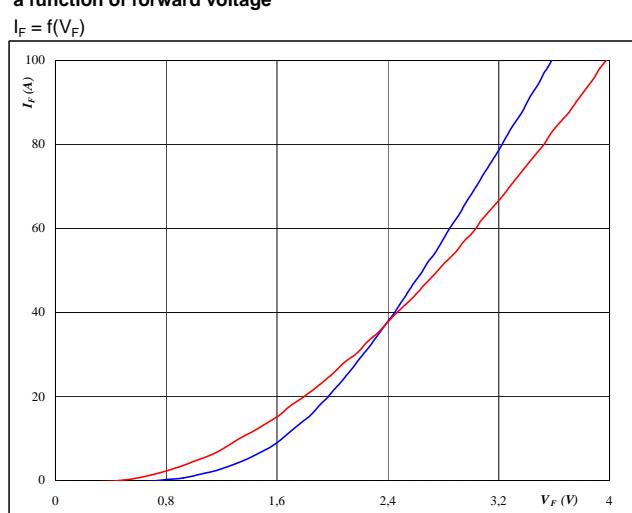
At
 $t_p = 350 \mu s$
 $T_j = 150^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$



At
 $T_j = 25/150^\circ C$
 $t_p = 350 \mu s$
 $V_{CE} = 10 V$

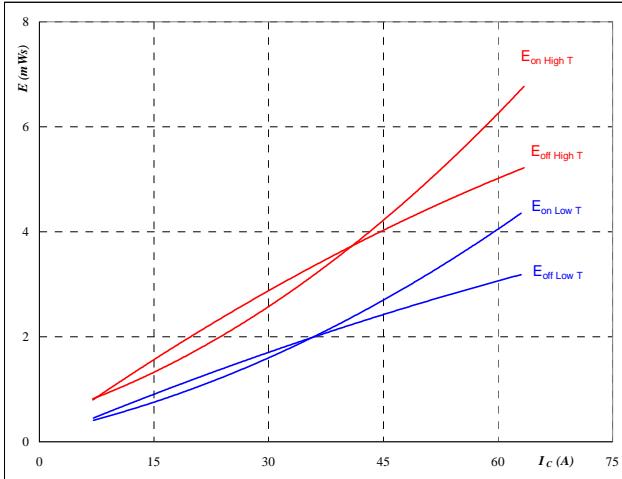
Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



At
 $t_p = 350 \mu s$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 5
T1,T2,T3,T4,T5,T6 IGBT
**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$

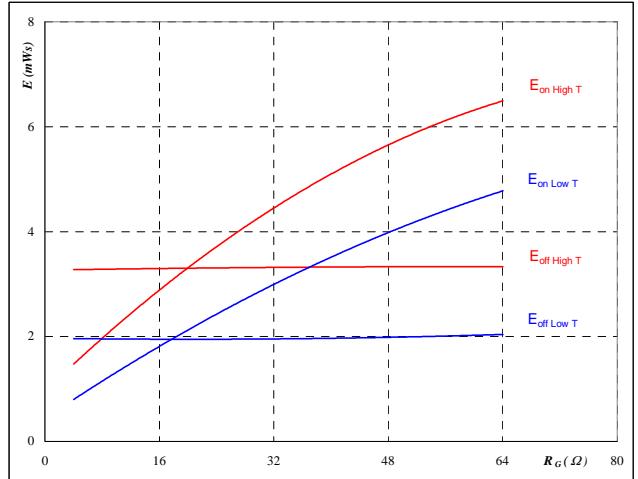


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 16 \quad \Omega \\ R_{goff} &= 16 \quad \Omega \end{aligned}$$

Figure 6
T1,T2,T3,T4,T5,T6 IGBT
**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$

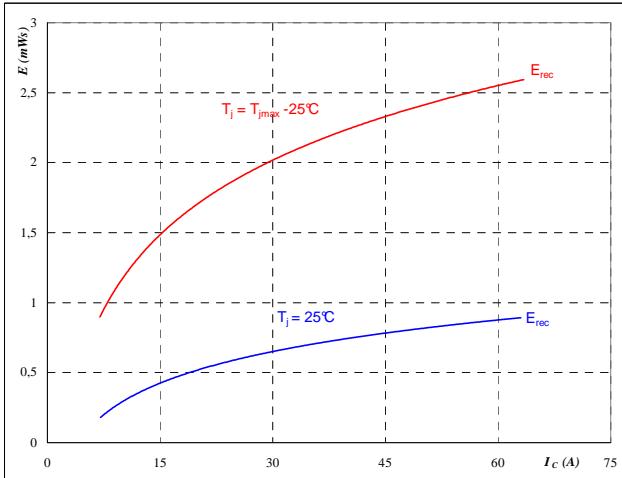


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 35 \quad A \end{aligned}$$

Figure 7
D1,D2,D3,D4,D5,D6 FWD
**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$

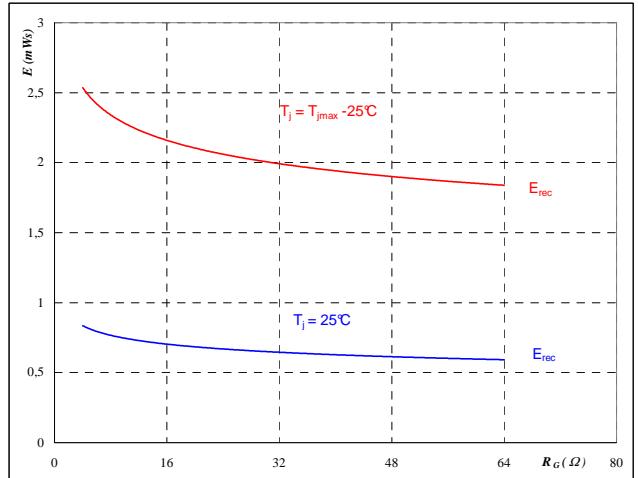


With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

Figure 8
D1,D2,D3,D4,D5,D6 FWD
**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ C \\ V_{CE} &= 600 \quad V \\ V_{GE} &= \pm 15 \quad V \\ I_C &= 35 \quad A \end{aligned}$$

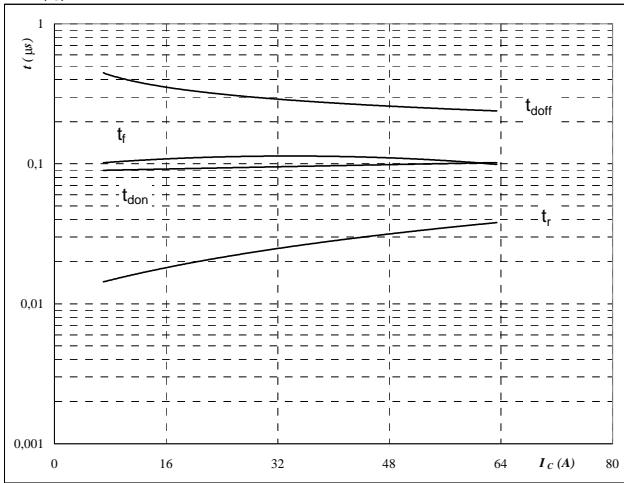
T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 9

T1,T2,T3,T4,T5,T6 IGBT

Typical switching times as a function of collector current

$t = f(I_C)$



With an inductive load at

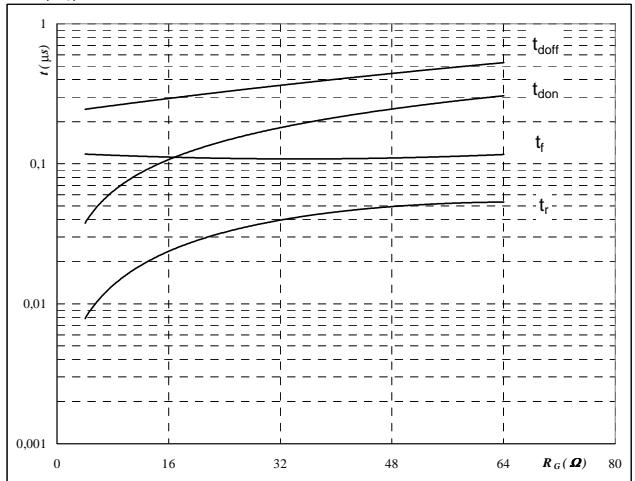
$$\begin{aligned} T_j &= 150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \\ R_{goff} &= 16 \quad \Omega \end{aligned}$$

Figure 10

T1,T2,T3,T4,T5,T6 IGBT

Typical switching times as a function of gate resistor

$t = f(R_G)$



With an inductive load at

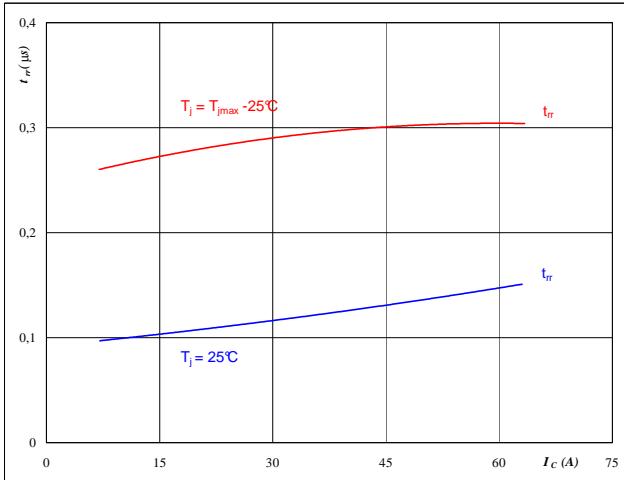
$$\begin{aligned} T_j &= 150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 35 \quad \text{A} \end{aligned}$$

Figure 11

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of collector current

$t_{rr} = f(I_C)$



At

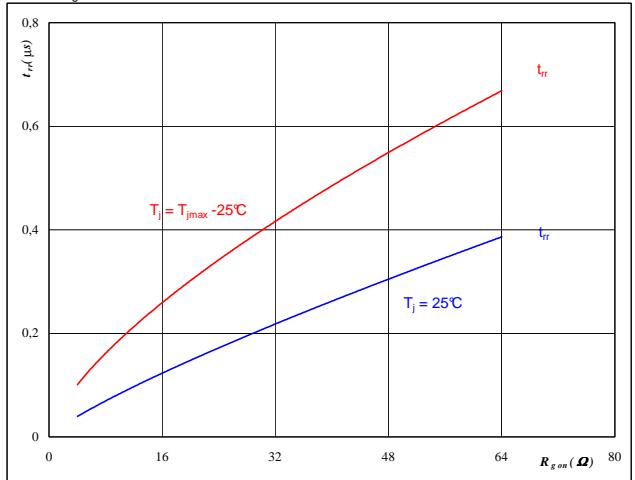
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

Figure 12

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$t_{rr} = f(R_{gon})$



At

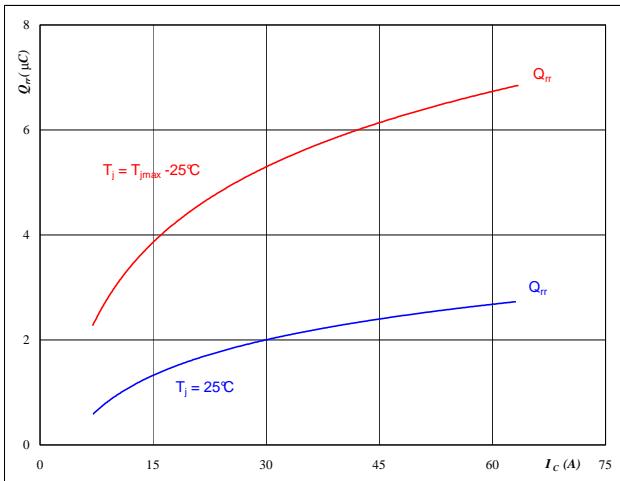
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 35 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 13

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


At

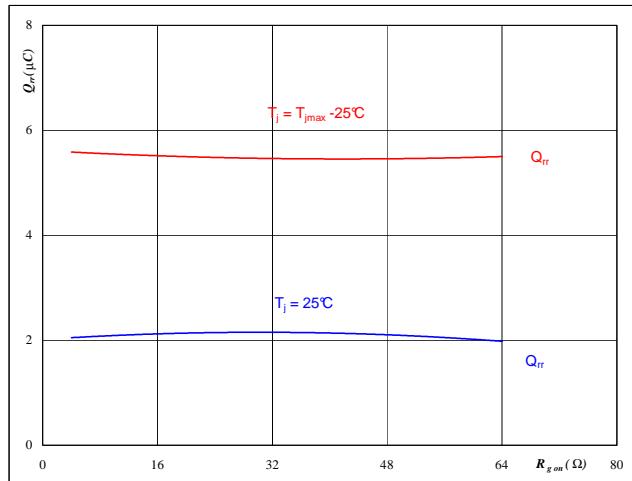
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

Figure 14

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


At

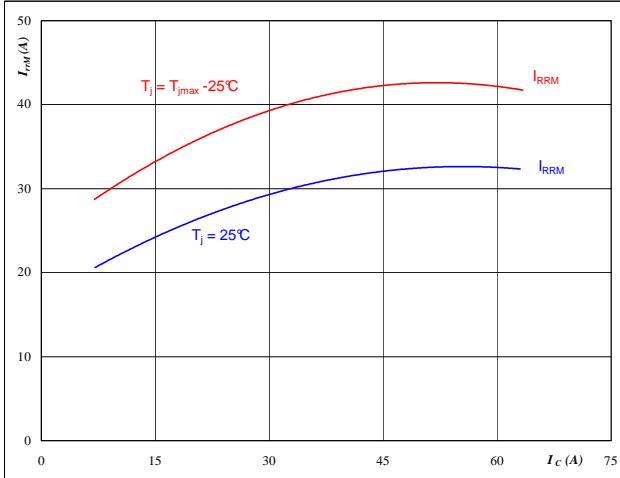
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 35 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

Figure 15

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


At

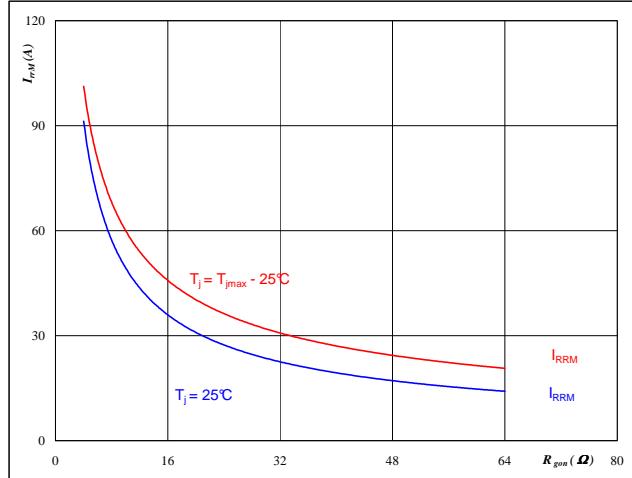
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

Figure 16

D1,D2,D3,D4,D5,D6 FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

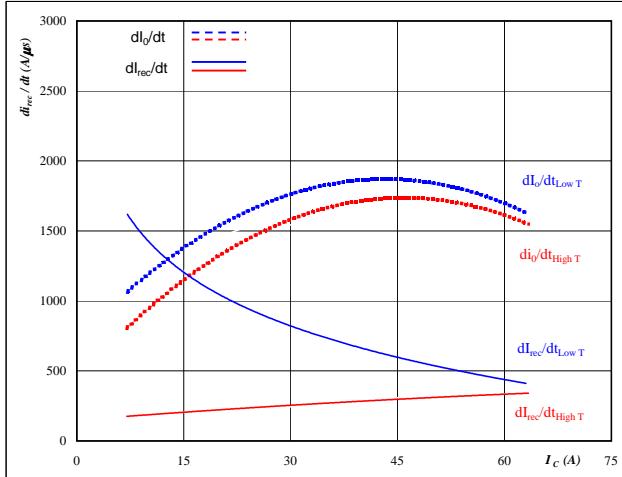
$$I_{RRM} = f(R_{gon})$$


At

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_R &= 600 \quad \text{V} \\ I_F &= 35 \quad \text{A} \\ V_{GE} &= \pm 15 \quad \text{V} \end{aligned}$$

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 17

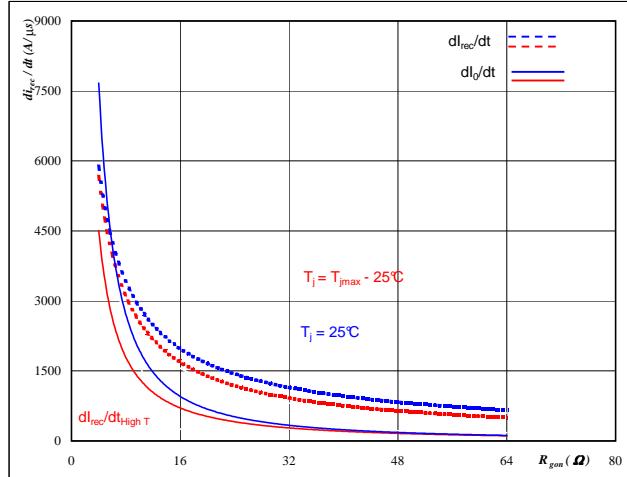
Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	16	Ω

D1,D2,D3,D4,D5,D6 FWD
Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

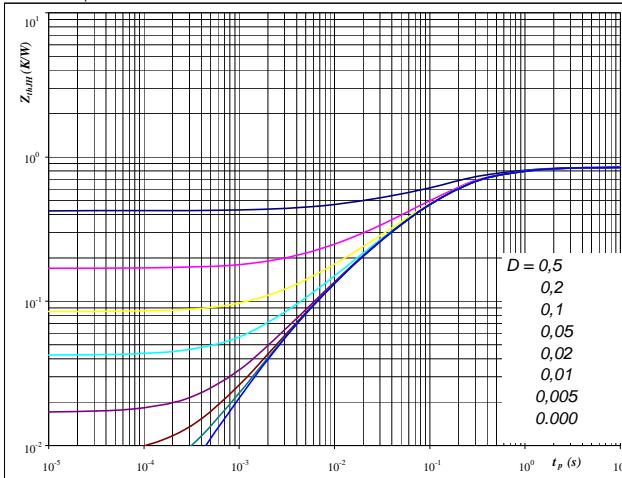

At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	35	A
$V_{GE} =$	±15	V

Figure 19
T1,T2,T3,T4,T5,T6 IGBT

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$D =$	t_p / T
$R_{thJH} =$	0,85 K/W

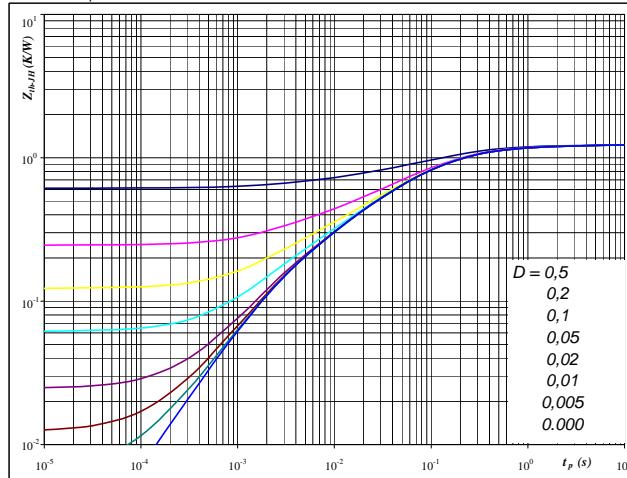
IGBT thermal model values

R (C/W)	Tau (s)
0,09	1,5E+00
0,26	2,7E-01
0,35	8,9E-02
0,11	1,4E-02
0,03	2,8E-03

Figure 20
D1,D2,D3,D4,D5,D6 FWD

FWD transient thermal impedance
as a function of pulse width

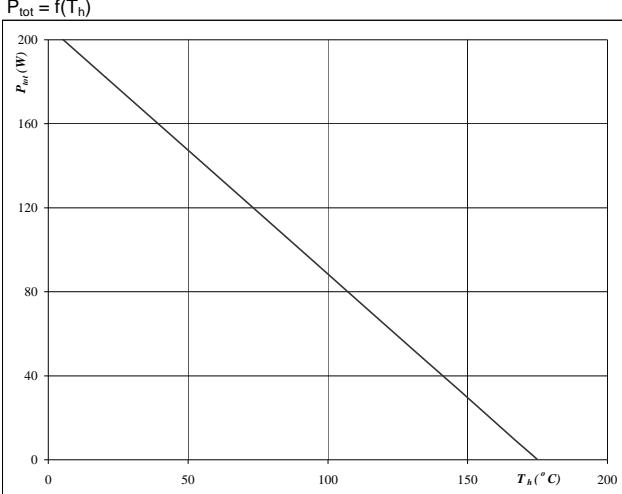
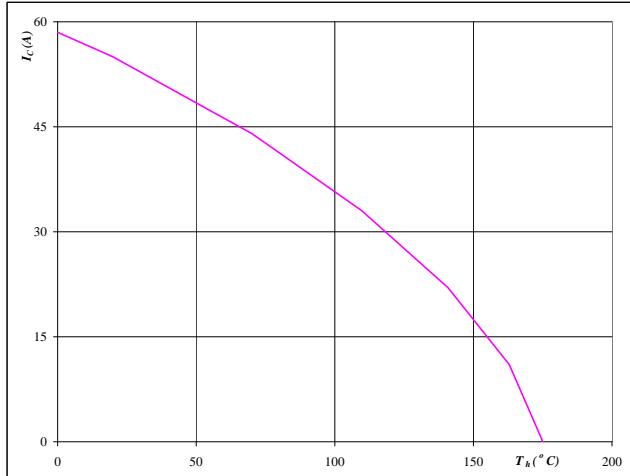
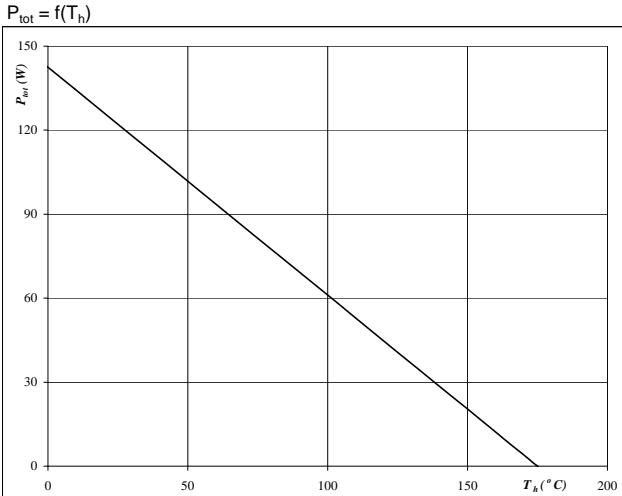
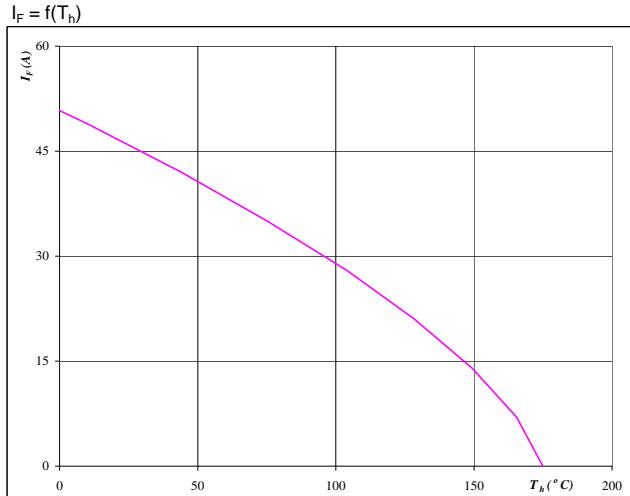
$$Z_{thJH} = f(t_p)$$


At

$D =$	t_p / T
$R_{thJH} =$	1,2 K/W

FWD thermal model values

R (C/W)	Tau (s)
0,08	2,1E+00
0,33	2,4E-01
0,50	6,6E-02
0,22	1,3E-02
0,10	2,3E-03

T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6
Figure 21
T1,T2,T3,T4,T5,T6 IGBT
Power dissipation as a function of heatsink temperature
 $P_{\text{tot}} = f(T_h)$

At
 $T_j = 175 \text{ } ^\circ\text{C}$
Figure 22
T1,T2,T3,T4,T5,T6 IGBT
Collector current as a function of heatsink temperature
 $I_C = f(T_h)$

At
 $T_j = 175 \text{ } ^\circ\text{C}$
 $V_{\text{GE}} = 15 \text{ } \text{V}$
Figure 23
D1,D2,D3,D4,D5,D6 FWD
Power dissipation as a function of heatsink temperature
 $P_{\text{tot}} = f(T_h)$

At
 $T_j = 175 \text{ } ^\circ\text{C}$
Figure 24
D1,D2,D3,D4,D5,D6 FWD
Forward current as a function of heatsink temperature
 $I_F = f(T_h)$

At
 $T_j = 175 \text{ } ^\circ\text{C}$

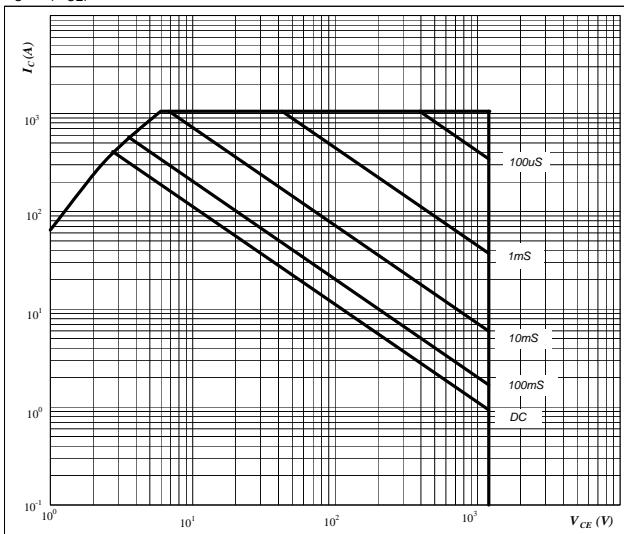
T1,T2,T3,T4,T5,T6/D1,D2,D3,D4,D5,D6

Figure 25

T1,T2,T3,T4,T5,T6 IGBT

**Safe operating area as a function
of collector-emitter voltage**

$$I_C = f(V_{CE})$$


At

D = single pulse

T_h = 80 °C

V_GE = ±15 V

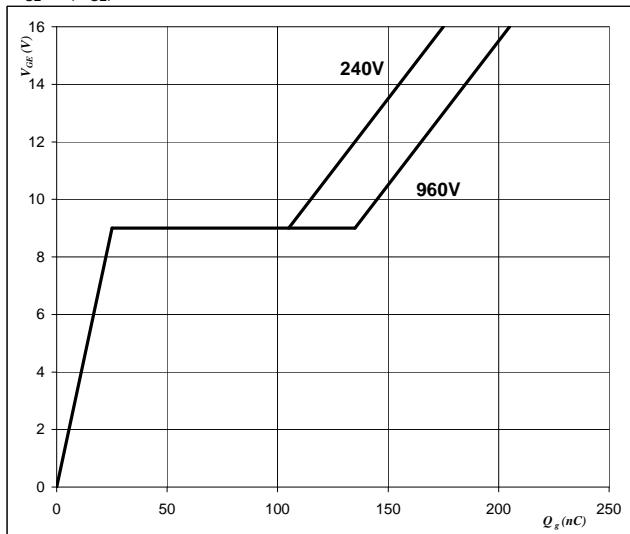
T_j = T_jmax °C

Figure 26

T1,T2,T3,T4,T5,T6 IGBT

Gate voltage vs Gate charge

$$V_{GE} = f(Q_{GE})$$


At

I_C = 35 A

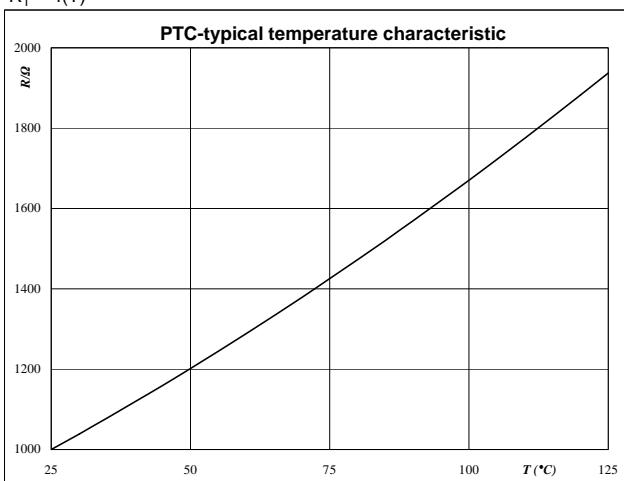
Thermistor

Figure 1

Thermistor

Typical PTC characteristic
as a function of temperature

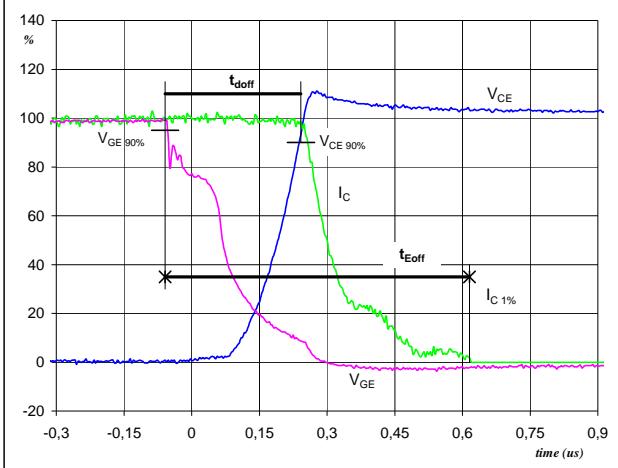
$$R_T = f(T)$$



Switching Definitions Output Inverter

General conditions

T_j	=	150 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

Figure 1
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$


$$V_{GE}(0\%) = -15 \text{ V}$$

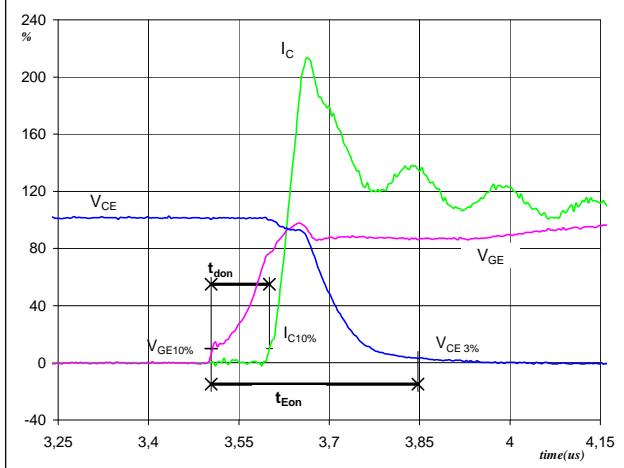
$$V_{GE}(100\%) = 15 \text{ V}$$

$$V_C(100\%) = 600 \text{ V}$$

$$I_C(100\%) = 35 \text{ A}$$

$$t_{doff} = 0,29 \mu\text{s}$$

$$t_{Eoff} = 0,67 \mu\text{s}$$

Figure 2
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$


$$V_{GE}(0\%) = -15 \text{ V}$$

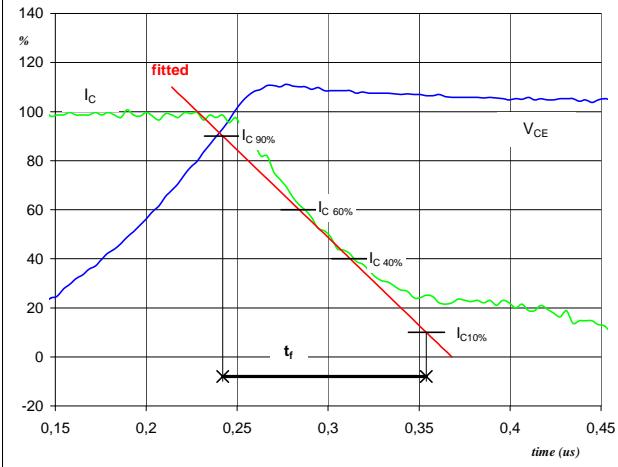
$$V_{GE}(100\%) = 15 \text{ V}$$

$$V_C(100\%) = 600 \text{ V}$$

$$I_C(100\%) = 35 \text{ A}$$

$$t_{don} = 0,10 \mu\text{s}$$

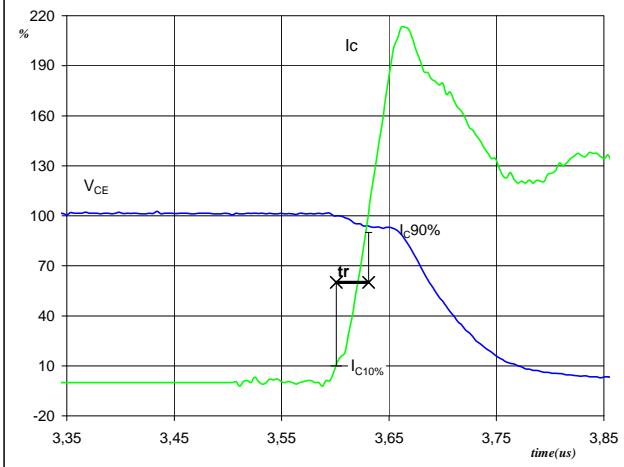
$$t_{Eon} = 0,34 \mu\text{s}$$

Figure 3
Output inverter IGBT
Turn-off Switching Waveforms & definition of t_f


$$V_C(100\%) = 600 \text{ V}$$

$$I_C(100\%) = 35 \text{ A}$$

$$t_f = 0,11 \mu\text{s}$$

Figure 4
Output inverter IGBT
Turn-on Switching Waveforms & definition of t_r


$$V_C(100\%) = 600 \text{ V}$$

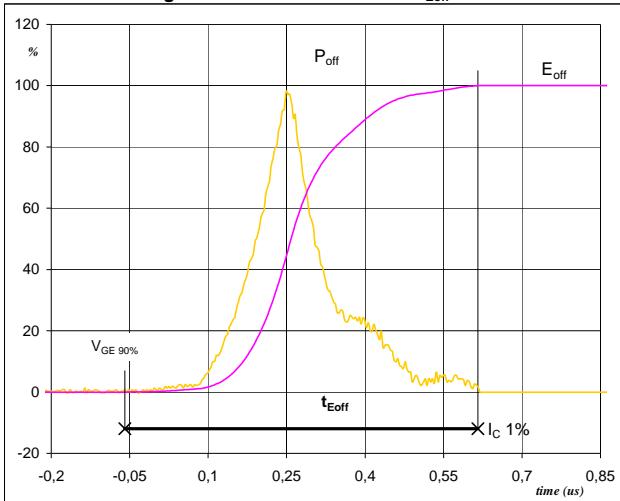
$$I_C(100\%) = 35 \text{ A}$$

$$t_r = 0,03 \mu\text{s}$$

Switching Definitions Output Inverter

Figure 5

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


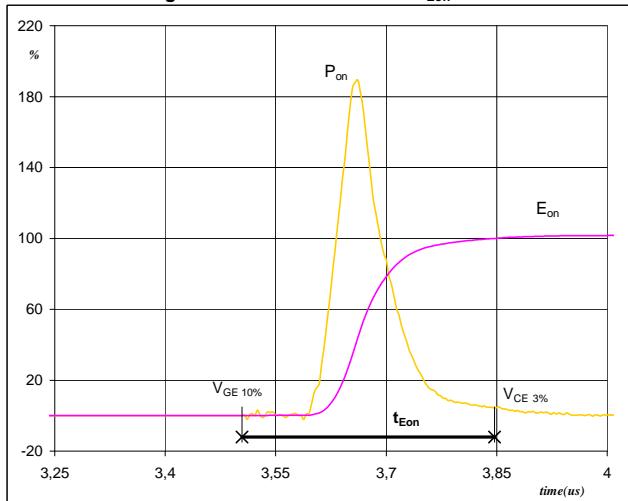
$P_{off\ (100\%)} = 21,03 \text{ kW}$

$E_{off\ (100\%)} = 3,28 \text{ mJ}$

$t_{Eoff} = 0,67 \mu s$

Figure 6

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{Eon}


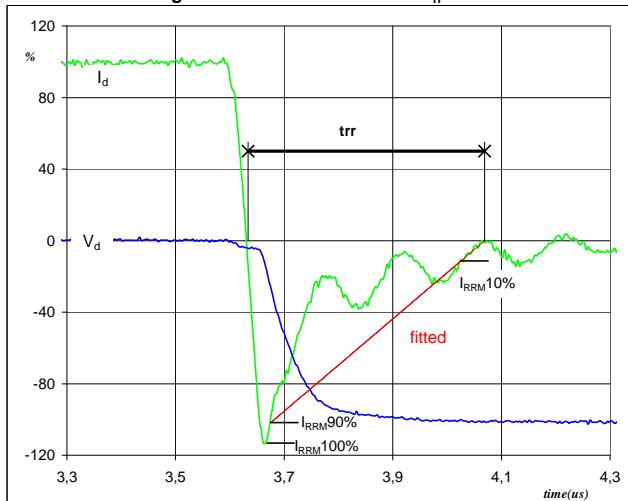
$P_{on\ (100\%)} = 21,03 \text{ kW}$

$E_{on\ (100\%)} = 3,07 \text{ mJ}$

$t_{Eon} = 0,34 \mu s$

Figure 7

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{trr}


$V_d\ (100\%) = 600 \text{ V}$

$I_d\ (100\%) = 35 \text{ A}$

$I_{RRM}\ (100\%) = -40 \text{ A}$

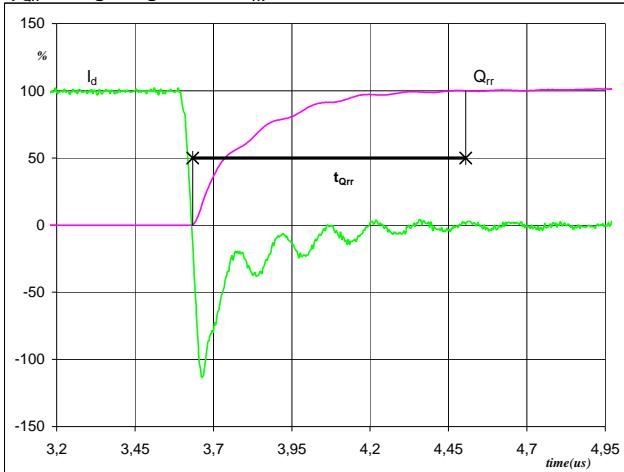
$t_{trr} = 0,29 \mu s$

Switching Definitions Output Inverter

Figure 8

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

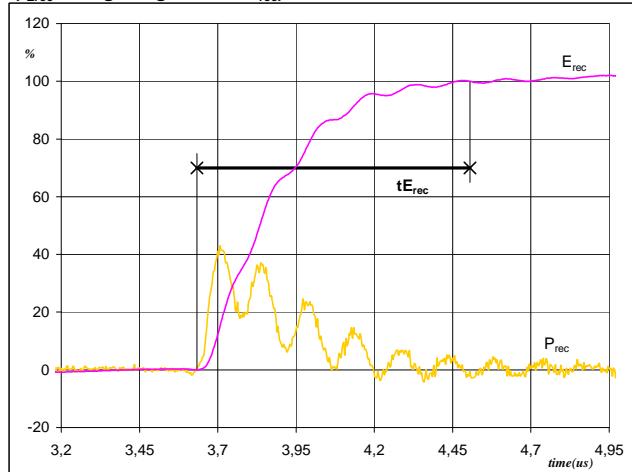


$I_d(100\%) = 35 \text{ A}$
 $Q_{rr}(100\%) = 5,34 \mu\text{C}$
 $t_{Qrr} = 0,87 \mu\text{s}$

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



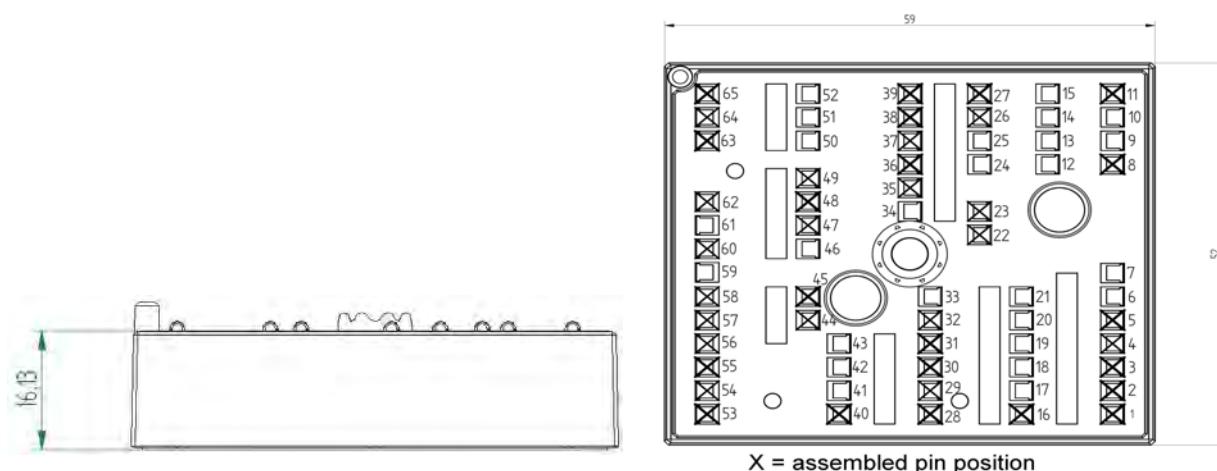
$P_{rec}(100\%) = 21,03 \text{ kW}$
 $E_{rec}(100\%) = 2,07 \text{ mJ}$
 $t_{Erec} = 0,87 \mu\text{s}$

Ordering Code and Marking - Outline - Pinout

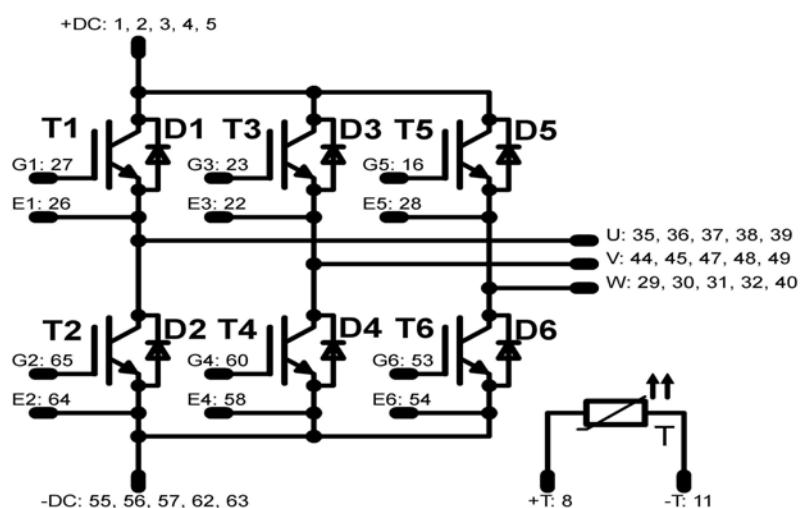
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K22-T-PM)	V23990-K238-F40-/0A-/PM	K238F40	K238F40-/0A/
with std lid (black V23990-K22-T-PM) and P12	V23990-K238-F40-/1A-/PM	K238F40	K238F40-/1A/
with thin lid (white V23990-K23-T-PM)	V23990-K238-F40-/0B-/PM	K238F40	K238F40-/0B/
with thin lid (white V23990-K23-T-PM) and P12	V23990-K238-F40-/1B-/PM	K238F40	K238F40-/1B/

Outline



Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.