



Vincotech

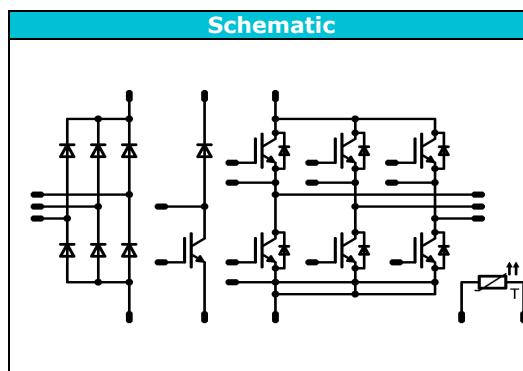
V23990-K220-A-PM

**MiniSKiiP® 2 PIM****1200 V / 35 A**

Features
<ul style="list-style-type: none"> <li>• Solderless interconnection</li> <li>• Trench Fieldstop technology</li> </ul>



Target Applications
<ul style="list-style-type: none"> <li>• Industrial Motor Drives</li> </ul>



Types
<ul style="list-style-type: none"> <li>• V23990-K220-A-PM</li> </ul>

**Maximum Ratings** $T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>D8,D9,D10,D11,D12,D13</b>				
Repetitive peak reverse voltage	$V_{RRM}$		1600	V
DC forward current	$I_{FAV}$	$T_j=T_{jmax}$	40	A
Surge forward current	$I_{FSM}$		370	A
I <sup>2</sup> t-value	$I^2t$	$t_p=10\text{ms}$ $T_j=25^\circ\text{C}$	680	$\text{A}^2\text{s}$
Power dissipation	$P_{tot}$	$T_j=T_{jmax}$	56	W
Maximum Junction Temperature	$T_{jmax}$		150	$^\circ\text{C}$

**T1,T2,T3,T4,T5,T6,T7**

Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{jmax}$	41	A
Pulsed collector current	$I_{Cpulse}$	$t_p$ limited by $T_{jmax}$	105	A
Turn off safe operating area		$V_{CE} \leq 1200\text{V}$ , $T_j \leq T_{jmax}$	105	A
Power dissipation	$P_{tot}$	$T_j=T_{jmax}$	93	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{SC}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 $< 1200$	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{jmax}$		150	$^\circ\text{C}$



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V23990-K220-A-PM

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
<b>D1,D2,D3,D4,D5,D6,D7</b>				
Peak Repetitive Reverse Voltage	$V_{RRM}$		1200	V
DC forward current	$I_F$	$T_j=T_{j\max}$	27	A
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_{j\max}$	200	A
Power dissipation	$P_{tot}$	$T_j=T_{j\max}$	47	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$

## Thermal Properties

Storage temperature	$T_{stg}$		-40...+125	$^\circ\text{C}$
Operation temperature under switching condition	$T_{op}$		-40...+125	$^\circ\text{C}$

## Insulation Properties

Insulation voltage	$V_{is}$	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	



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V23990-K220-A-PM

### Characteristic Values

Parameter	Symbol	Conditions				Value			Unit
		$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	

#### D8,D9,D10,D11,D12,D13

Forward voltage	$V_F$			25	$T_j=25^\circ C$ $T_j=125^\circ C$	1,09 1,05	1,19 1,14	1,31 1,26	V
Threshold voltage (for power loss calc. only)	$V_{to}$			25	$T_j=25^\circ C$ $T_j=125^\circ C$	0,82 0,71	0,9 0,77	0,96 0,83	V
Slope resistance (for power loss calc. only)	$r_t$			25	$T_j=25^\circ C$ $T_j=125^\circ C$	9 10	13 15	17 20	$m\Omega$
Reverse current	$I_r$		1600		$T_j=25^\circ C$ $T_j=125^\circ C$			0,1	mA
Thermal resistance chip to heatsink	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$					1,25		K/W

#### T1,T2,T3,T4,T5,T6,T7

Gate emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=V_{GE}$		0,0015	$T_j=25^\circ C$ $T_j=125^\circ C$	5	5,8	6,5	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		15	35	$T_j=25^\circ C$ $T_j=125^\circ C$		1,75 1,95		V
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200	$T_j=25^\circ C$ $T_j=125^\circ C$			0,4 4	mA
Gate-emitter leakage current	$I_{GES}$		20	0	$T_j=25^\circ C$ $T_j=125^\circ C$			600	nA
Integrated Gate resistor	$R_{gint}$						6		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=28 \Omega$ $R_{gon}=28 \Omega$	$\pm 15$	600	35	$T_j=25^\circ C$ $T_j=125^\circ C$		80	ns
Rise time	$t_r$					$T_j=25^\circ C$ $T_j=125^\circ C$		29	
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ C$ $T_j=125^\circ C$		486	
Fall time	$t_f$					$T_j=25^\circ C$ $T_j=125^\circ C$		188	
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ C$ $T_j=125^\circ C$		4,98	mWs
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ C$ $T_j=125^\circ C$		3,57	
Input capacitance	$C_{ies}$	$f=1\text{MHz}$	$0$	25		$T_j=25^\circ C$		2500	pF
Output capacitance	$C_{oss}$							130	
Reverse transfer capacitance	$C_{rss}$							110	
Gate charge	$Q_G$		$\pm 15$			$T_j=25^\circ C$		203	nC
Thermal resistance chip to heatsink	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						0,75	K/W

#### D1,D2,D3,D4,D5,D6,D7

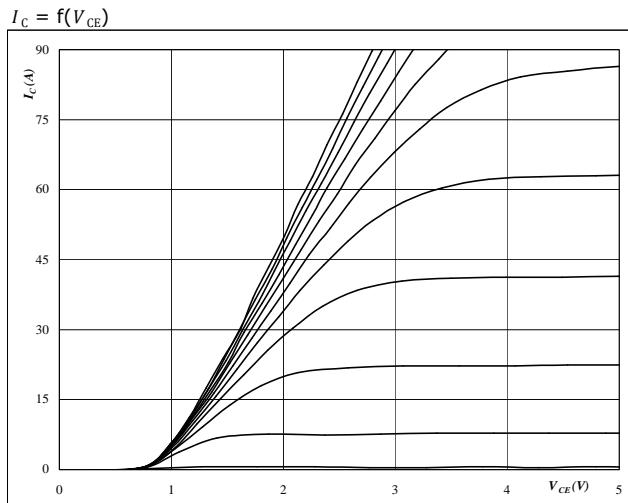
Diode forward voltage	$V_F$			28	$T_j=25^\circ C$ $T_j=125^\circ C$		1,48 1,5		V
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=28 \Omega$	$\pm 15$	600	35	$T_j=25^\circ C$ $T_j=125^\circ C$		34	A
Reverse recovery time	$t_{rr}$					$T_j=25^\circ C$ $T_j=125^\circ C$		715	ns
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ C$ $T_j=125^\circ C$		9,12	$\mu C$
Peak rate of fall of recovery current	$d(i_{rec})/\text{dt}$					$T_j=25^\circ C$ $T_j=125^\circ C$		204	$A/\mu s$
Reverse recovered energy	$E_{rec}$					$T_j=25^\circ C$ $T_j=125^\circ C$		3,66	mWs
Thermal resistance chip to heatsink	$R_{thJH}$	Thermal grease thickness≤50um $\lambda = 1 \text{ W/mK}$						1,5	K/W

### Thermistor

Rated resistance	$R$				$T=25^\circ C$		1000		$\Omega$
Deviation of R100	$\Delta R/R$	$R100=1670 \Omega$			$T=100^\circ C$	-3		3	%
R100	$R$				$T=100^\circ C$		1670,31		$\Omega$
Power dissipation constant					$T=25^\circ C$				$mW/K$
A-value	B(25/50)				$T=25^\circ C$		$7,635 \times 10^{-3}$		$1/K$
B-value	B(25/100)				$T=25^\circ C$		$1,731 \times 10^{-5}$		$1/K^2$
Vincotech NTC Reference								E	

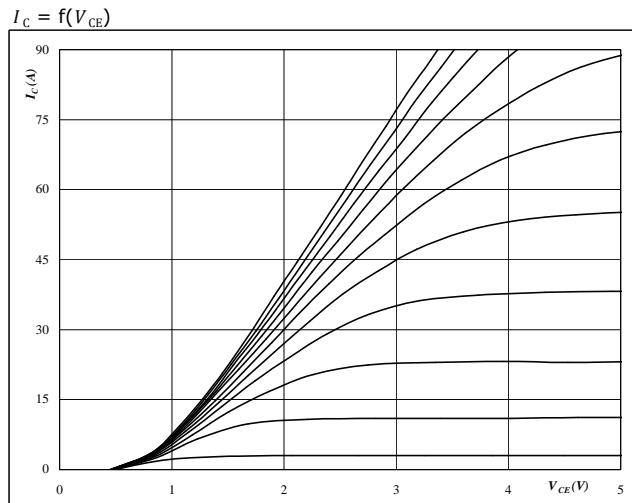
# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

**Figure 1**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



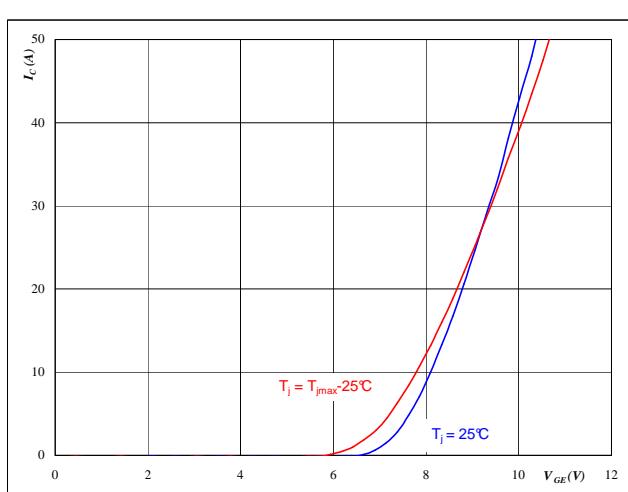
**At**  
 $t_p = 250 \mu s$   
 $T_j = 25 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
**Typical output characteristics**  
 $I_C = f(V_{CE})$



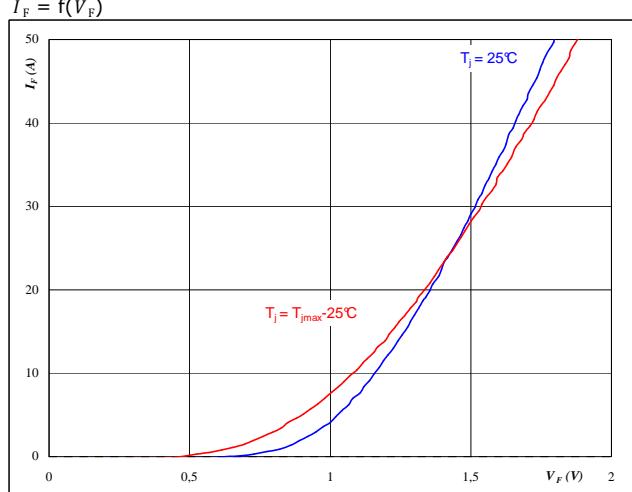
**At**  
 $t_p = 250 \mu s$   
 $T_j = 125 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
**Typical transfer characteristics**  
 $I_C = f(V_{GE})$



**At**  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

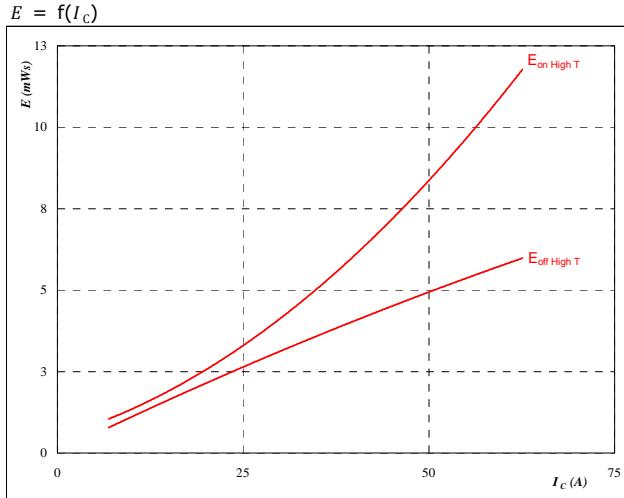
**Figure 4**  
**Typical diode forward current as a function of forward voltage**  
 $I_F = f(V_F)$



**At**  
 $t_p = 250 \mu s$

# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

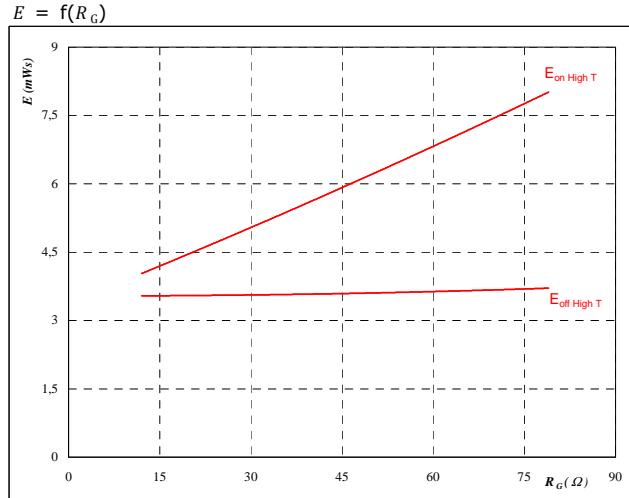
**Figure 5**  
**Typical switching energy losses  
as a function of collector current**  
 $E = f(I_C)$



With an inductive load at

$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 28 \Omega$   
 $R_{goff} = 28 \Omega$

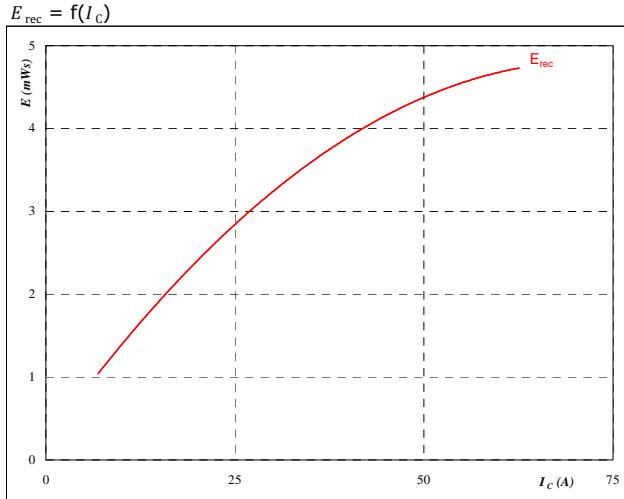
**Figure 6**  
**Typical switching energy losses  
as a function of gate resistor**  
 $E = f(R_G)$



With an inductive load at

$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 35 \text{ A}$

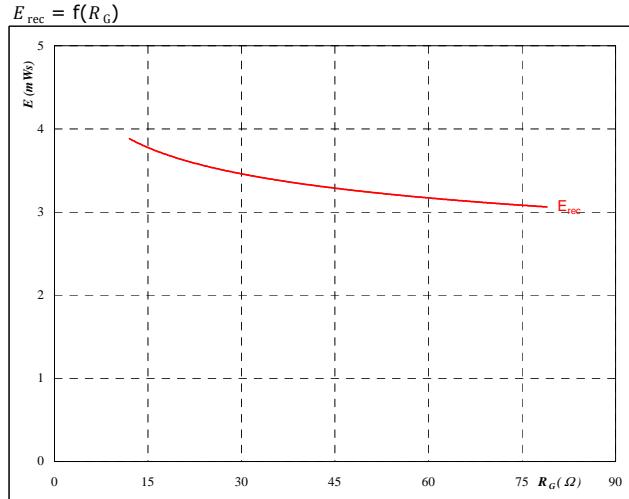
**Figure 7**  
**D1,D2,D3,D4,D5,D6,D7 FWD**  
**Typical reverse recovery energy loss  
as a function of collector current**  
 $E_{rec} = f(I_C)$



With an inductive load at

$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 28 \Omega$

**Figure 8**  
**D1,D2,D3,D4,D5,D6,D7 FWD**  
**Typical reverse recovery energy loss  
as a function of gate resistor**  
 $E_{rec} = f(R_G)$



With an inductive load at

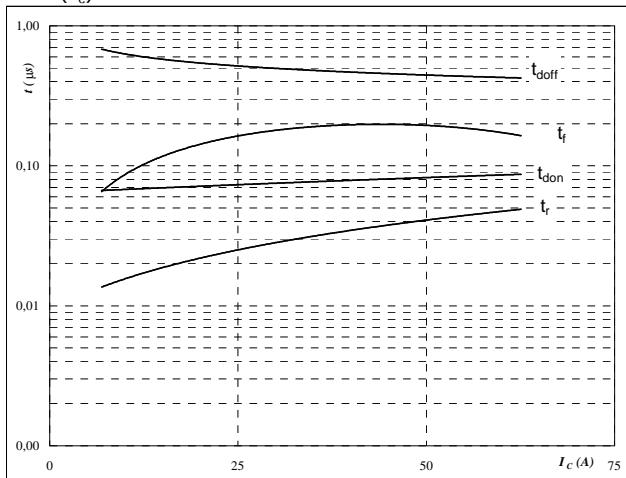
$T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $I_C = 35 \text{ A}$

## **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

**Figure 9** T1,T2,T3,T4,T5,T6,T7 IGBT

**Typical switching times as a function of collector current**

$$t = f(I_c)$$



With an inductive load at

$$T_j = 125 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

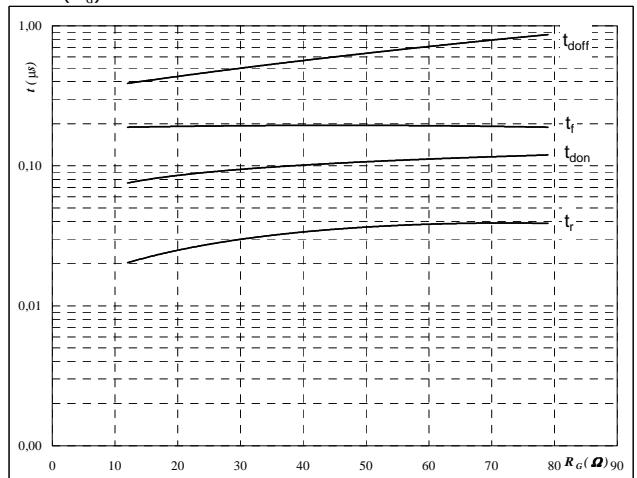
$$R_{gon} = 28 \quad \Omega$$

$$R_{goff} = 28 \quad \Omega$$

**Figure 10** T1,T2,T3,T4,T5,T6,T7 IGBT

**Typical switching times as a function of gate resistor**

$$t = f(R_G)$$



With an inductive load at

$$T_j = 125 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

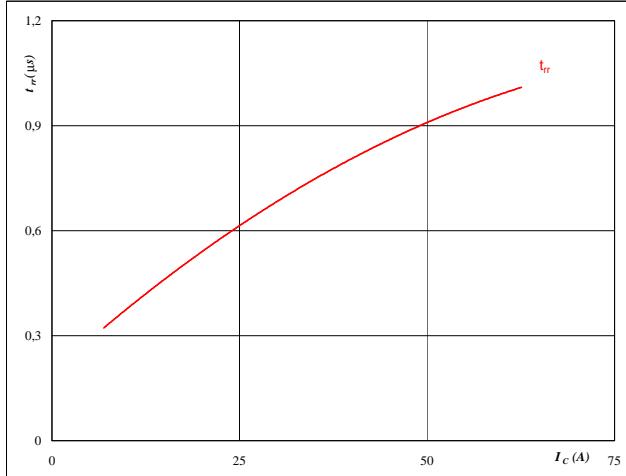
$$V_{GE} = \pm 15 \quad \text{V}$$

$$I_c = 35 \quad \text{A}$$

**Figure 11** Output inverter FWD

**Typical reverse recovery time as a function of collector current**

$$t_{rr} = f(I_c)$$



**At**

$$T_j = 125 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

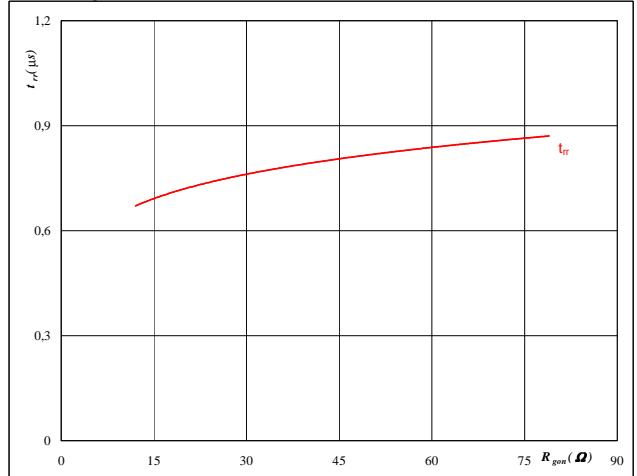
$$V_{GE} = \pm 15 \quad \text{V}$$

$$R_{gon} = 28 \quad \Omega$$

**Figure 12** Output inverter FWD

**Typical reverse recovery time as a function of IGBT turn on gate resistor**

$$t_{rr} = f(R_{gon})$$



**At**

$$T_j = 125 \quad ^\circ\text{C}$$

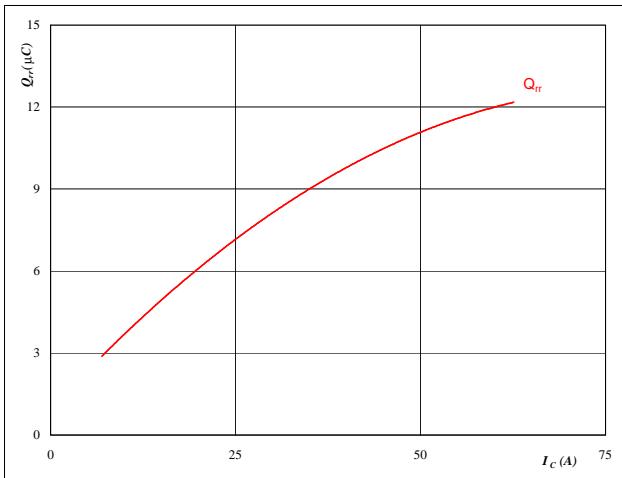
$$V_R = 600 \quad \text{V}$$

$$I_F = 35 \quad \text{A}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

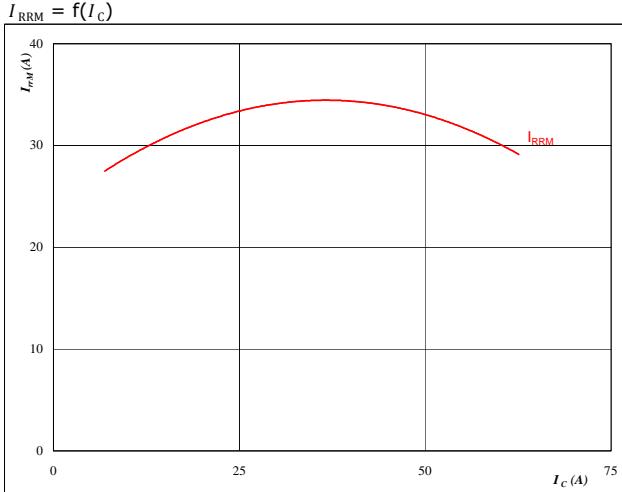
# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

**Figure 13** D1,D2,D3,D4,D5,D6,D7 FWD  
**Typical reverse recovery charge as a function of collector current**  
 $Q_{rr} = f(I_c)$



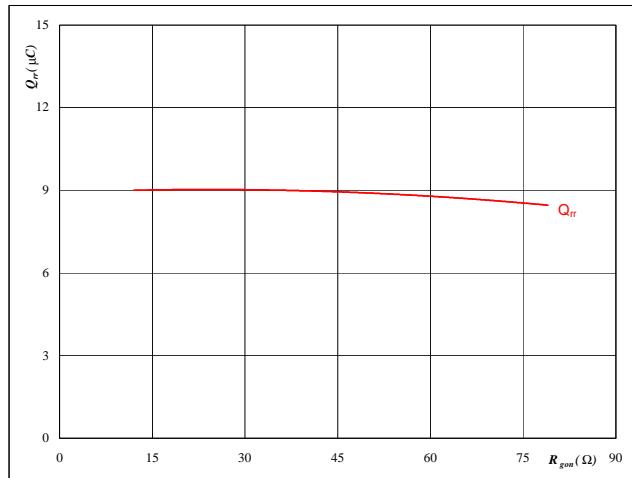
**At**  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 28 \text{ } \Omega$

**Figure 15** D1,D2,D3,D4,D5,D6,D7 FWD  
**Typical reverse recovery current as a function of collector current**



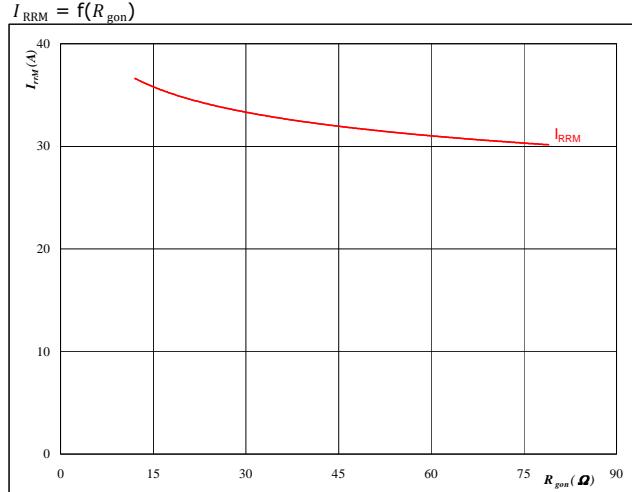
**At**  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 28 \text{ } \Omega$

**Figure 14** D1,D2,D3,D4,D5,D6,D7 FWD  
**Typical reverse recovery charge as a function of IGBT turn on gate resistor**  
 $Q_{rr} = f(R_{gon})$



**At**  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 600 \text{ V}$   
 $I_F = 35 \text{ A}$   
 $V_{GE} = \pm 15 \text{ V}$

**Figure 16** D1,D2,D3,D4,D5,D6,D7 FWD  
**Typical reverse recovery current as a function of IGBT turn on gate resistor**



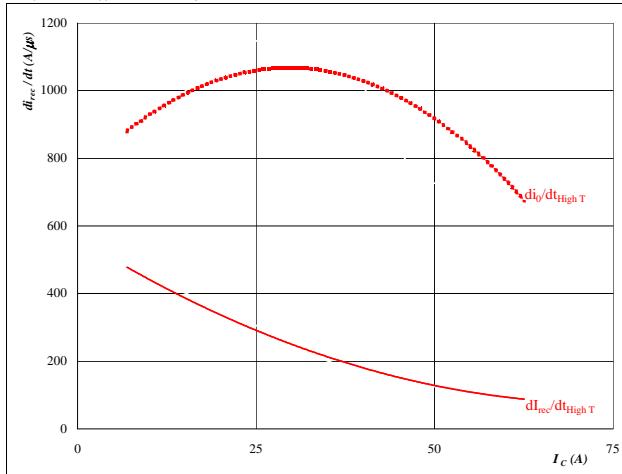
**At**  
 $T_j = 125 \text{ } ^\circ\text{C}$   
 $V_R = 600 \text{ V}$   
 $I_F = 35 \text{ A}$   
 $V_{GE} = \pm 15 \text{ V}$

# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

**Figure 17** D1,D2,D3,D4,D5,D6,D7 FWD

**Typical rate of fall of forward  
and reverse recovery current as a  
function of collector current**

$$dI_0/dt, dI_{rec}/dt = f(I_c)$$

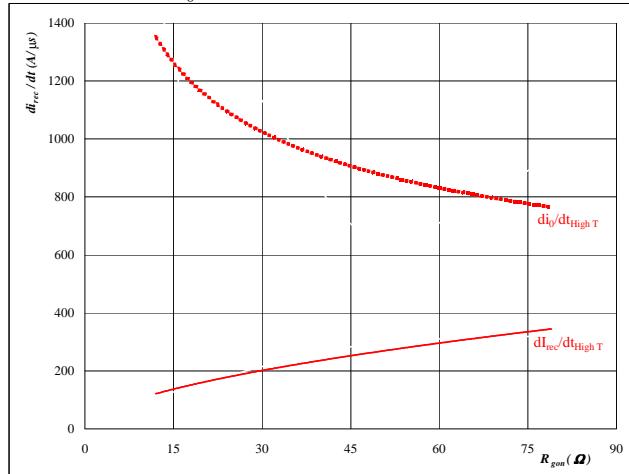

**At**

$T_j =$	125	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	28	Ω

**Figure 18** D1,D2,D3,D4,D5,D6,D7 FWD

**Typical rate of fall of forward  
and reverse recovery current as a  
function of IGBT turn on gate resistor**

$$dI_0/dt, dI_{rec}/dt = f(R_{gon})$$

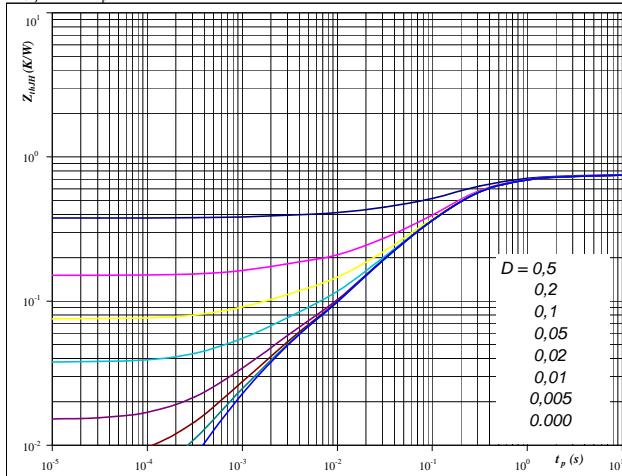

**At**

$T_j =$	125	°C
$V_R =$	600	V
$I_F =$	35	A
$V_{GE} =$	±15	V

**Figure 19** T1,T2,T3,T4,T5,T6,T7 IGBT

**IGBT transient thermal impedance  
as a function of pulse width**

$$Z_{thIH} = f(t_p)$$


**At**

$D =$	$t_p / T$
$R_{thIH} =$	0,75 K/W

IGBT thermal model values

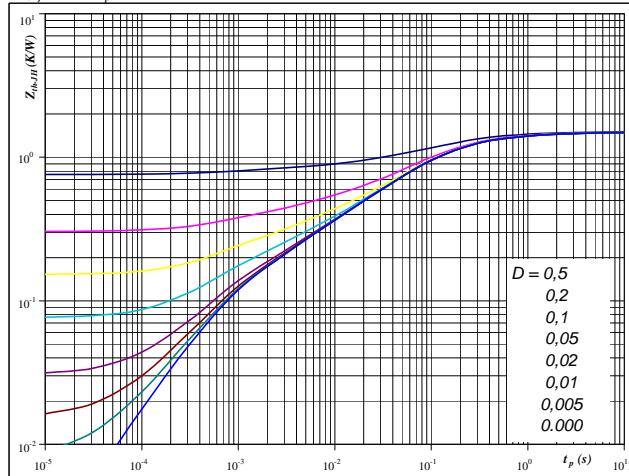
Thermal grease

$R$ (K/W)	Tau (s)
0,04	4,0E+00
0,16	6,0E-01
0,43	1,5E-01
0,09	2,0E-02
0,03	1,5E-03

**Figure 20** D1,D2,D3,D4,D5,D6,D7 FWD

**FWD transient thermal impedance  
as a function of pulse width**

$$Z_{thIH} = f(t_p)$$


**At**

$D =$	$t_p / T$
$R_{thIH} =$	1,50 K/W

FWD thermal model values

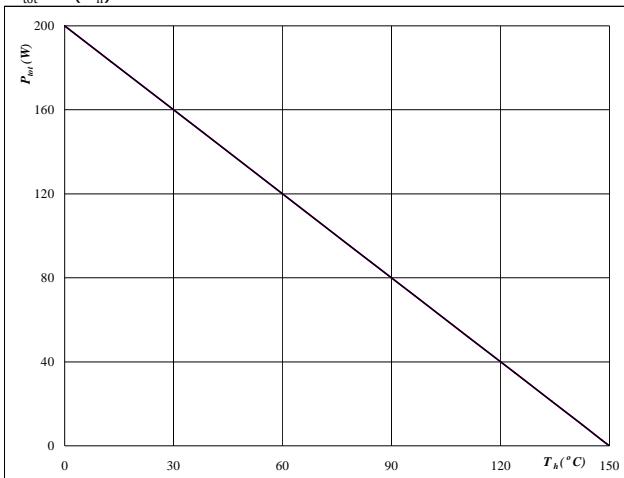
Thermal grease

$R$ (K/W)	Tau (s)
0,04	6,4E+01
0,12	1,8E+00
0,44	2,4E-01
0,62	6,3E-02
0,19	7,6E-03
0,12	7,8E-04

# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

**Figure 21**
**T1,T2,T3,T4,T5,T6,T7 IGBT**
**Power dissipation as a**
**function of heatsink temperature**

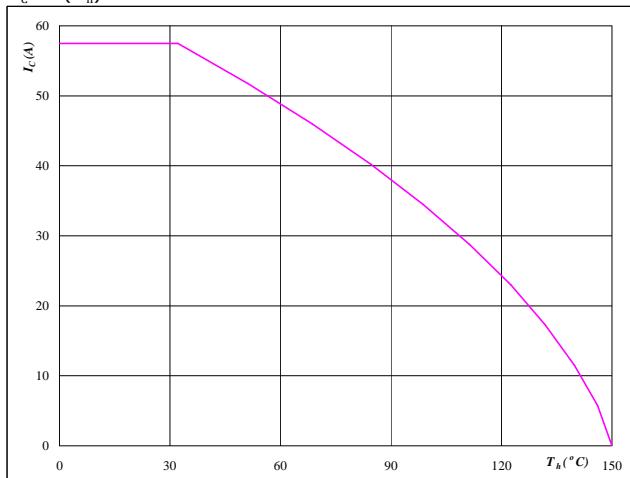
$$P_{\text{tot}} = f(T_h)$$


**At**

$$T_j = 150 \quad ^\circ\text{C}$$

**Figure 22**
**T1,T2,T3,T4,T5,T6,T7 IGBT**
**Collector current as a**
**function of heatsink temperature**

$$I_C = f(T_h)$$

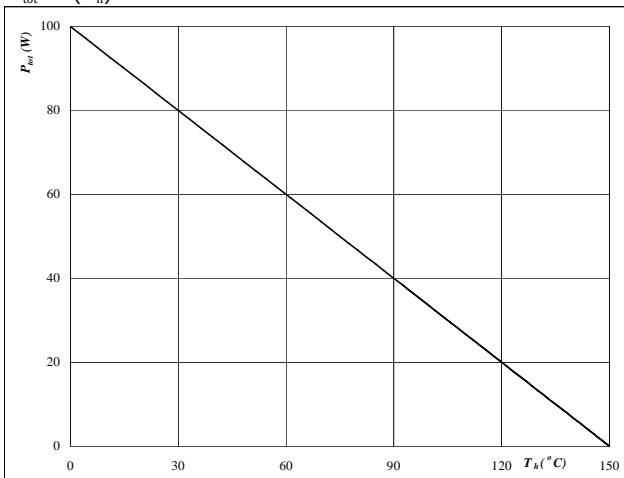

**At**

$$T_j = 150 \quad ^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 23**
**D1,D2,D3,D4,D5,D6,D7 FWD**
**Power dissipation as a**
**function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$


**At**

$$T_j = 150 \quad ^\circ\text{C}$$

**Figure 24**
**D1,D2,D3,D4,D5,D6,D7 FWD**
**Forward current as a**
**function of heatsink temperature**

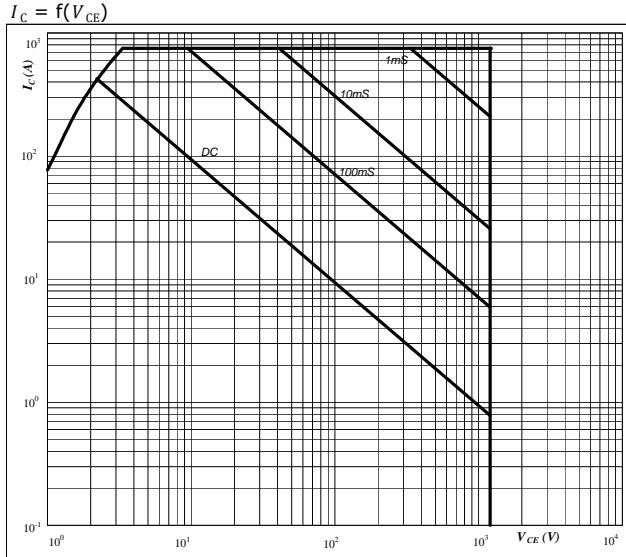
$$I_F = f(T_h)$$


**At**

$$T_j = 150 \quad ^\circ\text{C}$$

# **T1,T2,T3,T4,T5,T6,T7 / D1,D2,D3,D4,D5,D6,D7**

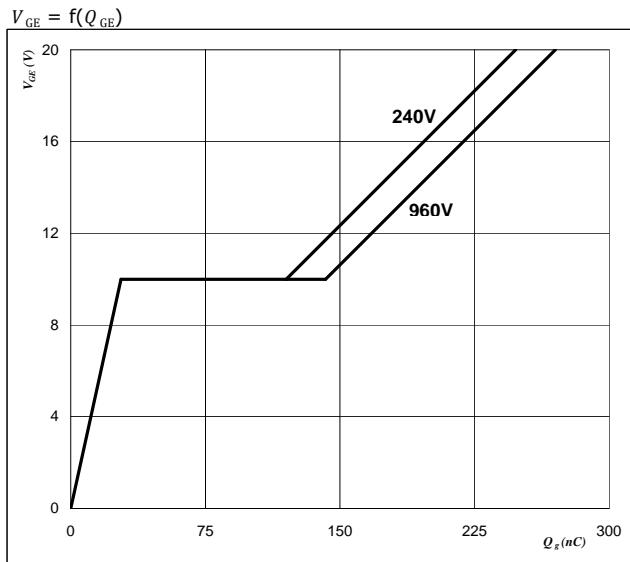
**Figure 25** T1,T2,T3,T4,T5,T6,T7 IGBT  
**Safe operating area as a function of collector-emitter voltage**



**At**

$D$  = single pulse  
 $T_h$  = 80 °C  
 $V_{GE}$  = ±15 V  
 $T_j$  =  $T_{jmax}$  °C

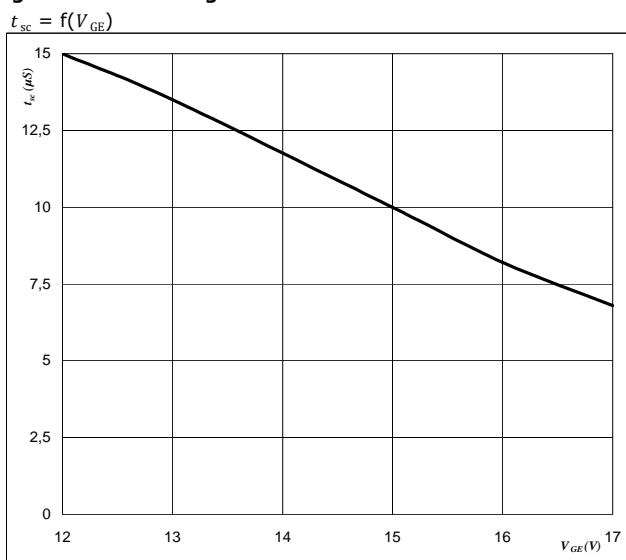
**Figure 26** T1,T2,T3,T4,T5,T6,T7 IGBT  
**Gate voltage vs Gate charge**



**At**

$I_C = 35$  A

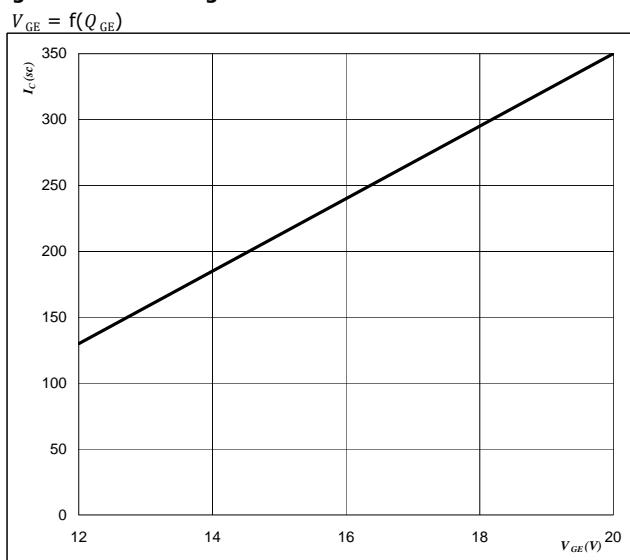
**Figure 27** T1,T2,T3,T4,T5,T6,T7 IGBT  
**Short circuit withstand time as a function of gate-emitter voltage**



**At**

$V_{CE} = 1200$  V  
 $T_j \leq 150$  °C

**Figure 28** T1,T2,T3,T4,T5,T6,T7 IGBT  
**Typical short circuit collector current as a function of gate-emitter voltage**



**At**

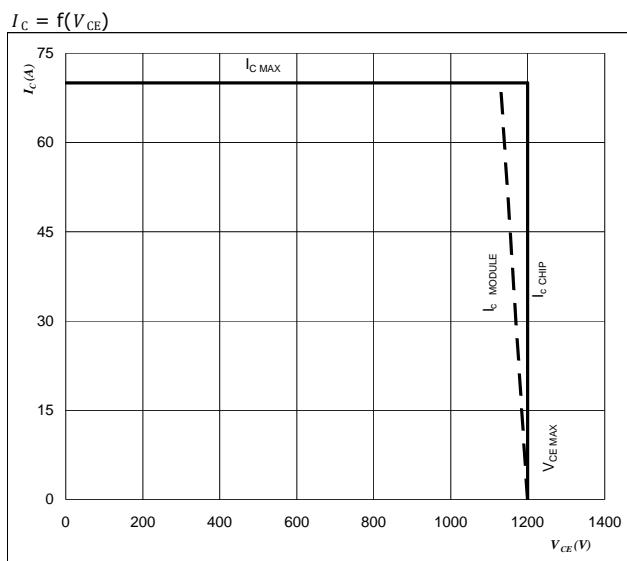
$V_{CE} \leq 1200$  V  
 $T_j = 150$  °C

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Figure 29

T1,T2,T3,T4,T5,T6,T7 IGBT

Reverse bias safe operating area

**At**

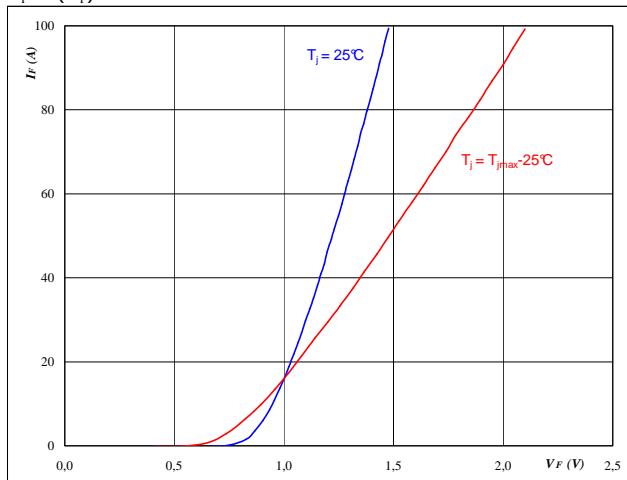
$T_j = 125^\circ\text{C}$   
 $R_{gon} = 28 \Omega$   
 $R_{goff} = 28 \Omega$

## **D8,D9,D10,D11,D12,D13**

**Figure 1** D8,D9,D10,D11,D12,D13 diode

**Typical diode forward current as a function of forward voltage**

$$I_F = f(V_F)$$



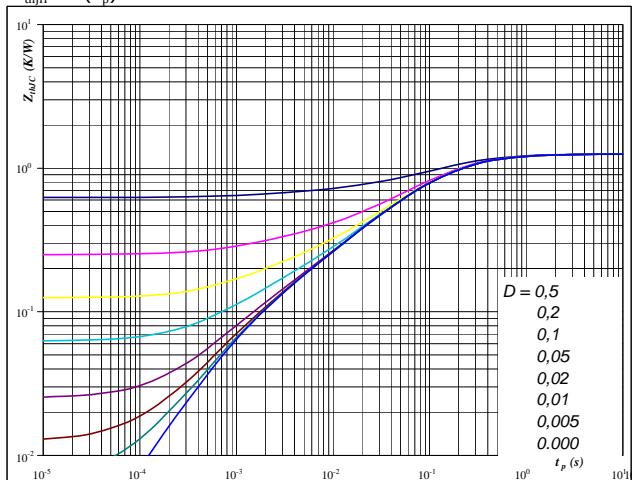
**At**

$$t_p = 250 \mu\text{s}$$

**Figure 2** D8,D9,D10,D11,D12,D13 diode

**Diode transient thermal impedance as a function of pulse width**

$$Z_{thjH} = f(t_p)$$



**At**

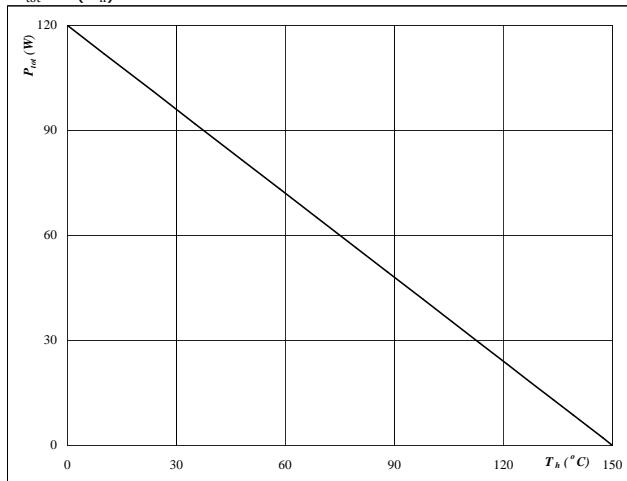
$$D = tp / T$$

$$R_{thjH} = 1,25 \text{ K/W}$$

**Figure 3** D8,D9,D10,D11,D12,D13 diode

**Power dissipation as a function of heatsink temperature**

$$P_{tot} = f(T_h)$$



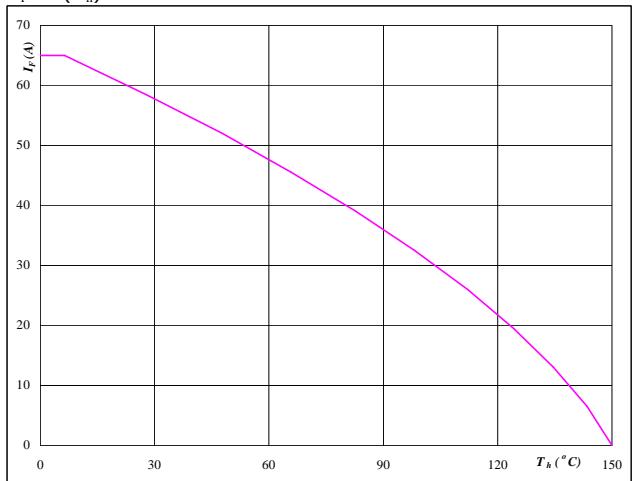
**At**

$$T_j = 150 ^\circ\text{C}$$

**Figure 4** D8,D9,D10,D11,D12,D13 diode

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$



**At**

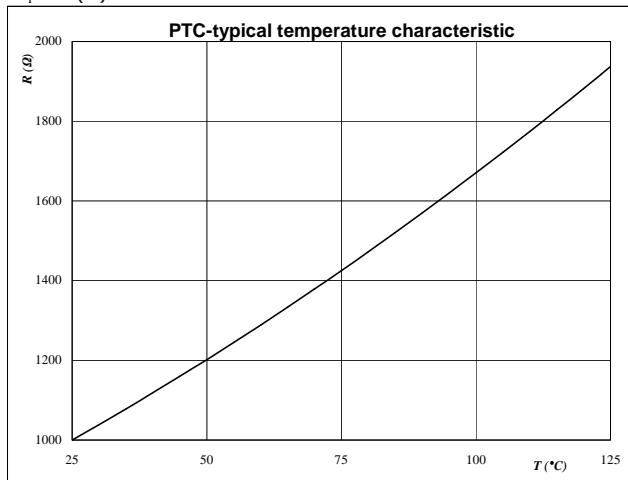
$$T_j = 150 ^\circ\text{C}$$

## Thermistor

**Figure 1** Thermistor

**Typical PTC characteristic  
as a function of temperature**

$$R_T = f(T)$$



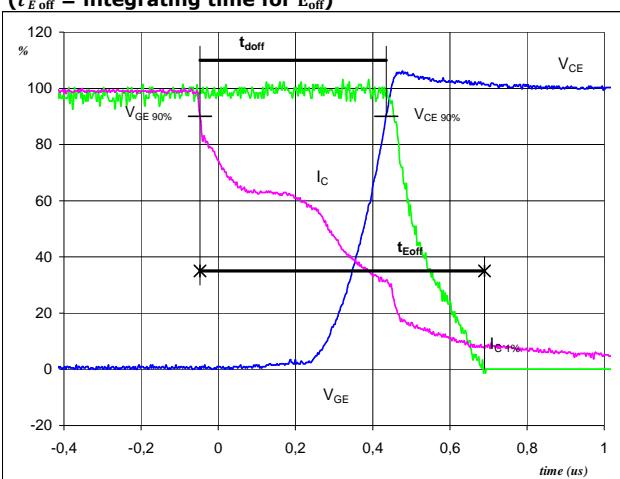
## Switching Definitions Output Inverter

**General conditions**

$T_j$	= 125 °C
$R_{gon}$	= 28 Ω
$R_{goff}$	= 28 Ω

**Figure 1**

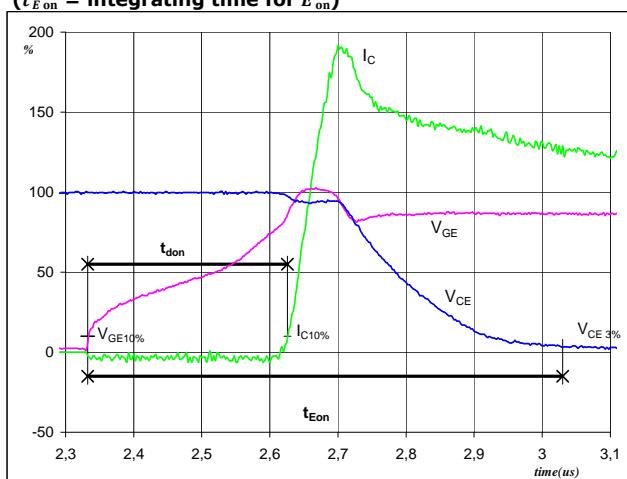
Output inverter IGBT

**Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$**   
( $t_{Eoff}$  = integrating time for  $E_{off}$ )


$V_{GE} (0\%) =$	-15	V
$V_{GE} (100\%) =$	15	V
$V_C (100\%) =$	600	V
$I_C (100\%) =$	35	A
$t_{doff} =$	0,49	μs
$t_{Eoff} =$	0,74	μs

**Figure 2**

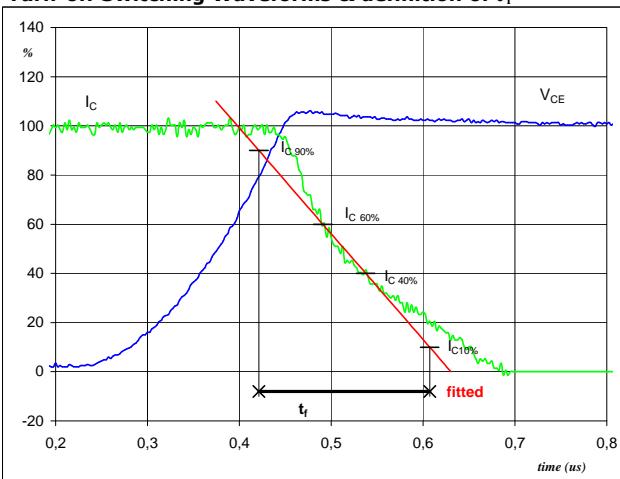
Output inverter IGBT

**Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$**   
( $t_{Eon}$  = integrating time for  $E_{on}$ )


$V_{GE} (0\%) =$	-15	V
$V_{GE} (100\%) =$	15	V
$V_C (100\%) =$	600	V
$I_C (100\%) =$	35	A
$t_{don} =$	0,08	μs
$t_{Eon} =$	0,70	μs

**Figure 3**

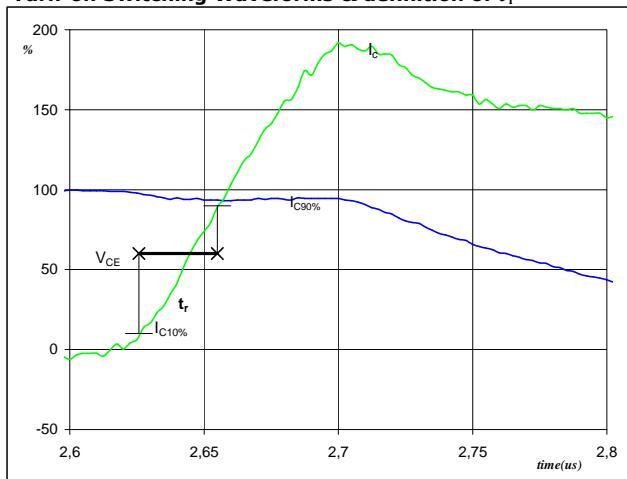
Output inverter IGBT

**Turn-off Switching Waveforms & definition of  $t_f$** 


$V_C (100\%) =$	600	V
$I_C (100\%) =$	35	A
$t_f =$	0,19	μs

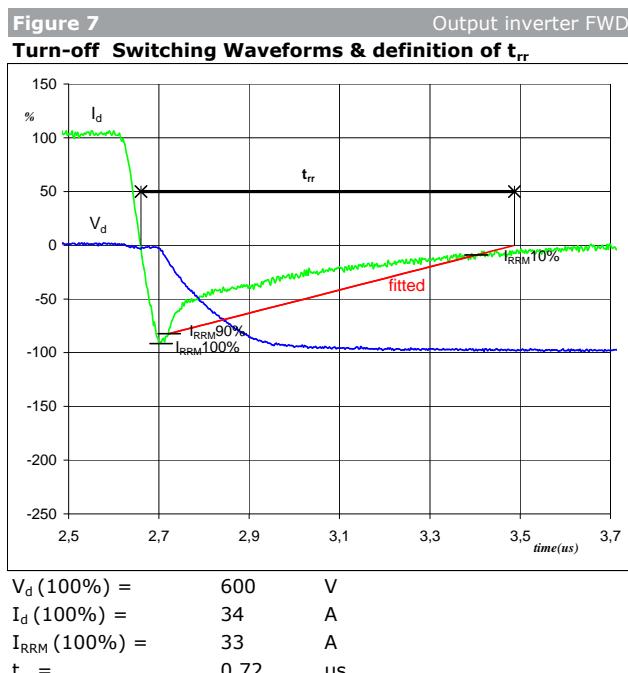
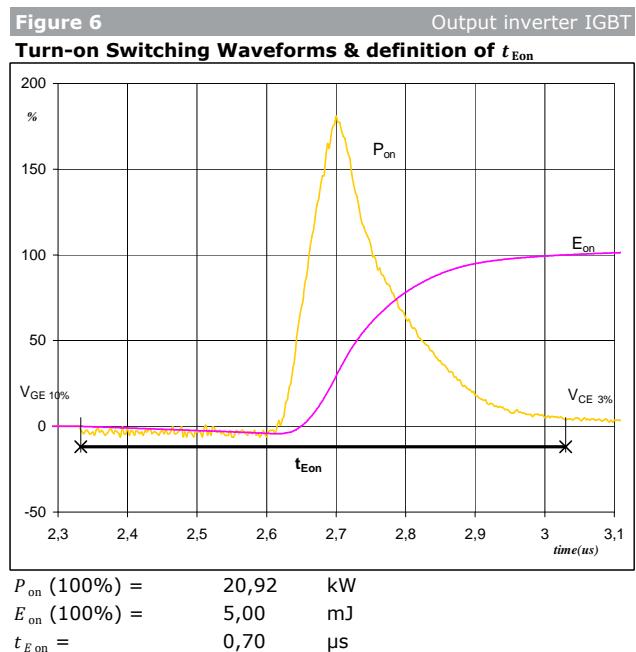
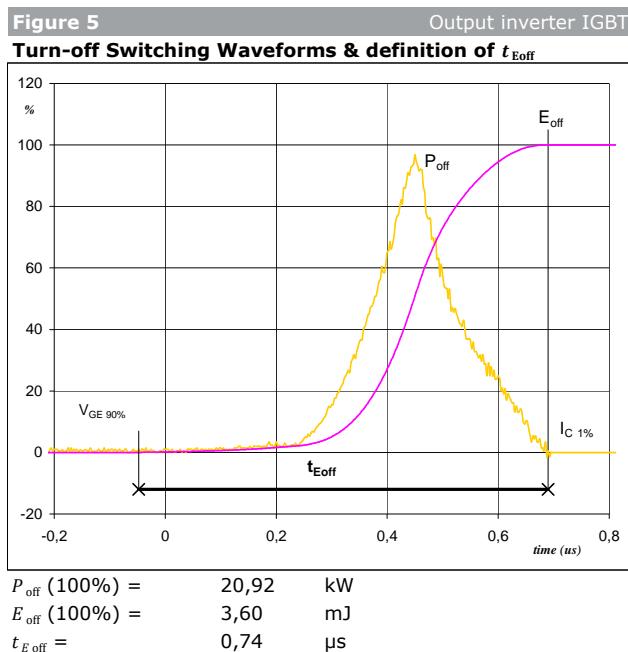
**Figure 4**

Output inverter IGBT

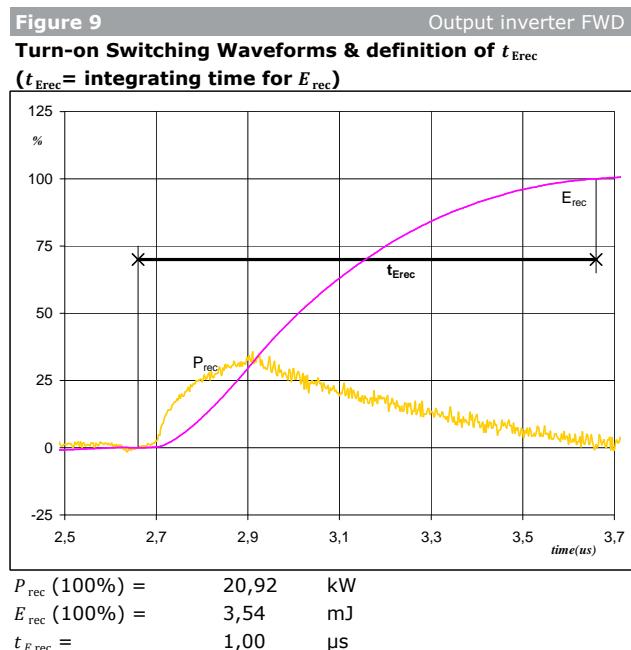
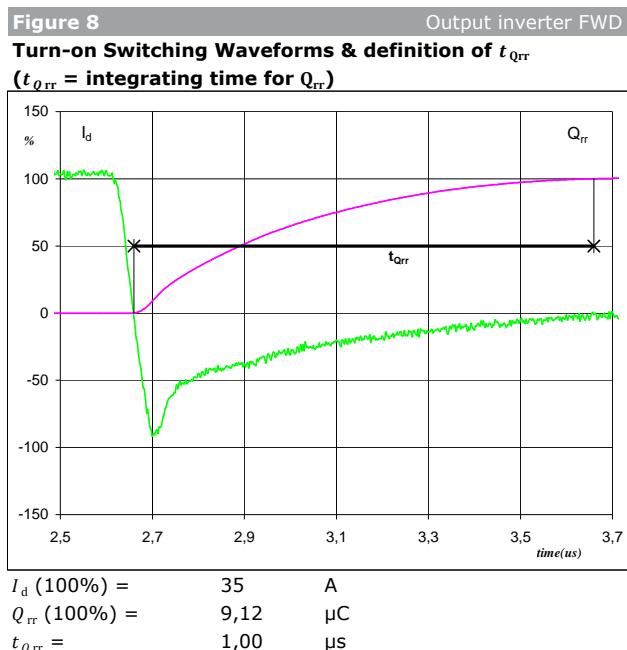
**Turn-on Switching Waveforms & definition of  $t_r$** 


$V_C (100\%) =$	600	V
$I_C (100\%) =$	35	A
$t_r =$	0,03	μs

## Switching Definitions Output Inverter



## Switching Definitions Output Inverter

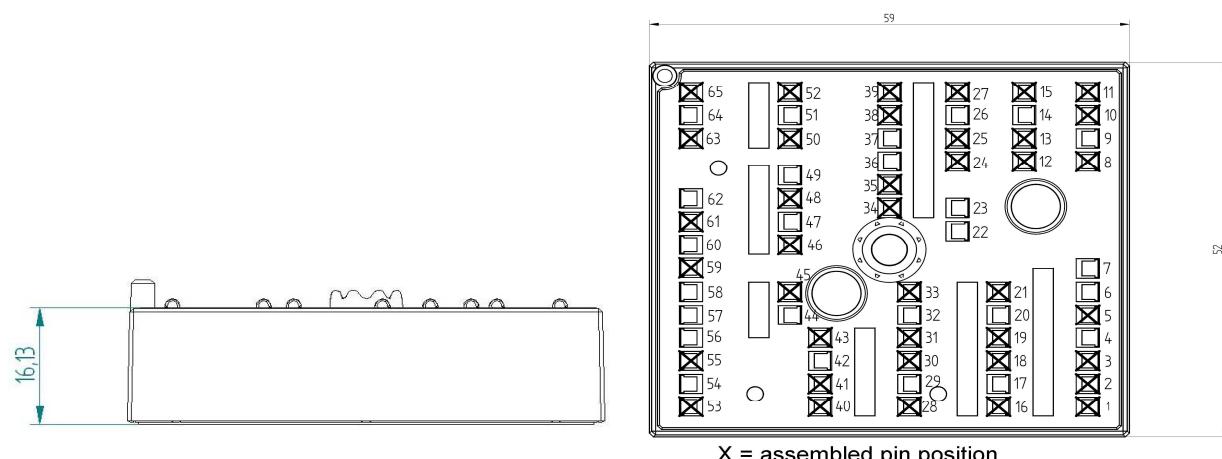


## Ordering Code and Marking - Outline - Pinout

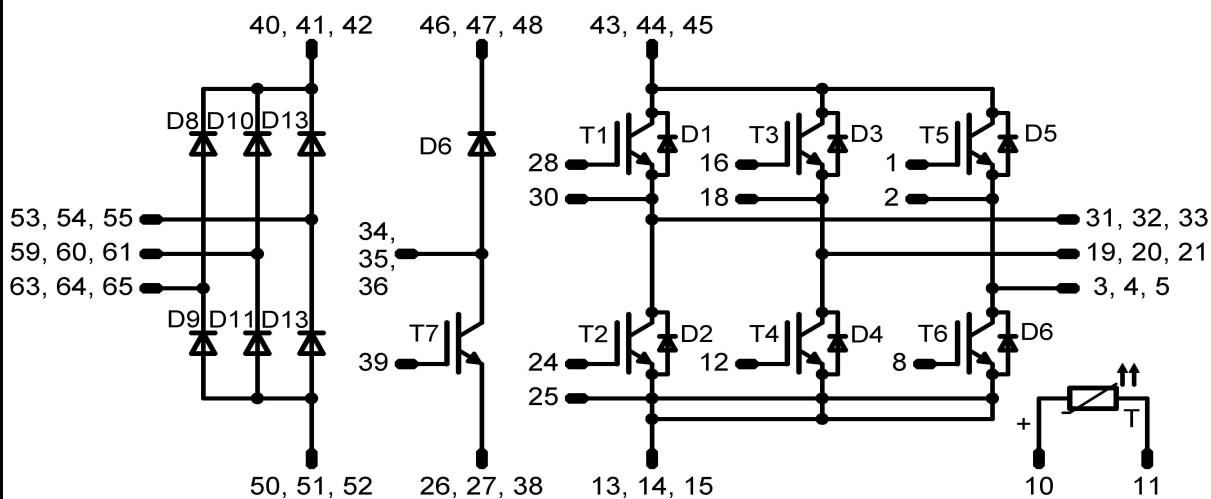
### Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K22-T-PM)	V23990-K220-A-/0A/-PM	K220A	K220A-/0A/
with std lid (black V23990-K22-T-PM) and P12	V23990-K220-A-/1A/-PM	K220A	K220A-/1A/
with thin lid (white V23990-K23-T-PM)	V23990-K220-A-/0B/-PM	K220A	K220A-/0B/
with thin lid (white V23990-K23-T-PM) and P12	V23990-K220-A-/1B/-PM	K220A	K220A-/1B/

### Outline



### Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.