10-EY12PMA035M7-L188A78T

datasheet

***flow*PIM E2** | **1200 V / 35 A**

Features

- IGBT M7 with low VCEsat and improved EMC behavior
- Standard industrial housing
- Optimized Rth(j-s) with Phase Change Material
- Built-in NTC

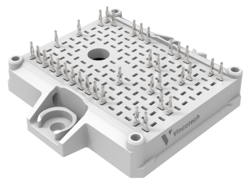
Target applications

- Industrial Drives

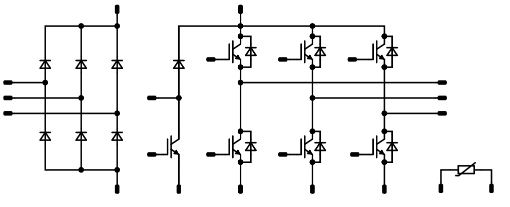
Types

- 10-EY12PMA035M7-L188A78T

flow E2 12 mm housing



Schematic



Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Inverter Switch					
Collector-emitter voltage	V_{CES}			1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	51	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		70	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	108	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 800 V	T_j = 150 °C	9,5	µs
Maximum junction temperature	T_{jmax}			175	°C
Inverter Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	41	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}		70	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	78	W
Maximum junction temperature	T_{jmax}			175	°C
Brake Switch					
Collector-emitter voltage	V_{CES}			1200	V
Collector current (DC current)	I_C	$T_j = T_{jmax}$	T_s = 80 °C	51	A
Repetitive peak collector current	I_{CRM}	t_p limited by T_{jmax}		70	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	108	W
Gate-emitter voltage	V_{GES}			±20	V
Short circuit ratings	t_{SC}	V_{GE} = 15 V, V_{CC} = 800 V	T_j = 150 °C	9,5	µs
Maximum junction temperature	T_{jmax}			175	°C

Maximum Ratings

T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Value	Unit
Brake Diode					
Peak repetitive reverse voltage	V_{RRM}			1200	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	19	A
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	43	W
Maximum junction temperature	T_{jmax}			175	°C
Rectifier Diode					
Peak repetitive reverse voltage	V_{RRM}			1600	V
Forward current (DC current)	I_F	$T_j = T_{jmax}$	T_s = 80 °C	58	A
Surge (non-repetitive) forward current	I_{FSM}	Single Half Sine Wave, t_p = 10 ms	T_j = 150 °C	400	A
Surge current capability	I^2t			800	A^2s
Total power dissipation	P_{tot}	$T_j = T_{jmax}$	T_s = 80 °C	68	W
Maximum junction temperature	T_{jmax}			150	°C

Module Properties

Parameter	Symbol	Conditions		Value	Unit
Thermal Properties					
Storage temperature	T_{stg}			-40…+125	°C
Operation temperature under switching condition	T_{jop}			-40…+(T_{jmax} - 25)	°C
Isolation Properties					
Isolation voltage	V_{isol}	DC Test Voltage*	t_p = 2 s	6000	V
Isolation voltage	V_{isol}	AC Voltage	t_p = 1 min	2500	V
Creepage distance				>12,7	mm
Clearance				8.83	mm
Comparative Tracking Index	CTI			≥ 600	

*100 % tested in production

Characteristic Values

Parameter	Symbol	Conditions	V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	Unit
Inverter Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0035	25	5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CEsat}		15		35	25 125 150		1,47 1,64 1,68	1,85(1)	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			0,08	mA
Gate-emitter leakage current	I_{GES}		20	0		25			0,5	μA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}		0	10		25		7900		pF
Output capacitance	C_{oes}		0	10		25		270		pF
Reverse transfer capacitance	C_{res}		0	10		25		97		pF
Gate charge	Q_g	V_{CC} = 600 V	15		35	25		260		nC
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,88		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 16 Ω R_{goff} = 16 Ω	±15	600	35	25 125 150		223,4 240,2 233		ns
Rise time	t_r	R_{gon} = 16 Ω R_{goff} = 16 Ω	±15	600	35	25 125 150		28 34 35,2		ns
Turn-off delay time	$t_{d(off)}$	R_{gon} = 16 Ω R_{goff} = 16 Ω	±15	600	35	25 125 150		227 252,2 259,4		ns
Fall time	t_f	R_{gon} = 16 Ω R_{goff} = 16 Ω	±15	600	35	25 125 150		96,87 114,26 122,62		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=3,8 μC Q_{rFWD}=5,84 μC Q_{rFWD}=6,39 μC	±15	600	35	25 125 150		2,45 3,23 3,44		mWs
Turn-off energy (per pulse)	E_{off}		±15	600	35	25 125 150		2,46 3,24 3,46		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Inverter Diode										
Static										
Forward voltage	V_F				35	25 125 150		1,66 1,76 1,75	2,1[1]	V
Reverse leakage current	I_R	V_r = 1200 V				25			40	µA
Thermal										
Thermal resistance junction to sink [2]	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,22		K/W
Dynamic										
Peak recovery current	I_{RRM}	d*i*/d*t*=1364 A/µs d*i*/d*t*=1192 A/µs d*i*/d*t*=1157 A/µs	±15	600	35	25 125 150		41,09 40,2 40,64		A
Reverse recovery time	t_{rr}					25 125 150		266,56 425,38 450,01		ns
Recovered charge	Q_r					25 125 150		3,8 5,84 6,39		µC
Reverse recovered energy	E_{rec}					25 125 150		1,48 2,39 2,6		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		485,16 353,08 342,83		A/µs

Characteristic Values

Parameter	Symbol	Conditions	V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_{F} [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Values Min	Typ	Max	Unit
Brake Switch										
Static										
Gate-emitter threshold voltage	$V_{GE(th)}$			10	0,0035	25	5,4	6	6,6	V
Collector-emitter saturation voltage	V_{CEsat}		15		35	25 125 150		1,47 1,64 1,68	1,85[1]	V
Collector-emitter cut-off current	I_{CES}		0	1200		25			0,08	mA
Gate-emitter leakage current	I_{GES}		20	0		25			0,5	µA
Internal gate resistance	r_g							None		Ω
Input capacitance	C_{ies}		0	10		25		7900		pF
Output capacitance	C_{oes}		0	10		25		270		pF
Reverse transfer capacitance	C_{res}		0	10		25		97		pF
Gate charge	Q_g	V_{CC} = 600 V	15		35	25		260		nC
Thermal										
Thermal resistance junction to sink [2]	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						0,88		K/W
Dynamic										
Turn-on delay time	$t_{d(on)}$	R_{gon} = 8 Ω R_{goff} = 8 Ω	0/15	600	35	25 125 150		56 54,08 53,76		ns
Rise time	t_r	R_{gon} = 8 Ω R_{goff} = 8 Ω	0/15	600	35	25 125 150		45,12 46,72 47,68		ns
Turn-off delay time	$t_{d(off)}$	R_{gon} = 8 Ω R_{goff} = 8 Ω	0/15	600	35	25 125 150		255,36 288 297,92		ns
Fall time	t_f	R_{gon} = 8 Ω R_{goff} = 8 Ω	0/15	600	35	25 125 150		79,39 99,71 106,17		ns
Turn-on energy (per pulse)	E_{on}	Q_{rFWD}=1,79 µC Q_{rFWD}=2,63 µC Q_{rFWD}=2,88 µC	0/15	600	35	25 125 150		2,14 2,49 2,61		mWs
Turn-off energy (per pulse)	E_{off}		0/15	600	35	25 125 150		2,65 3,6 3,85		mWs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Brake Diode										
Static										
Forward voltage	V_F				10	25 125 150		1,61 1,69 1,7	2,1[1]	V
Reverse leakage current	I_R	V_r = 1200 V				25			25	µA
Thermal										
Thermal resistance junction to sink [2]	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						2,19		K/W
Dynamic										
Peak recovery current	I_{RRM}	di/dt=564 A/µs di/dt=549 A/µs di/dt=552 A/µs	0/15	600	35	25 125 150		16,27 17,88 18,38		A
Reverse recovery time	t_{rr}					25 125 150		281,59 414 449		ns
Recovered charge	Q_r					25 125 150		1,79 2,63 2,88		µC
Reverse recovered energy	E_{rec}					25 125 150		0,692 1,09 1,21		mWs
Peak rate of fall of recovery current	$(di_{rf}/dt)_{max}$					25 125 150		242,57 180,2 168,69		A/µs

Characteristic Values

Parameter	Symbol	Conditions					Values			Unit
			V_{GE} [V] V_{GS} [V]	V_{CE} [V] V_{DS} [V] V_F [V]	I_C [A] I_D [A] I_F [A]	T_j [°C]	Min	Typ	Max	
Rectifier Diode										
Static										
Forward voltage	V_F				35	25 125 150		1,09 1,02 1,02	1,5(1)	V
Reverse leakage current	I_R	V_r = 1600 V				25 150			100 2000	µA
Thermal										
Thermal resistance junction to sink (2)	$R_{th(j-s)}$	λ_{paste} = 3,4 W/mK (PSX)						1,03		K/W
Thermistor										
Static										
Rated resistance	R					25		5		kΩ
Deviation of R_{100}	$\Delta_{R/R}$	R_{100} = 493 Ω				100	-5		5	%
Power dissipation	P							245		mW
Power dissipation constant	d					25		1,4		mW/K
B-value	$B_{(25/50)}$	Tol. ±2 %						3375		K
B-value	$B_{(25/100)}$	Tol. ±2 %						3437		K
Vincotech Thermistor Reference									K	

(1) Value at chip level

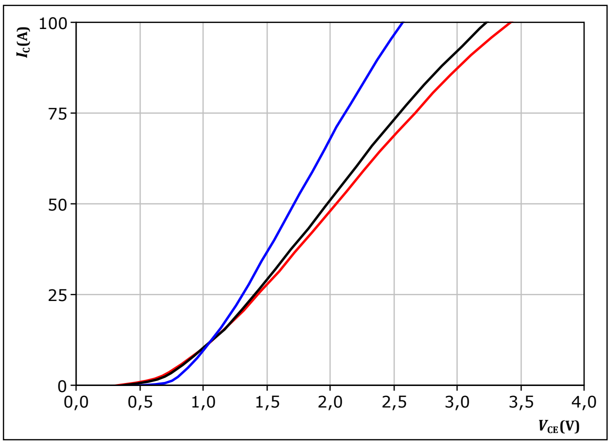
(2) Only valid with pre-applied Vincotech thermal interface material.

Inverter Switch Characteristics

figure 1. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



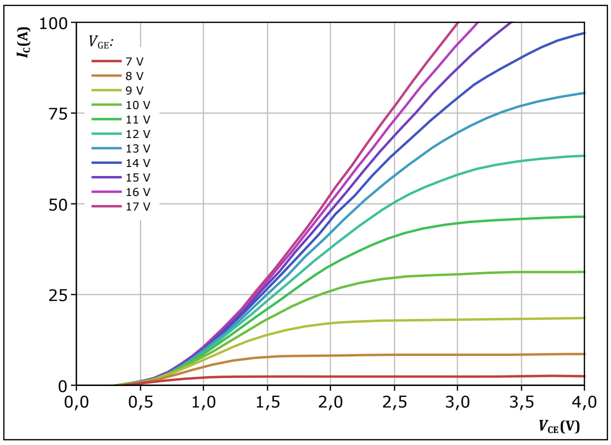
t_p = 250 µs
V_{GE} = 15 V

T_j: 25 °C, 125 °C, 150 °C

figure 2. **IGBT**

Typical output characteristics

I_C = f(V_{CE})

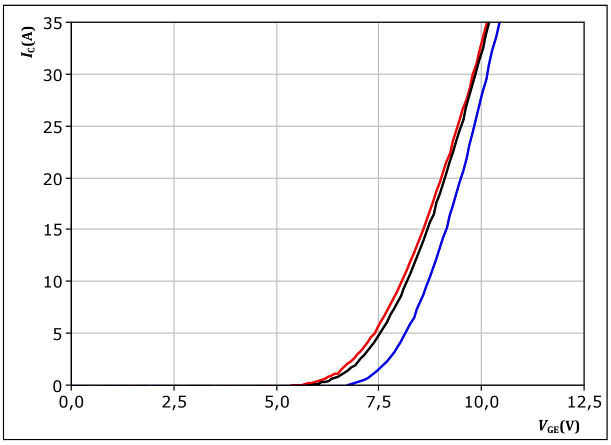


t_p = 250 µs
T_j = 150 °C
V_{GE} from 7 V to 17 V in steps of 1 V

figure 3. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})



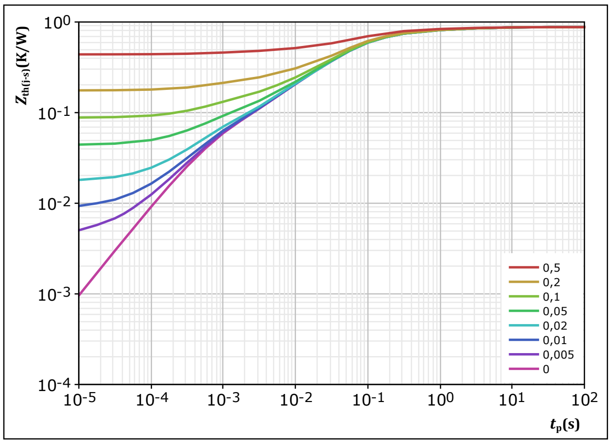
t_p = 250 µs
V_{CE} = 10 V

T_j: 25 °C, 125 °C, 150 °C

figure 4. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T
$R_{th(j-s)}$ = 0,878 K/W

IGBT thermal model values

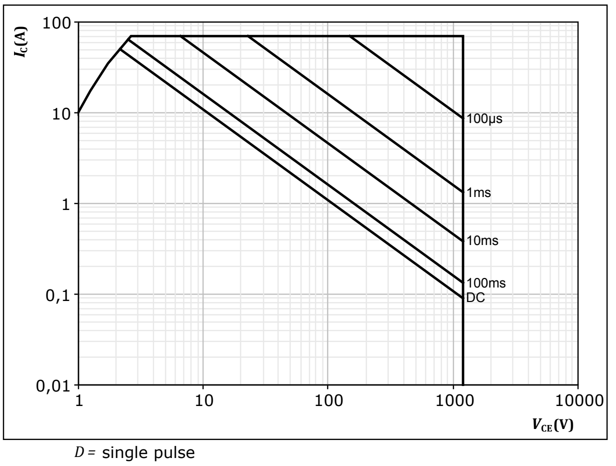
R (K/W)	τ (s)
4,21E-02	5,44E+00
8,81E-02	8,75E-01
2,30E-01	1,59E-01
3,79E-01	4,61E-02
9,10E-02	6,73E-03
4,73E-02	6,45E-04

Inverter Switch Characteristics

figure 5. **IGBT**

Safe operating area

I_C = f(V_{CE})



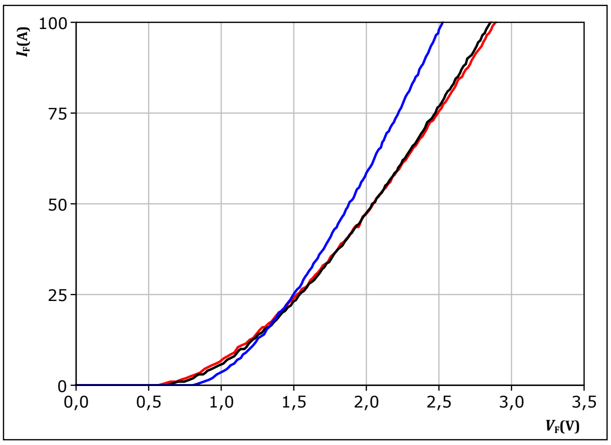
D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Inverter Diode Characteristics

figure 6. **FWD**

Typical forward characteristics

I_F = f(V_F)



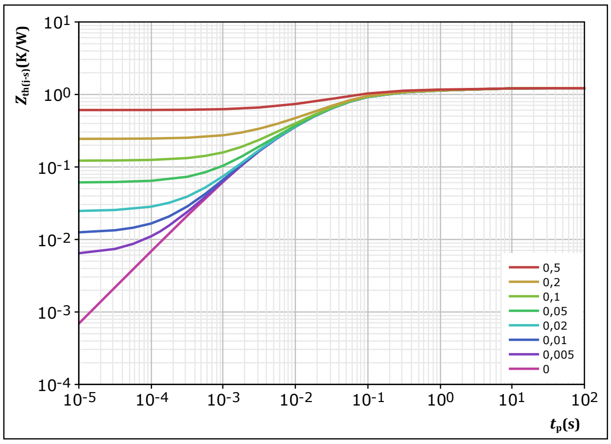
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 7. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,219 K/W

FWD thermal model values

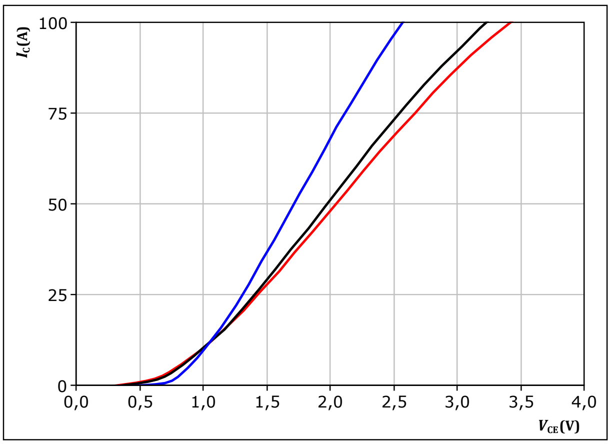
R (K/W)	τ (s)
1,07E-01	3,56E+00
1,60E-01	2,77E-01
5,76E-01	5,00E-02
2,75E-01	1,24E-02
1,01E-01	2,87E-03

Brake Switch Characteristics

figure 8. **IGBT**

Typical output characteristics

I_C = f(V_{CE})

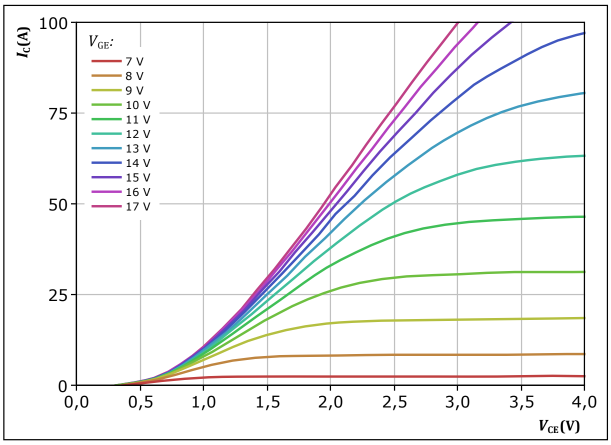


t_p =	250	μs		25 °C
V_{GE} =	15	V	T_j:	125 °C
				150 °C

figure 9. **IGBT**

Typical output characteristics

I_C = f(V_{CE})



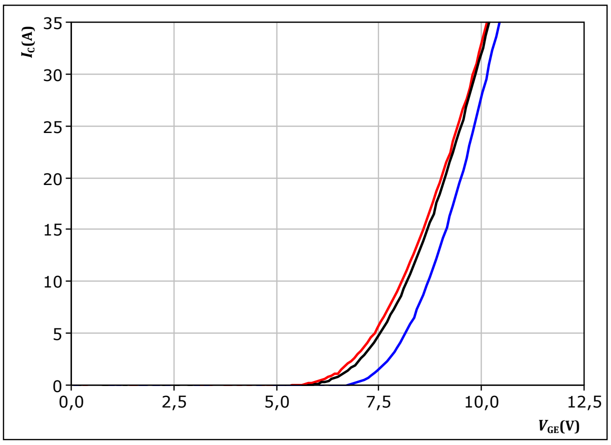
t_p =	250	μs
T_j =	150	°C

V_{GE} from 7 V to 17 V in steps of 1 V

figure 10. **IGBT**

Typical transfer characteristics

I_C = f(V_{GE})

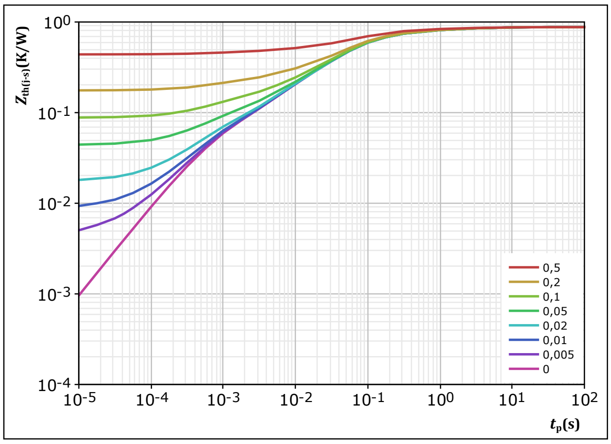


t_p =	250	μs		25 °C
V_{CE} =	10	V	T_j:	125 °C
				150 °C

figure 11. **IGBT**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D =	t_p / T	
$R_{th(j-s)}$ =	0,878	K/W

IGBT thermal model values

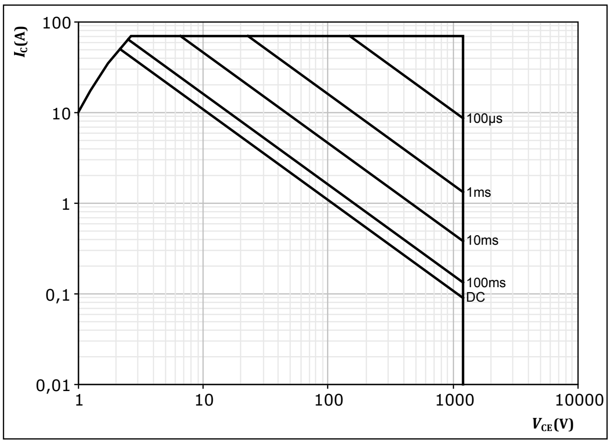
R (K/W)	τ (s)
4,21E-02	5,44E+00
8,81E-02	8,75E-01
2,30E-01	1,59E-01
3,79E-01	4,61E-02
9,10E-02	6,73E-03
4,73E-02	6,45E-04

Brake Switch Characteristics

figure 12.	IGBT

Safe operating area

I_C = f(V_{CE})



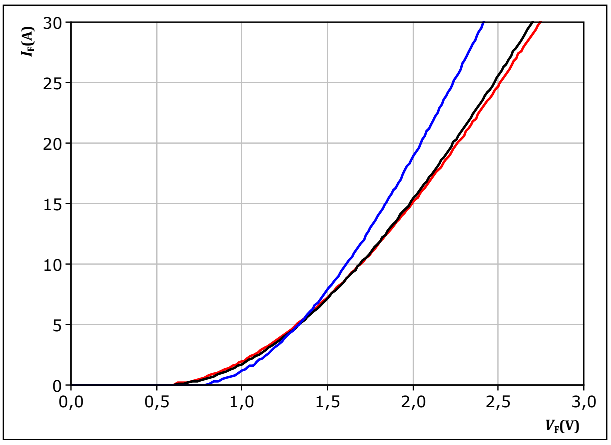
D =	single pulse	
T_s =	80	°C
V_{GE} =	15	V
T_j =	T_{jmax}	

Brake Diode Characteristics

figure 13. **FWD**

Typical forward characteristics

I_F = f(V_F)



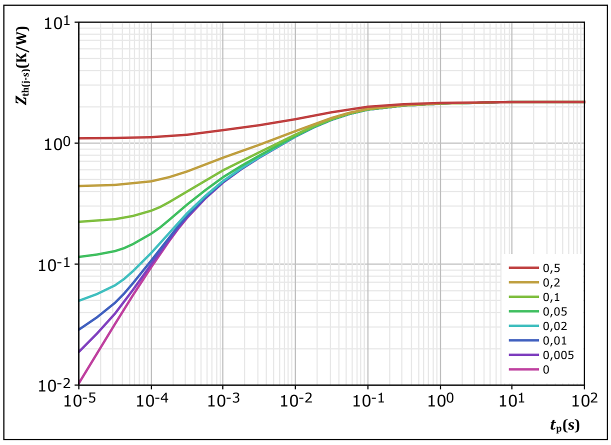
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 14. **FWD**

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 2,189 K/W

FWD thermal model values

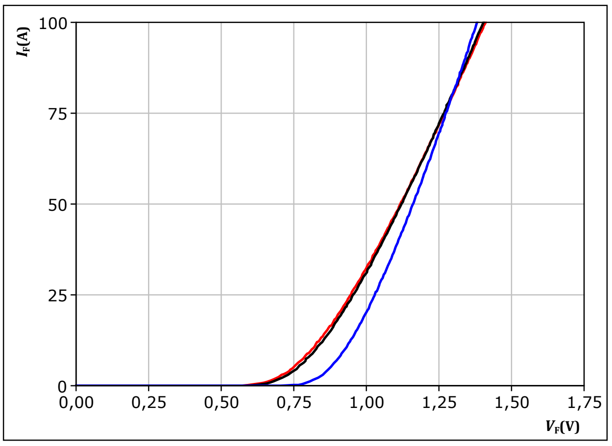
R (K/W)	τ (s)
8,09E-02	3,20E+00
2,08E-01	2,82E-01
6,85E-01	4,41E-02
5,92E-01	1,02E-02
3,27E-01	2,02E-03
2,95E-01	3,64E-04

Rectifier Diode Characteristics

figure 15. Rectifier

Typical forward characteristics

I_F = f(V_F)



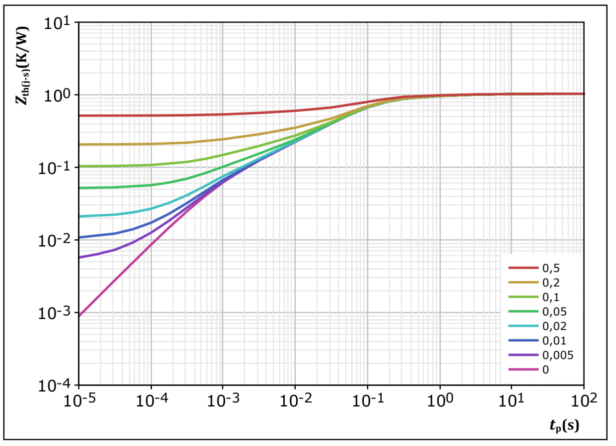
t_p = 250 µs

T_j: 25 °C, 125 °C, 150 °C

figure 16. Rectifier

Transient thermal impedance as a function of pulse width

$Z_{th(j-s)}$ = f(t_p)



D = t_p / T

$R_{th(j-s)}$ = 1,032 K/W

Rectifier thermal model values

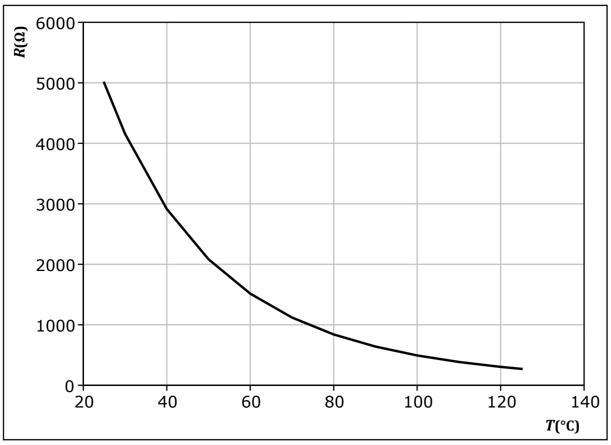
R (K/W)	τ (s)
4,86E-02	5,67E+00
1,31E-01	6,68E-01
5,24E-01	9,95E-02
1,97E-01	3,35E-02
8,14E-02	4,54E-03
4,98E-02	8,29E-04

Thermistor Characteristics

figure 17.	Thermistor

Typical NTC characteristic as function of temperature

R_T = f(T)

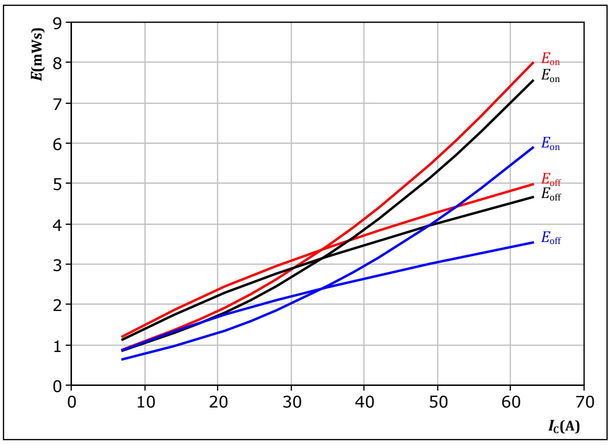


Inverter Switching Characteristics

figure 18. **IGBT**

Typical switching energy losses as a function of collector current

$E = f(I_C)$



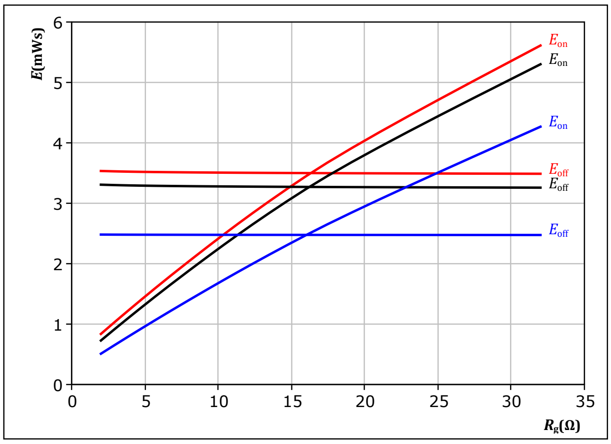
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C
V_{GE} =	±15	V	125 °C
R_{gon} =	16	Ω	150 °C
R_{goff} =	16	Ω	

figure 19. **IGBT**

Typical switching energy losses as a function of gate resistor

$E = f(R_g)$



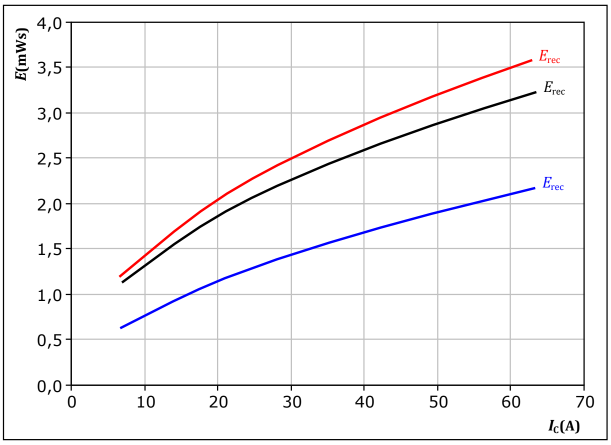
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C
V_{GE} =	±15	V	125 °C
I_C =	35	A	150 °C

figure 20. **FWD**

Typical reverse recovered energy loss as a function of collector current

$E_{rec} = f(I_C)$



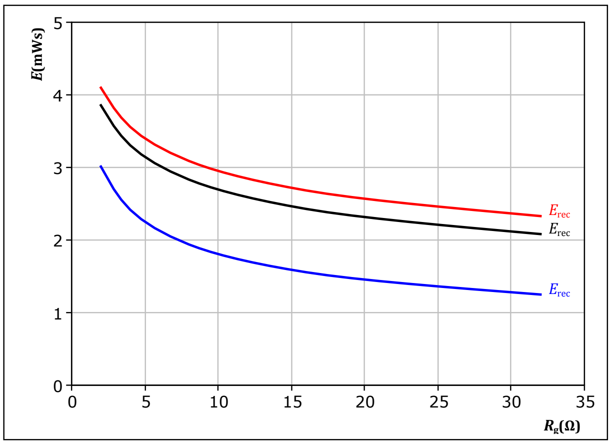
With an inductive load at

V_{CE} =	600	V	T_j: 25 °C
V_{GE} =	±15	V	125 °C
R_{gon} =	16	Ω	150 °C

figure 21. **FWD**

Typical reverse recovered energy loss as a function of gate resistor

$E_{rec} = f(R_g)$



With an inductive load at

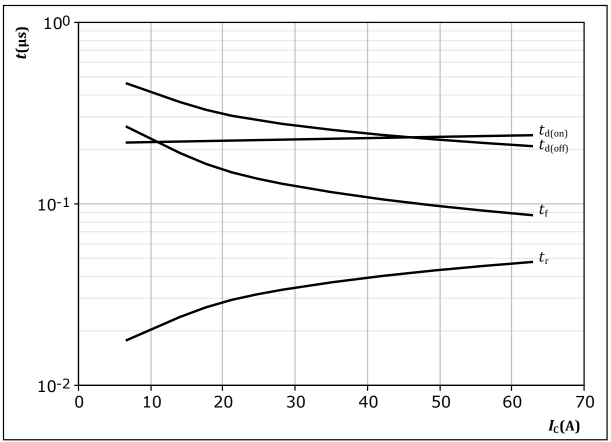
V_{CE} =	600	V	T_j: 25 °C
V_{GE} =	±15	V	125 °C
I_C =	35	A	150 °C

Inverter Switching Characteristics

figure 22. **IGBT**

Typical switching times as a function of collector current

t = f(I_C)



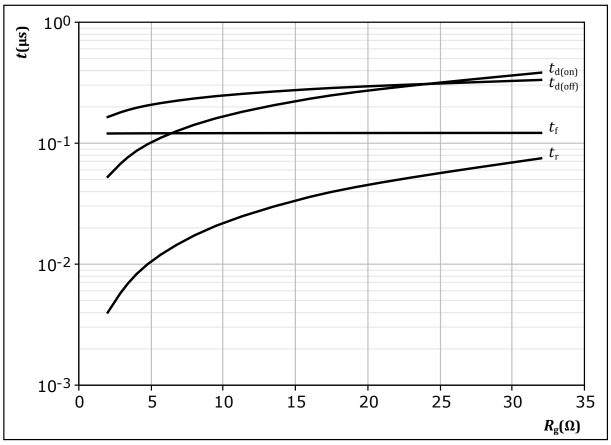
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	16	Ω
R_{goff} =	16	Ω

figure 23. **IGBT**

Typical switching times as a function of gate resistor

t = f(R_g)



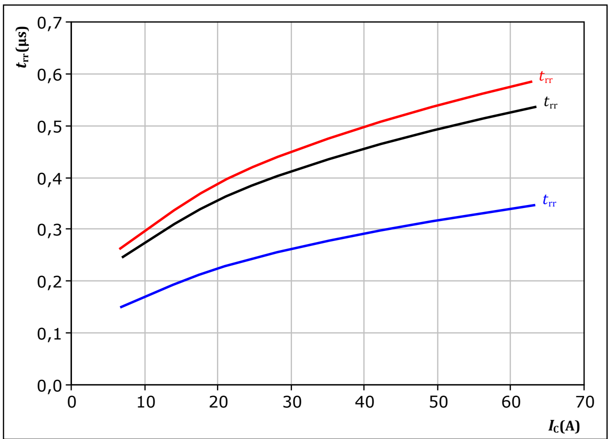
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	35	A

figure 24. **FWD**

Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



With an inductive load at

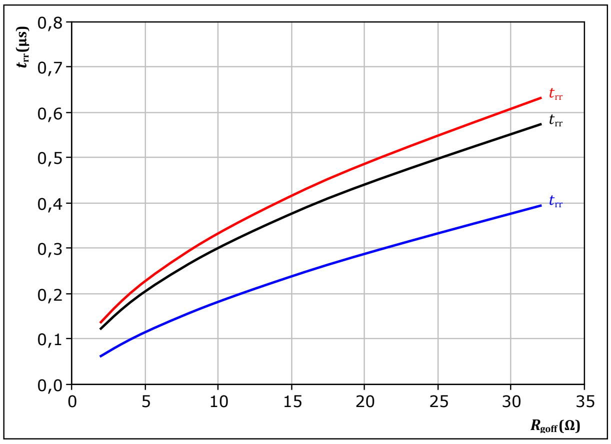
V_{CE} =	600	V
V_{GE} =	±15	V
R_{gon} =	16	Ω

T_j: 25 °C / 125 °C / 150 °C

figure 25. **FWD**

Typical reverse recovery time as a function of IGBT turn off gate resistor

t_{rr} = f(R_{goff})



With an inductive load at

V_{CE} =	600	V
V_{GE} =	±15	V
I_C =	35	A

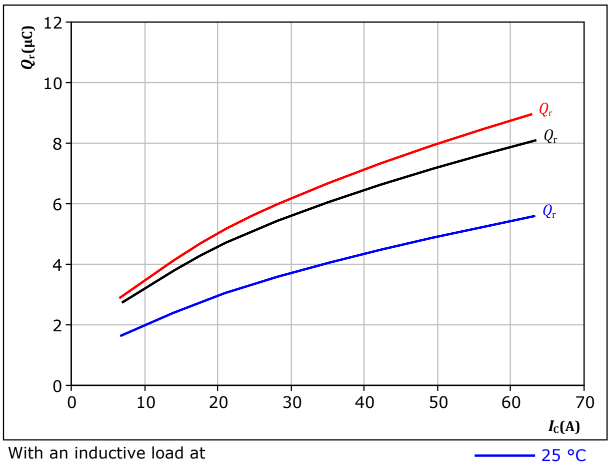
T_j: 25 °C / 125 °C / 150 °C

Inverter Switching Characteristics

figure 26. FWD

Typical recovered charge as a function of collector current

Q_r = f(I_C)



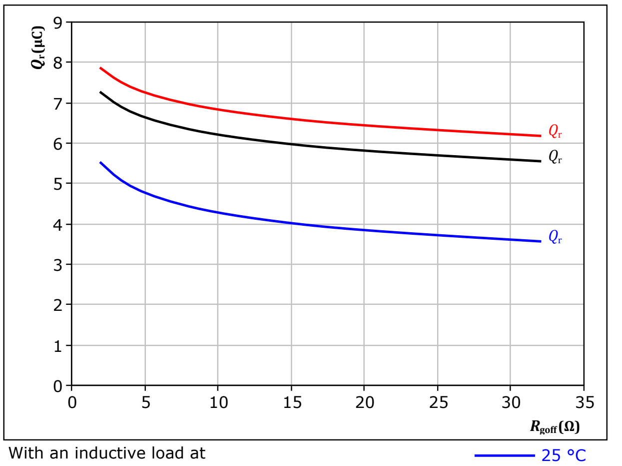
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 27. FWD

Typical recovered charge as a function of turn off gate resistor

Q_r = f(R_{goff})



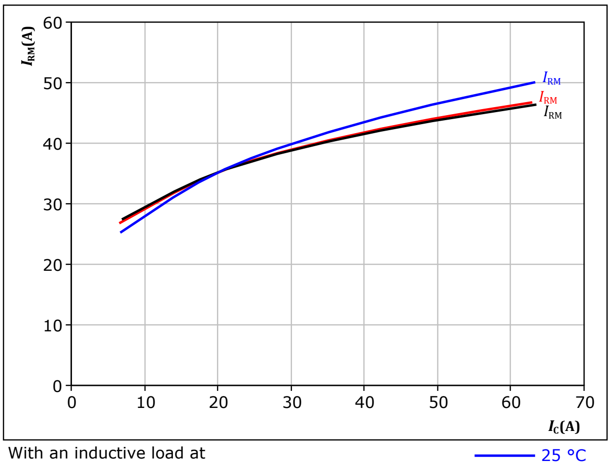
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	35	A		150 °C

figure 28. FWD

Typical peak reverse recovery current as a function of collector current

I_{RM} = f(I_C)



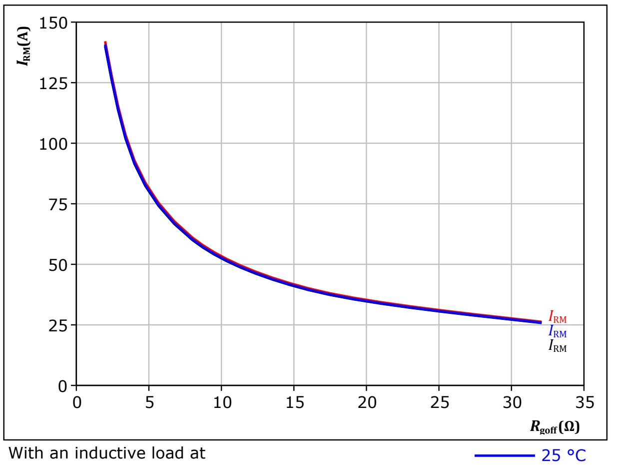
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 29. FWD

Typical peak reverse recovery current as a function of turn off gate resistor

I_{RM} = f(R_{goff})



With an inductive load at

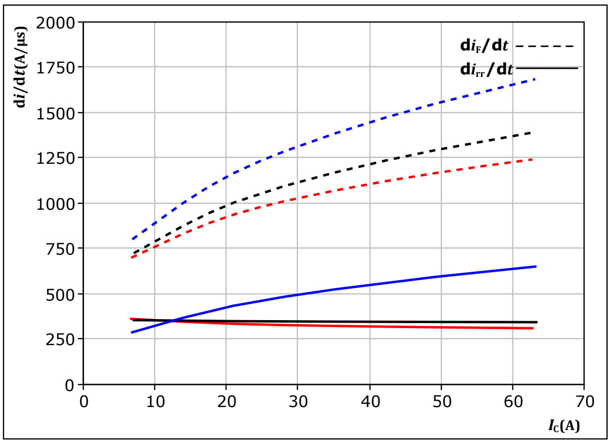
V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	35	A		150 °C

Inverter Switching Characteristics

figure 30. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



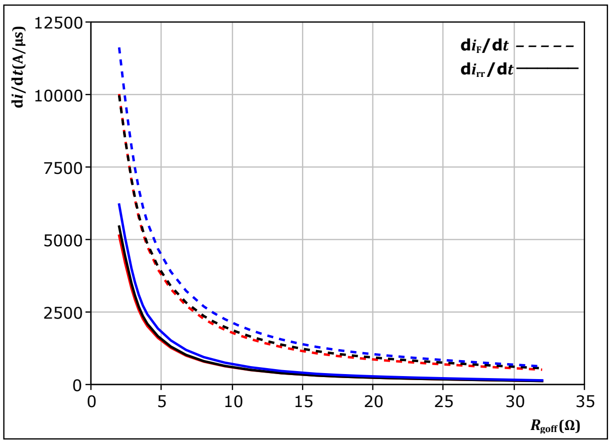
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
R_{gon} =	16	Ω		150 °C

figure 31. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor

di_F/dt, di_{rr}/dt = f(R_{goff})



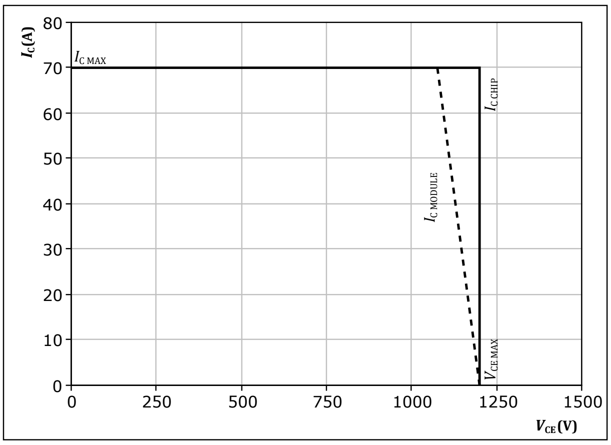
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	±15	V		125 °C
I_C =	35	A		150 °C

figure 32. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



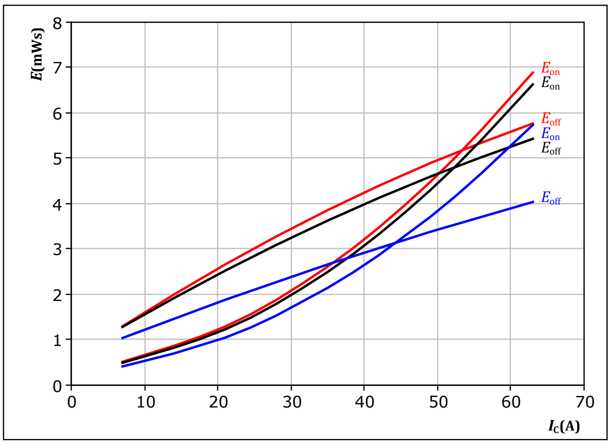
At	T_j =	150	°C
	R_{gon} =	16	Ω
	R_{goff} =	16	Ω

Brake Switching Characteristics

figure 33.	**IGBT**

Typical switching energy losses as a function of collector current

E = f(I_C)



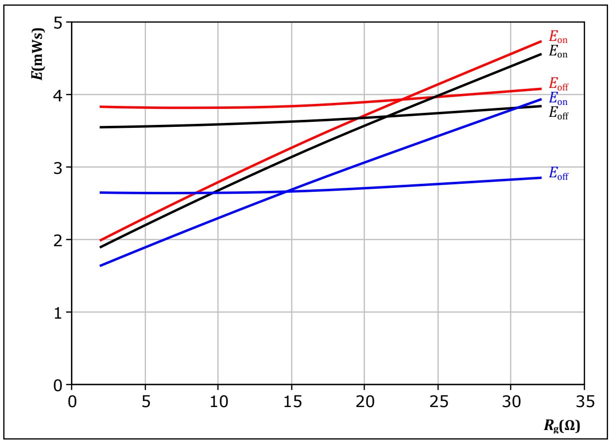
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
R_{gon} =	8	Ω		150 °C
R_{goff} =	8	Ω		

figure 34.	**IGBT**

Typical switching energy losses as a function of gate resistor

E = f(R_g)



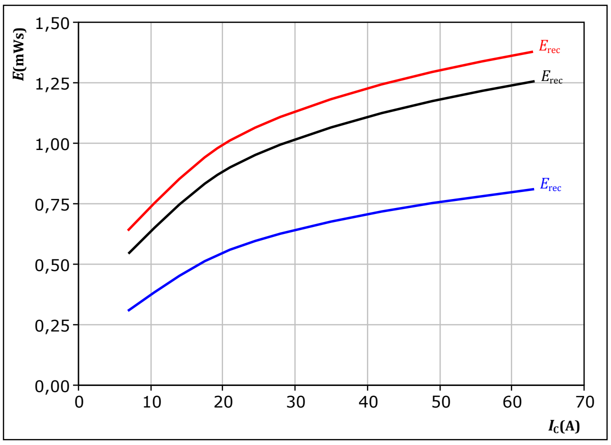
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
I_C =	35	A		150 °C

figure 35.	**FWD**

Typical reverse recovered energy loss as a function of collector current

E_{rec} = f(I_C)



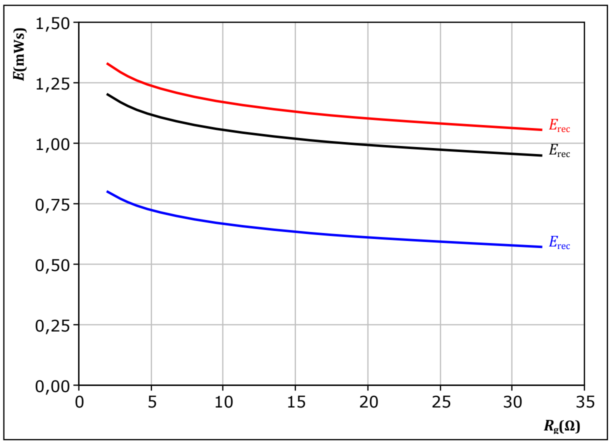
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 36.	**FWD**

Typical reverse recovered energy loss as a function of gate resistor

E_{rec} = f(R_g)



With an inductive load at

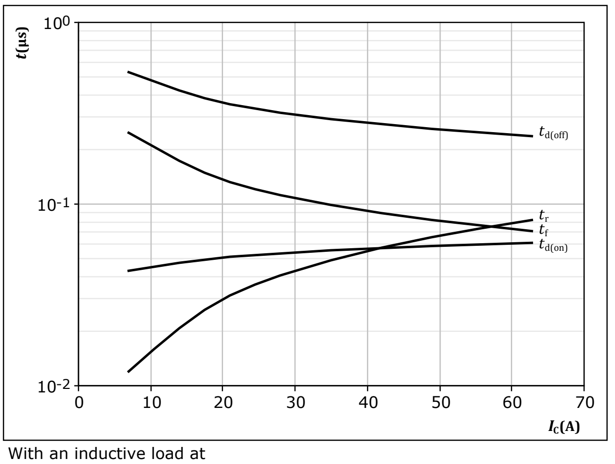
V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
I_C =	35	A		150 °C

Brake Switching Characteristics

figure 37. **IGBT**

Typical switching times as a function of collector current

$t = f(I_C)$



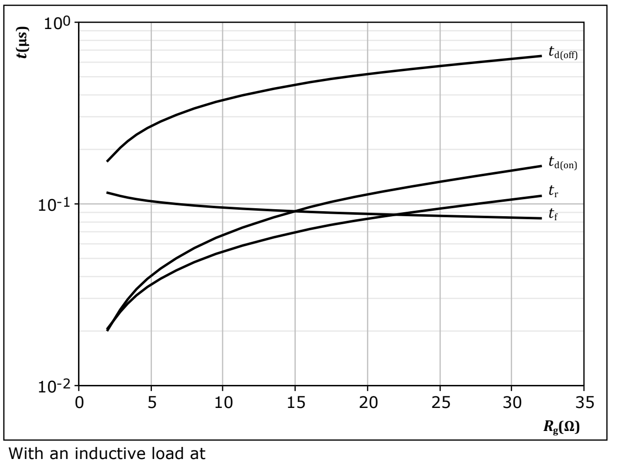
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	0/15	V
R_{gon} =	8	Ω
R_{goff} =	8	Ω

figure 38. **IGBT**

Typical switching times as a function of gate resistor

$t = f(R_g)$



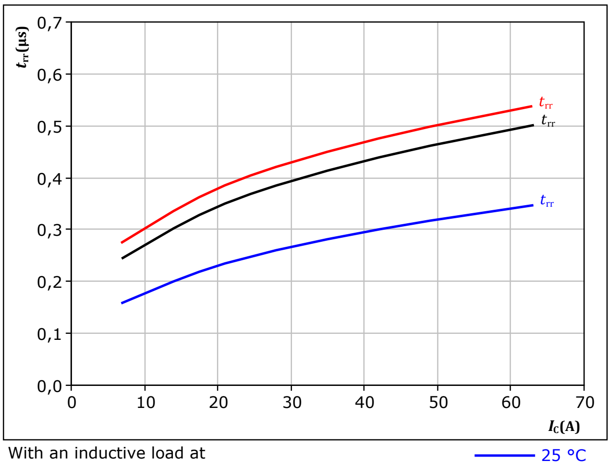
With an inductive load at

T_j =	150	°C
V_{CE} =	600	V
V_{GE} =	0/15	V
I_C =	35	A

figure 39. **FWD**

Typical reverse recovery time as a function of collector current

$t_{rr} = f(I_C)$



With an inductive load at

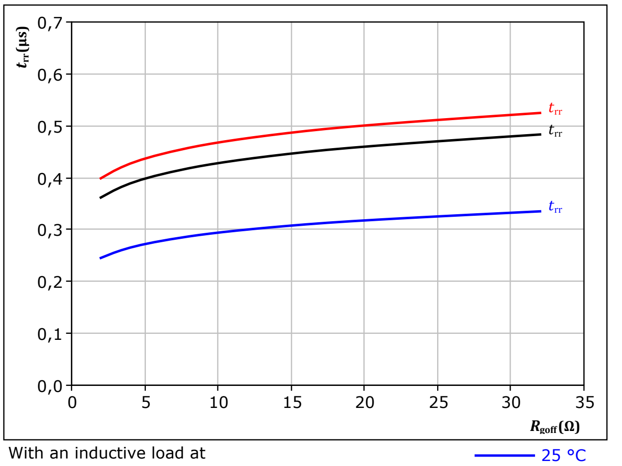
V_{CE} =	600	V
V_{GE} =	0/15	V
R_{gon} =	8	Ω

T_j: 25 °C, 125 °C, 150 °C

figure 40. **FWD**

Typical reverse recovery time as a function of IGBT turn off gate resistor

$t_{rr} = f(R_{goff})$



With an inductive load at

V_{CE} =	600	V
V_{GE} =	0/15	V
I_C =	35	A

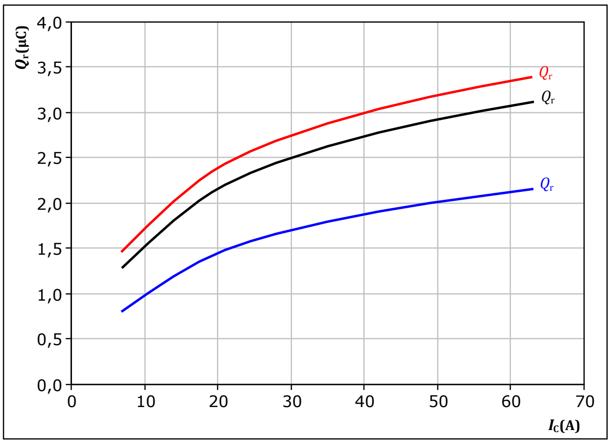
T_j: 25 °C, 125 °C, 150 °C

Brake Switching Characteristics

figure 41. FWD

Typical recovered charge as a function of collector current

$Q_r = f(I_C)$



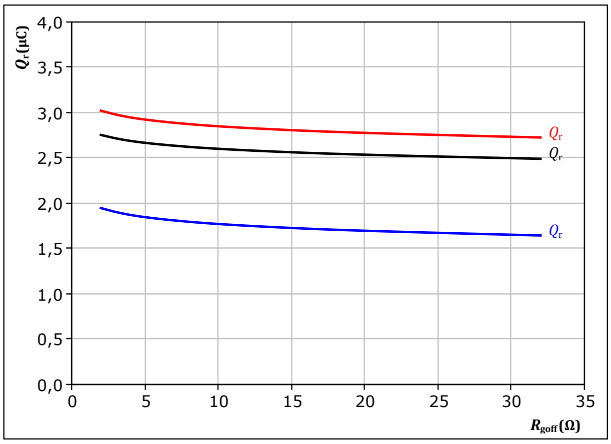
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C (blue)
V_{GE} =	0/15	V		125 °C (black)
R_{gon} =	8	Ω		150 °C (red)

figure 42. FWD

Typical recovered charge as a function of turn off gate resistor

$Q_r = f(R_{goff})$



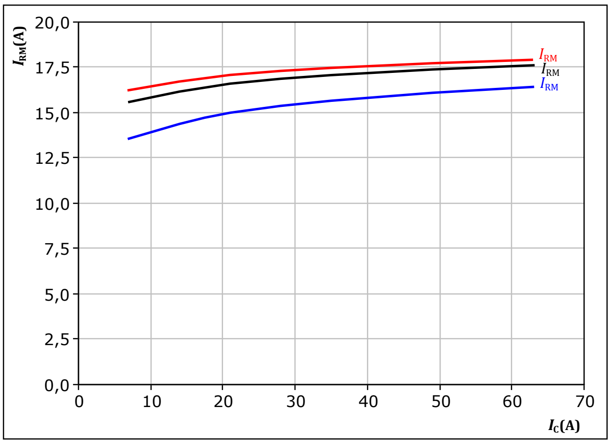
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C (blue)
V_{GE} =	0/15	V		125 °C (black)
I_C =	35	A		150 °C (red)

figure 43. FWD

Typical peak reverse recovery current as a function of collector current

$I_{RM} = f(I_C)$



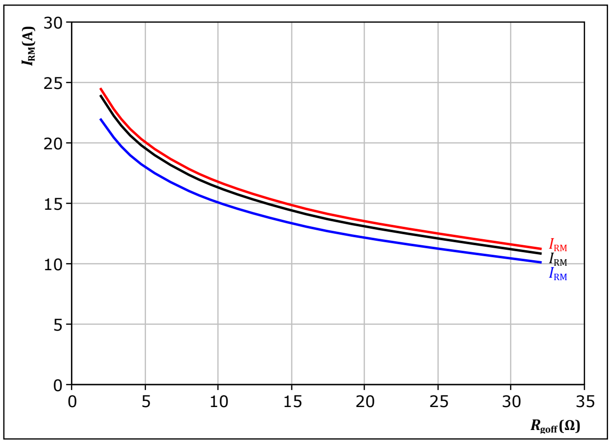
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C (blue)
V_{GE} =	0/15	V		125 °C (black)
R_{gon} =	8	Ω		150 °C (red)

figure 44. FWD

Typical peak reverse recovery current as a function of turn off gate resistor

$I_{RM} = f(R_{goff})$



With an inductive load at

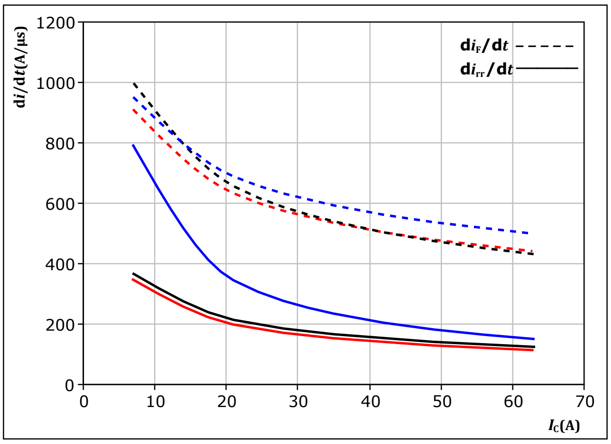
V_{CE} =	600	V	T_j:	25 °C (blue)
V_{GE} =	0/15	V		125 °C (black)
I_C =	35	A		150 °C (red)

Brake Switching Characteristics

figure 45. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of collector current

di_F/dt, di_{rr}/dt = f(I_C)



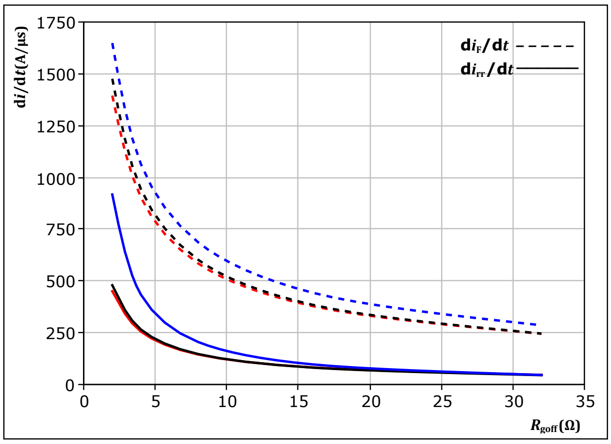
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
R_{gon} =	8	Ω		150 °C

figure 46. **FWD**

Typical rate of fall of forward and reverse recovery current as a function of turn off gate resistor

di_F/dt, di_{rr}/dt = f(R_{goff})



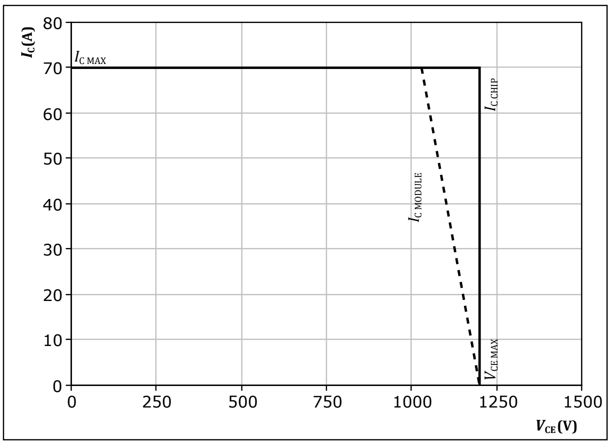
With an inductive load at

V_{CE} =	600	V	T_j:	25 °C
V_{GE} =	0/15	V		125 °C
I_C =	35	A		150 °C

figure 47. **IGBT**

Reverse bias safe operating area

I_C = f(V_{CE})



At	T_j =	150	°C
	R_{gon} =	8	Ω
	R_{goff} =	8	Ω

Switching Definitions

figure 48. **IGBT**

Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff} (t_{tEoff} **= integrating time for** E_{off})

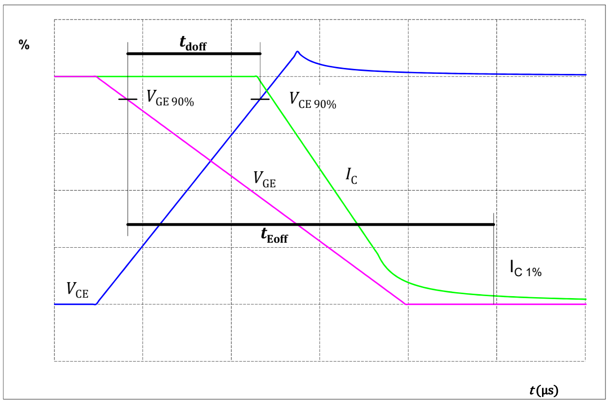


figure 49. **IGBT**

Turn-on Switching Waveforms & definition of t_{don}, t_{Eon} (t_{Eon} **= integrating time for** E_{on})

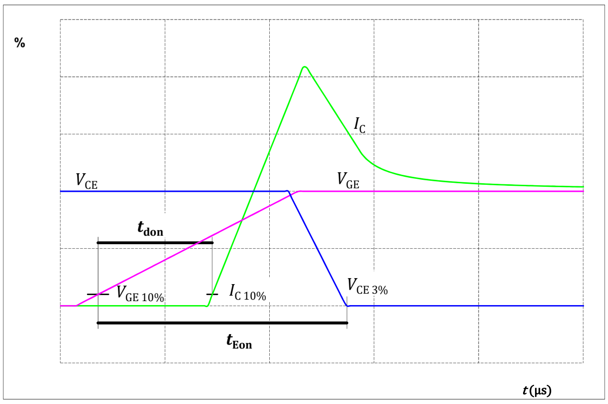


figure 50. **IGBT**

Turn-off Switching Waveforms & definition of t_f

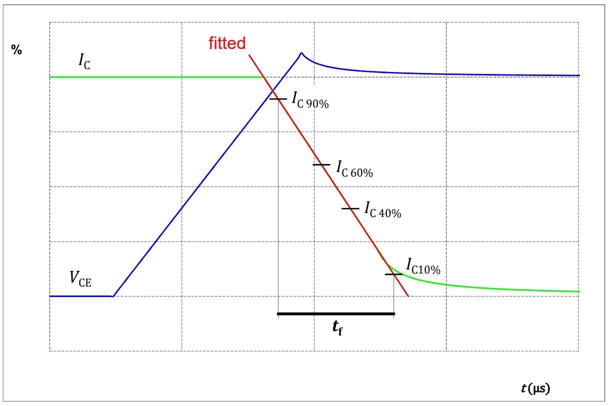
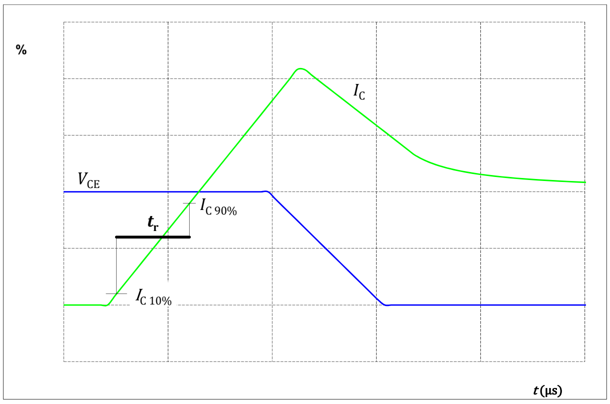


figure 51. **IGBT**

Turn-on Switching Waveforms & definition of t_r



Switching Definitions

figure 52. FWD

Turn-off Switching Waveforms & definition of t_{rr}

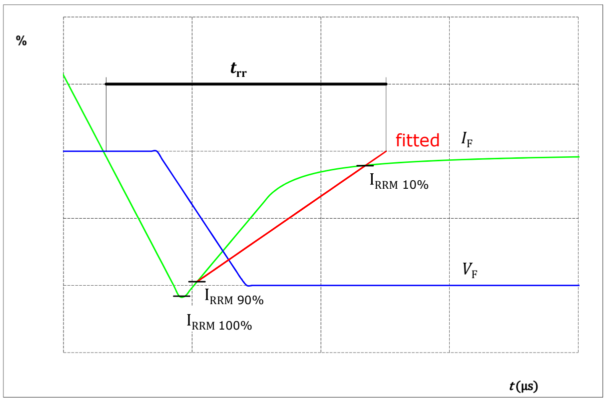
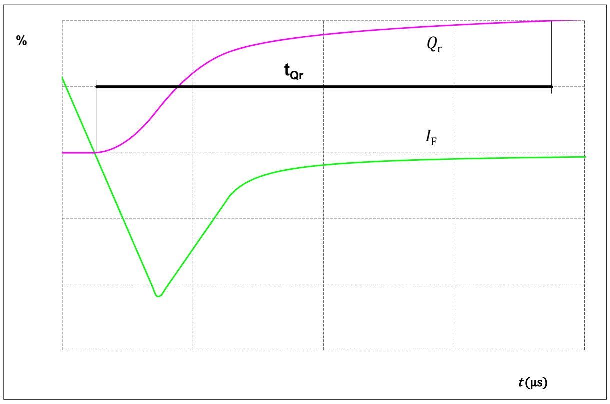


figure 53. FWD

Turn-on Switching Waveforms & definition of t_{Qr} (t_{Qr} = integrating time for Q_r)



Ordering Code

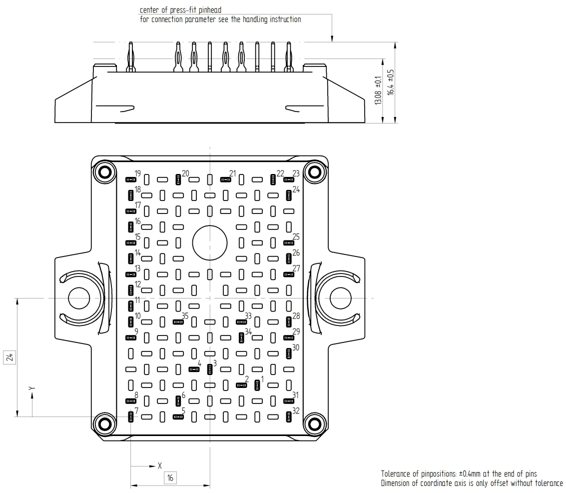
Version	Ordering Code
Without thermal paste	10-EY12PMA035M7-L188A78T
With thermal paste (3,4 W/mK, PSX-P7)	10-EY12PMA035M7-L188A78T-/3/

Marking

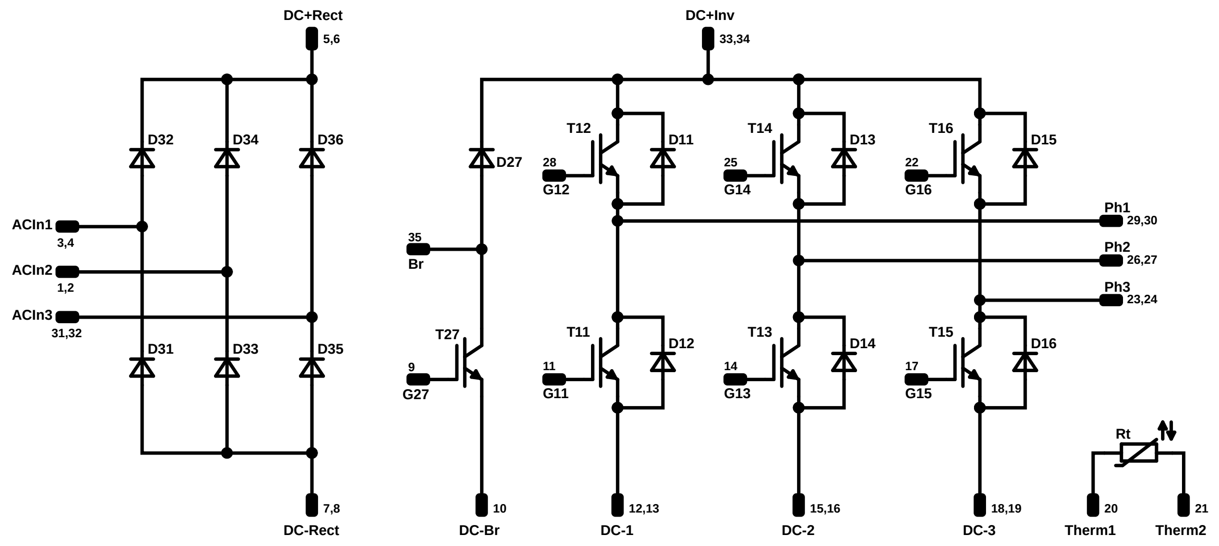
	Name		Date code	UL & VIN	Lot	Serial
Text	NN-NNNNNNNNNNNNNN-TTTTTTTVV		WWYY	UL VIN	LLLLL	SSSS
	Type&Ver	Lot number	Serial	Date code		
Datamatrix	TTTTTTTVV	LLLLL	SSSS	WWYY		

Outline

Pin table [mm]			
Pin	X	Y	Function
1	25,6	6,4	ACIn2
2	22,4	6,4	ACIn2
3	16	9,6	ACIn1
4	12,8	9,6	ACIn1
5	9,6	0	DC+Rect
6	9,6	3,2	DC+Rect
7	0	0	DC-Rect
8	0	3,2	DC-Rect
9	0	16	G27
10	0	19,2	DC-Br
11	0	22,4	G11
12	0	25,6	DC-1
13	0	28,8	DC-1
14	0	32	G13
15	0	35,2	DC-2
16	0	38,4	DC-2
17	0	41,6	G15
18	0	44,8	DC-3
19	0	48	DC-3
20	9,6	48	Therm1
21	19,2	48	Therm2
22	28,8	48	G16
23	32	48	Ph3
24	32	44,8	Ph3
25	32	35,2	G14
26	32	32	Ph2
27	32	28,8	Ph2
28	32	19,2	G12
29	32	16	Ph1
30	32	12,8	Ph1
31	32	3,2	ACIn3
32	32	0	ACIn3
33	22,4	19,2	DC+Inv
34	22,4	16	DC+Inv
35	9,6	19,2	Br



Pinout



Identification

ID	Component	Voltage	Current	Function	Comment
T11, T12, T13, T14, T15, T16	IGBT	1200 V	35 A	Inverter Switch	
D11, D12, D13, D14, D15, D16	FWD	1200 V	35 A	Inverter Diode	
T27	IGBT	1200 V	35 A	Brake Switch	
D27	FWD	1200 V	10 A	Brake Diode	
D31, D32, D33, D34, D35, D36	Rectifier	1600 V	35 A	Rectifier Diode	
Rt	NTC			Thermistor	

Packaging instruction				
Standard packaging quantity (SPQ) 100	>SPQ	Standard	<SPQ	Sample

Handling instruction
Handling instructions for *flow* E2 packages see vincotech.com website.

Package data
Package data for *flow* E2 packages see vincotech.com website.

Vincotech thermistor reference
See Vincotech thermistor reference table at vincotech.com website.

UL recognition and file number
This device is certified according to UL 1557 standard, UL file number E192116. For more information see vincotech.com website.

Document No.:	Date:	Modification:	Pages
10-EY12PMA035M7-L188A78T-D4-14	27 Jul. 2021	New datasheet format, separate solder and pressfit pin variant Update characteristics of rectifier diode, leakage current max value from 50 -> 100 uA Update Brake Switching Characteristics No change in the module	